

A

A

B

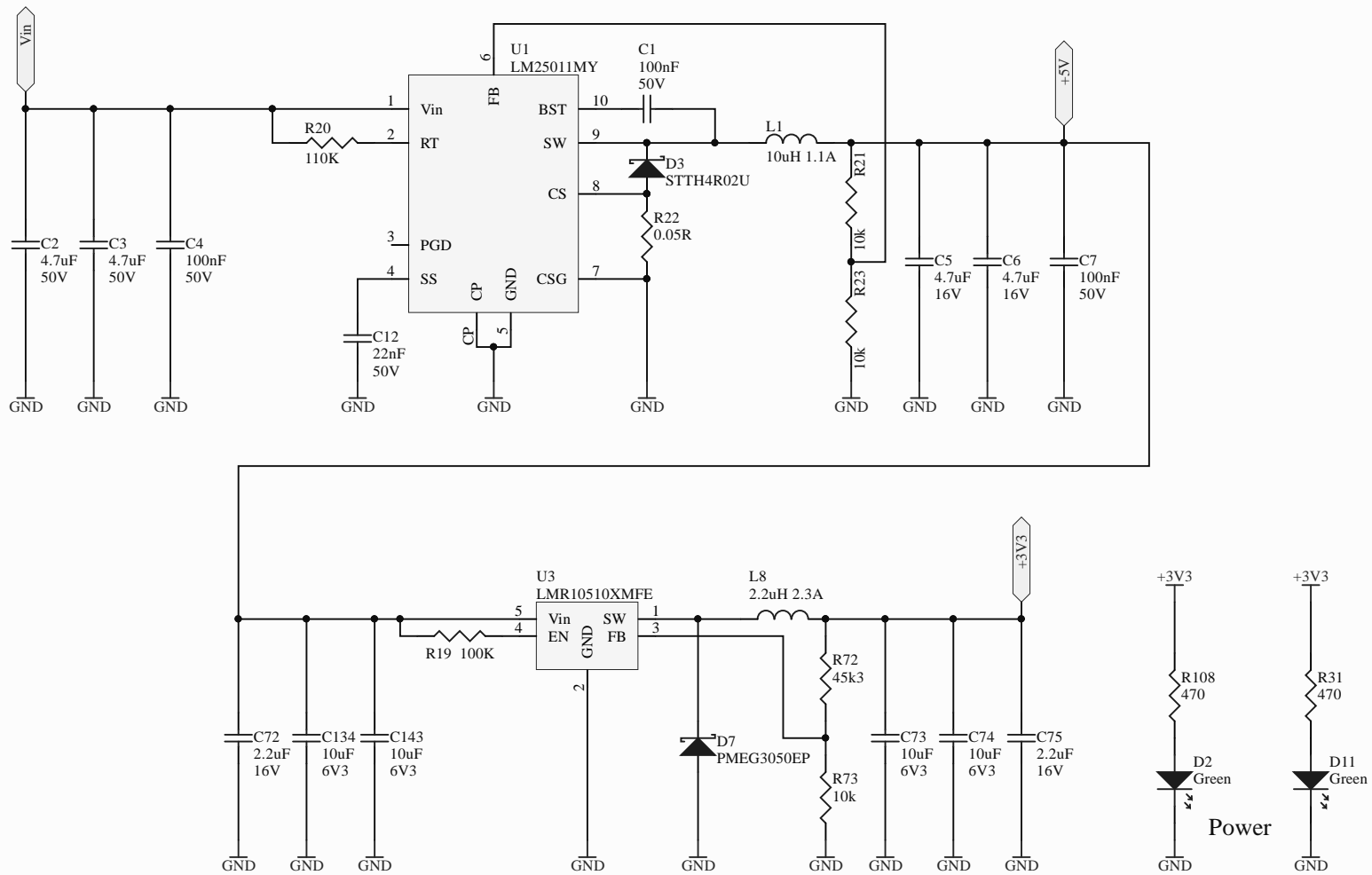
B

C

C

D

D



Title		
Size	Number	Revision
A4		
Date:	23-Nov-16	Sheet of
File:	C:\Projecten\...\DB10012_ControlPower	Drawn By:

A

B

C

D

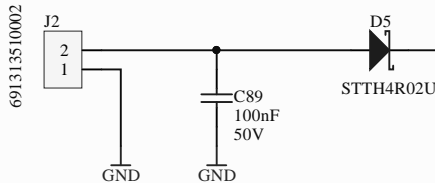
A

B

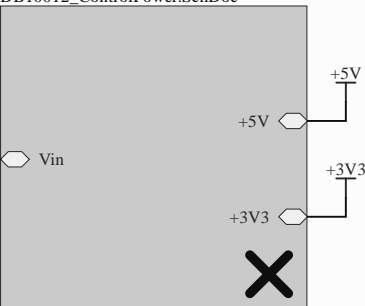
C

D

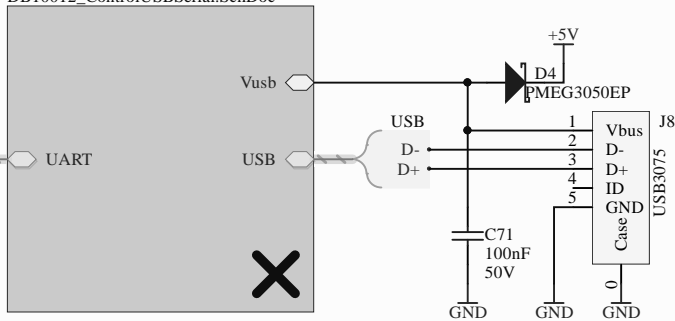
Power Input



Power  
DB10012\_ControlPower.SchDoc

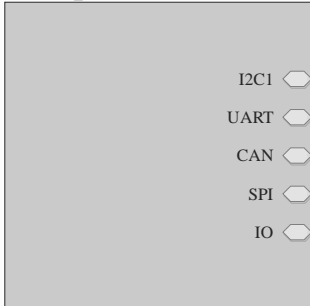


USB  
DB10012\_ControlUSBSerial.SchDoc

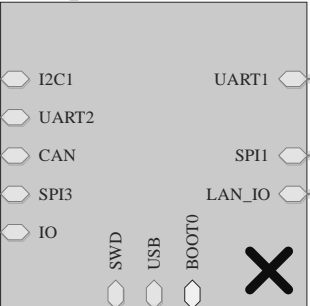


USB Boot

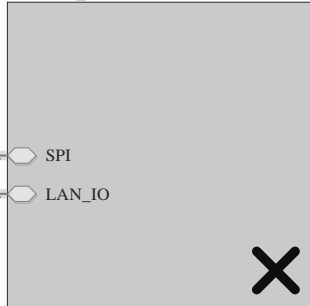
IO Connector  
DB10012\_ControlIOConnector.SchDoc



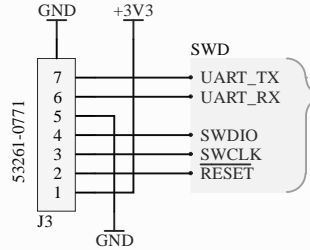
Control  
DB10012\_ControlController.SchDoc



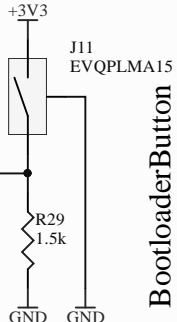
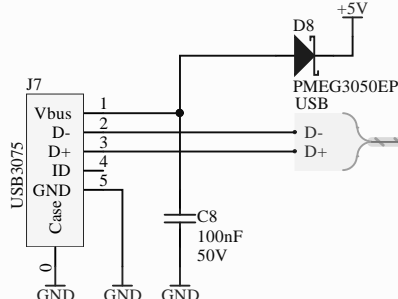
EtherCAT  
DB10012\_ControlEtherCAT.SchDoc



Debug + SWD

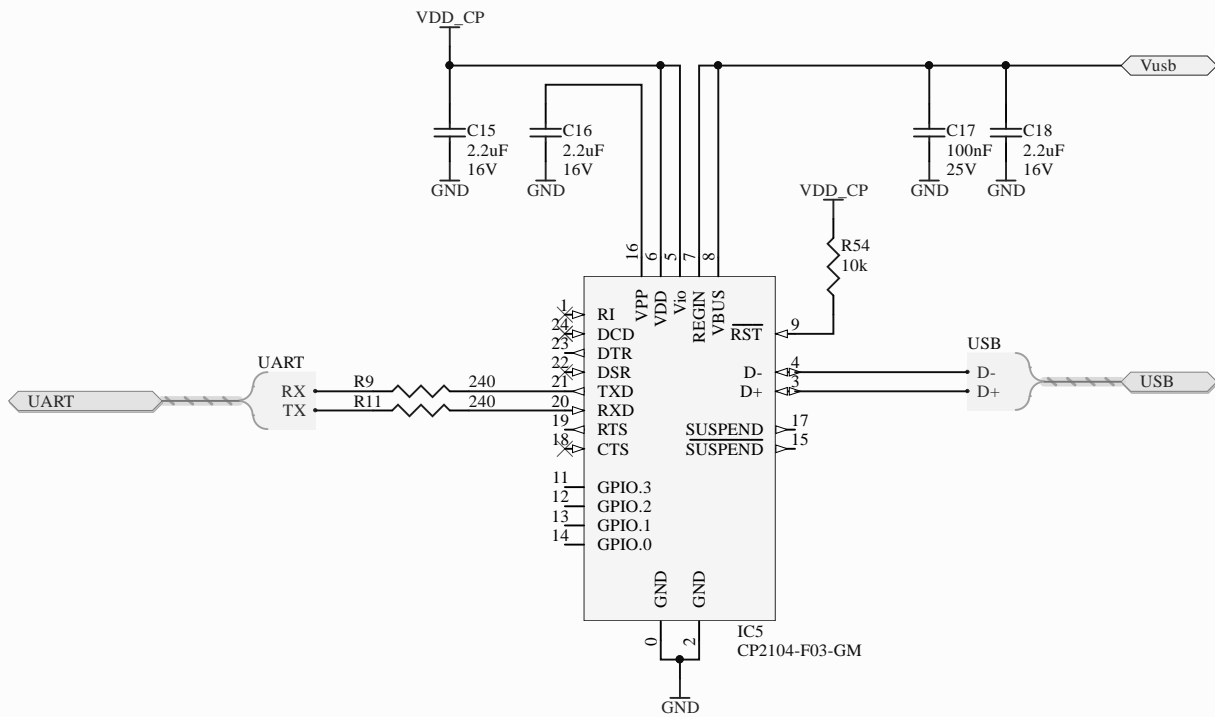


USB uC



BootloaderButton

Title		
Size	Number	Revision
A4		
Date:	23-Nov-16	Sheet of
File:	C:\Projecten\...\DB10012_ControlTopLevel.SchDoc	Drawn By:



Title		
Size	Number	Revision
A4		
Date:	23-Nov-16	Sheet of
File:	C:\Projecten\...\DB10012_ControlUSBSerialSch.Dwg	1

BOOT0

STATUS  
RUN  
POWER

DieBieSlave ←  
HW: V0.1 →

SOD-128

