N300 Quick-Start

N300 product has the complete set of documents, which are listed at below table, to help user quickly familiarize it.

ID	Doc Name	Descriptions
0	<Nuclei_RISCV_ISA_Spec.pdf> <Nuclei_TEE_Architecture.pdf> <Nuclei_NICE_Extension.pdf> <Nuclei_DSP_QuickStart.pdf> <RISC-V_P_Extension_Proposal_v0.5-draft-20190426.pdf>	These documents systematically introduce the Nuclei RISC-V architecture specification, including: ● Instruction set, ● Privileged architecture, ● CSR registers, ● Low-power architecture scheme, ● And the interrupts, etc.
1	<Nuclei_N300_Integration_Guide.pdf>	This doc introduce: ● How to configure the IP options and generate the Verilog RTL codes and view it. ● How to run the basic CCT (Customer Confident Tests) with the attached basic testbench environment. ● How to integrate the CPU core into the customer SoC. ● How to generate FPGA bitstream for SoC prototype (including RISC-V Core) . ● How to run the synthesis. ● Other overall introductions, etc.
2	<Nuclei_N300_Databook.pdf>	This doc as Databook introduces the IP's: ● Features, ● Components, ● Interfaces, ● The configuration options, ● Other overall introductions, etc.
3	<Nuclei_Eval_SoC_Intro.pdf>	This document introduce the prototype SoC for evaluation purpose, this prototype SoC included bus, common peripherals, etc.
4	Nuclei_SDK Website (https://doc.nucleisys.com/nuclei_sdk/)	The quick-start manual for user to start the embedded software development with Nuclei SDK.
5	<Nuclei_SES_IDE_QuickStart.pdf>	The quick-start manual for user to start the embedded software development with Windows IDE (Integrated Development Environment): Segger Embedded Studio.
6	RISC-V MCU Website (Chinese Version Only) (https://www.rvmcu.com)	The webpage as the embedded RISC-V MCU ecosystem website.
7	Nuclei Board Labs Website (English Version Only) (https://doc.nucleisys.com/nuclei_board_labs/)	The webpage as the embedded software labs ecosystem website, for Nuclei MCU development board.