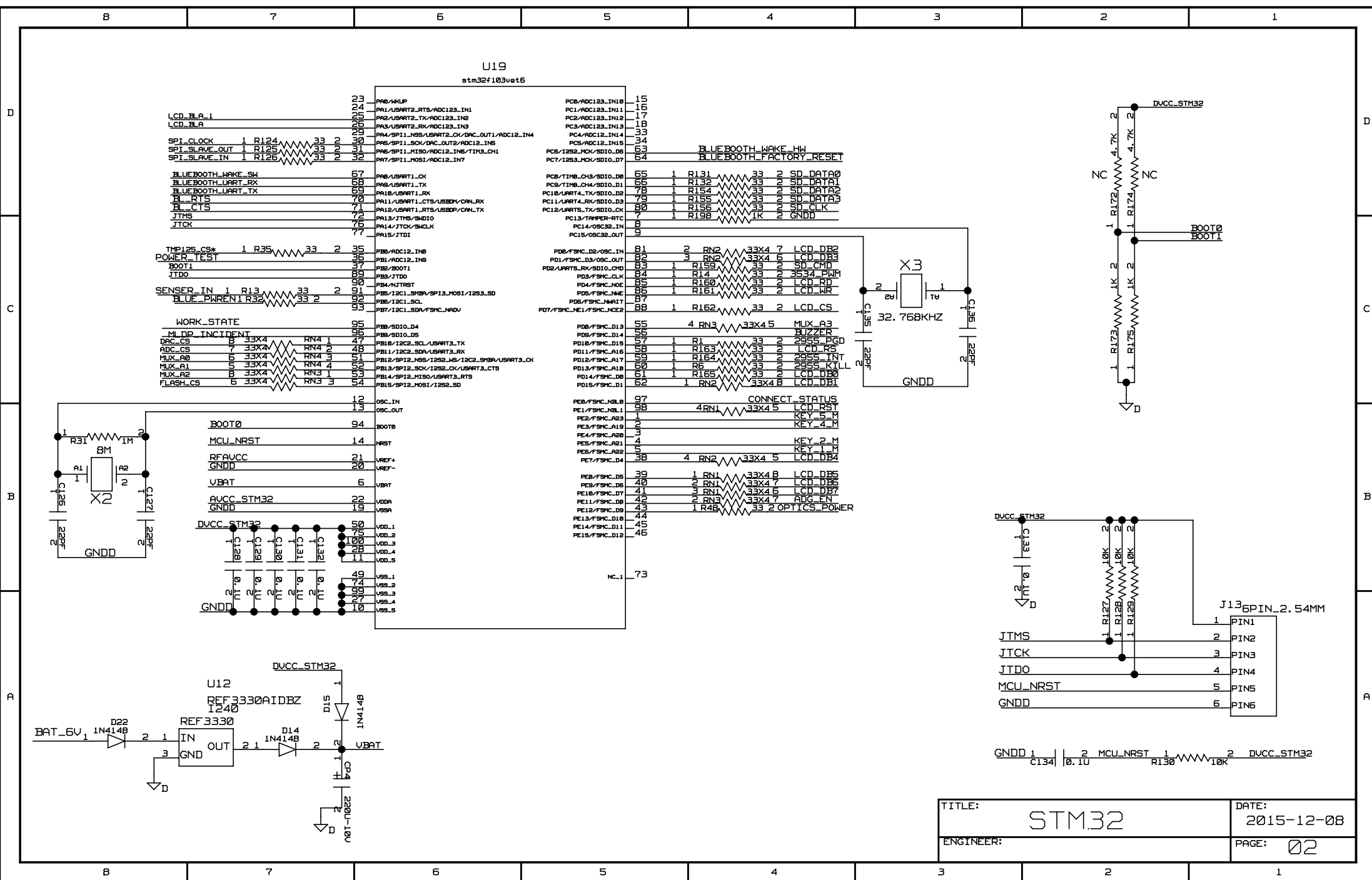
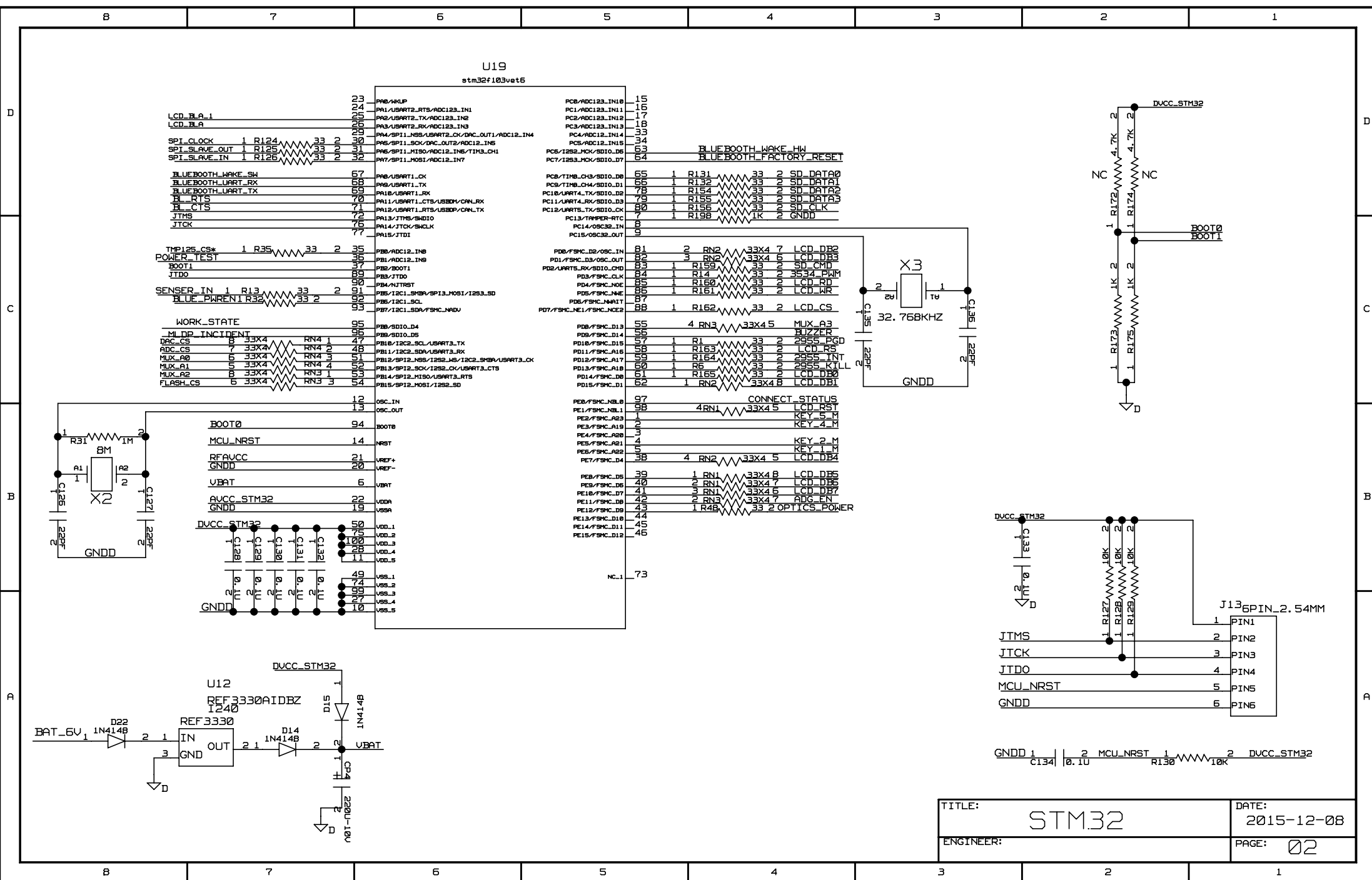


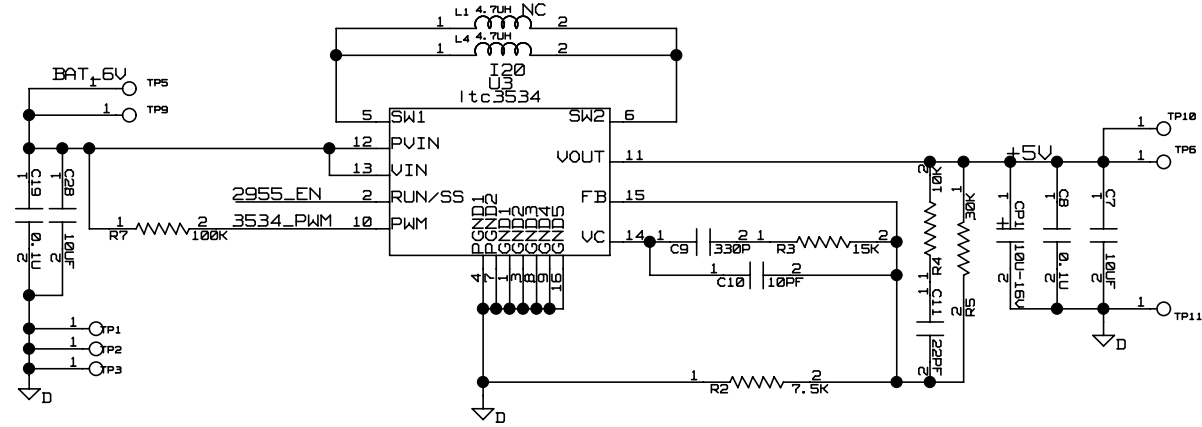
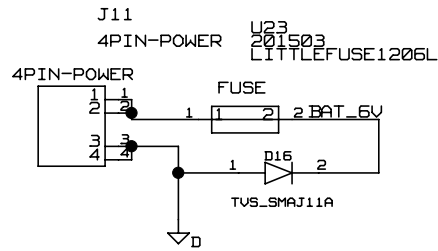
SP910

SHEET #	DESCRIPTION
01	COVER PAGE
02	STM32
03	STM32 PERIPHERAL
04	POWER/POWER SWITCH
05	OPTICAL POWER
06	DAC MODULE
07	ADC MODULE
08	ADC MODULE
09	TF&BLUETOOTH&USB
10	KEYBOARD

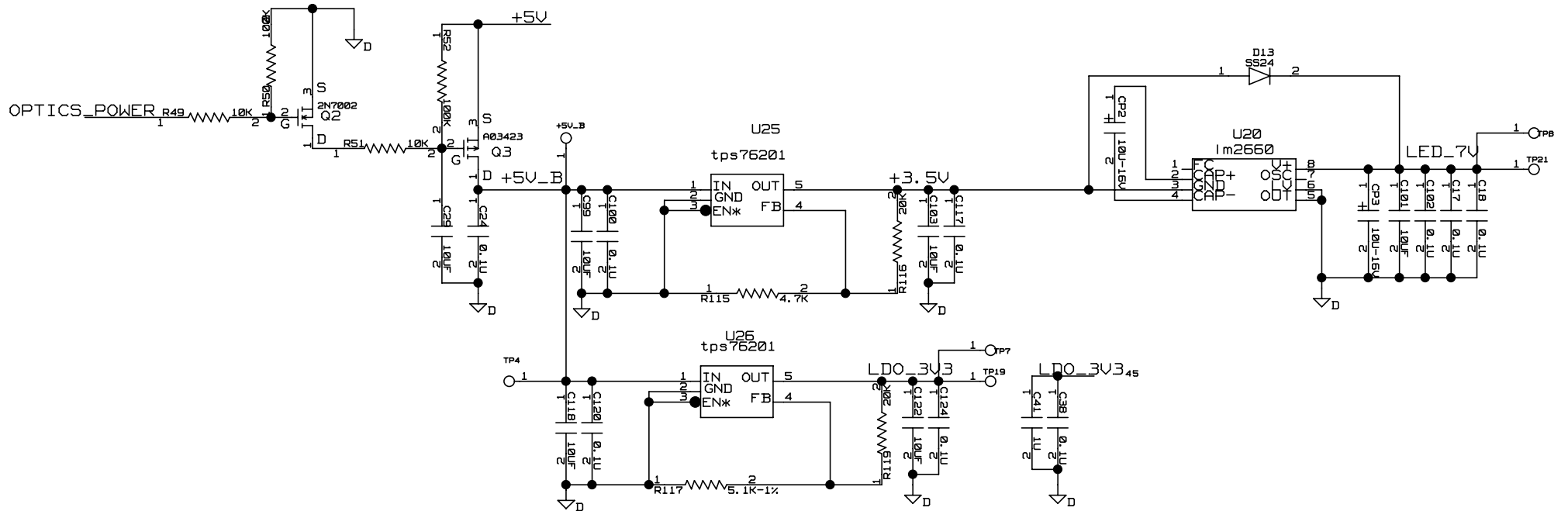
PYXIS LAB INC

TITLE: COVER PAGE	DATE: 2015-12-08
ENGINEER: CHEN PENG	PAGE: 01

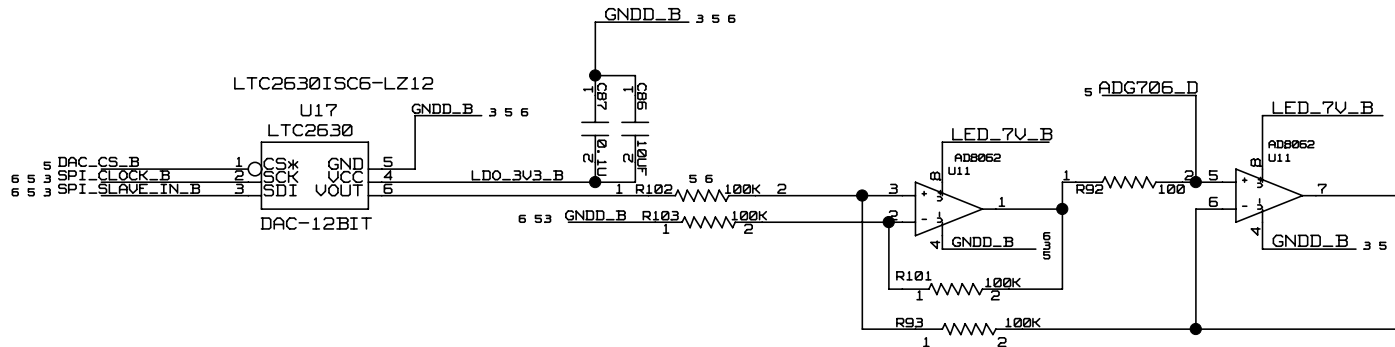
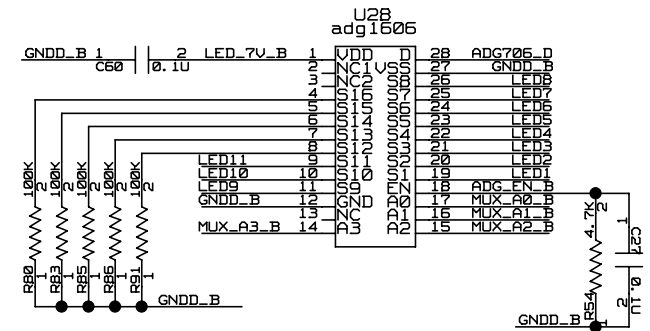
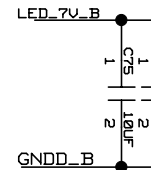
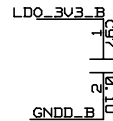
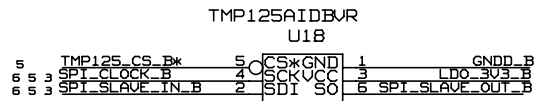




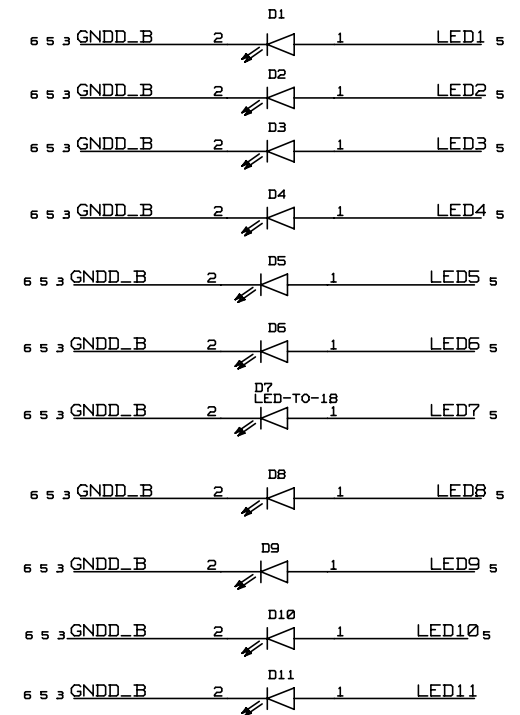
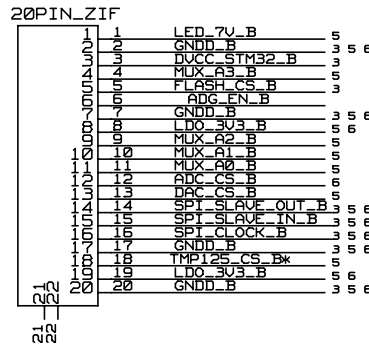
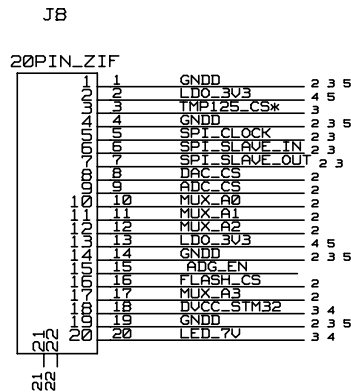
OPTICS BOARD POWER CONTROL



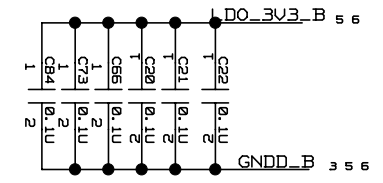
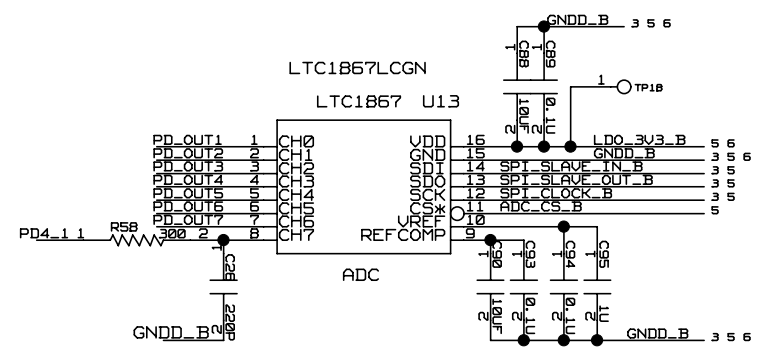
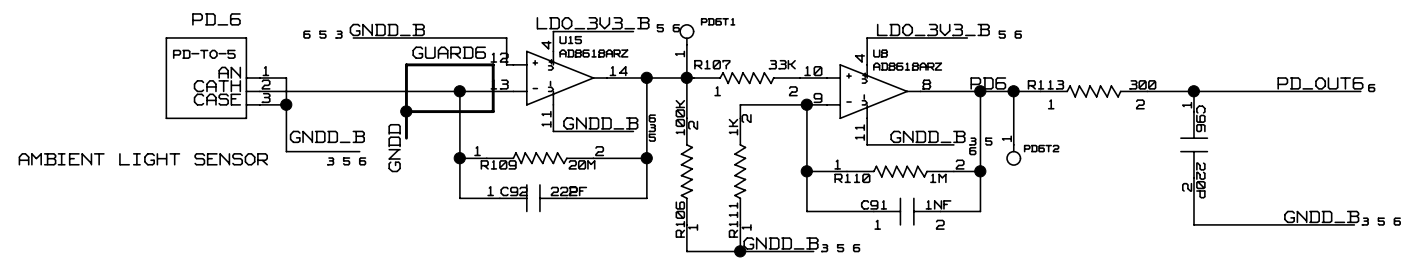
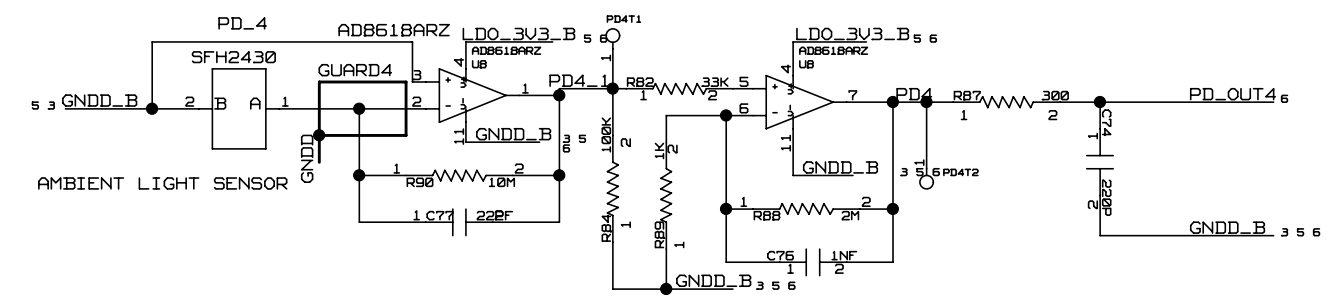
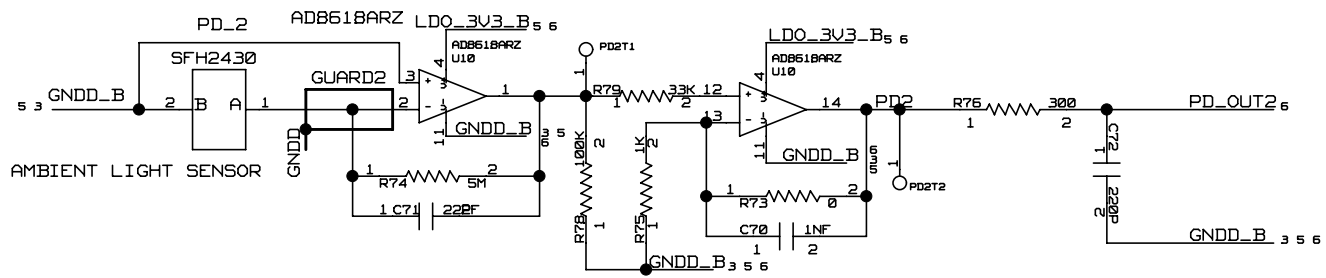
TITLE: OPTICS_POWER	DATE: 2015-12-08
ENGINEER:	PAGE: 05



NOTED:
R92, R93, R101, R102, R103
RESISTANCE TOLERANCE: 5%
TEMPERATURE COEFFICIENT: BELOW 100PPM

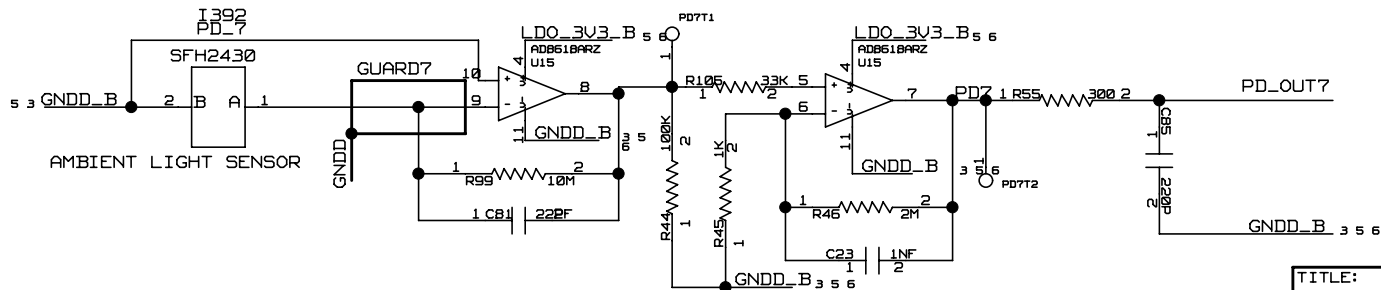
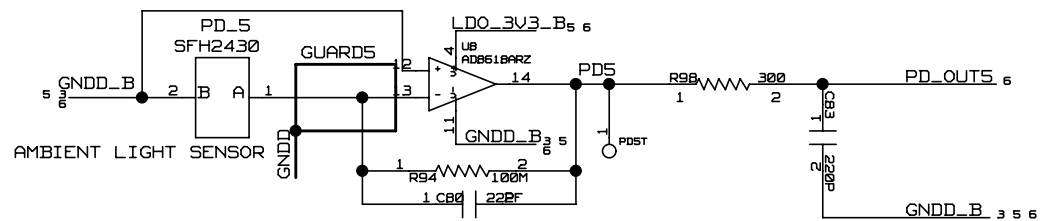
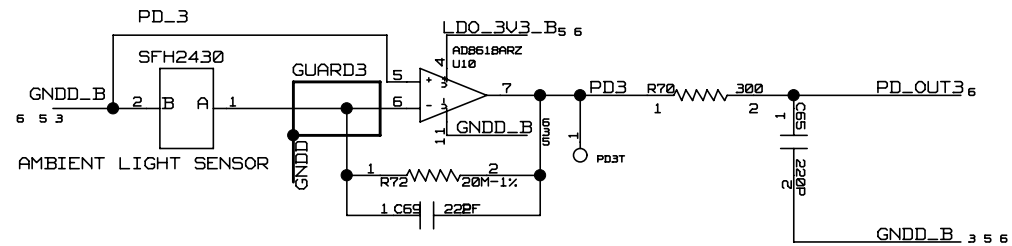
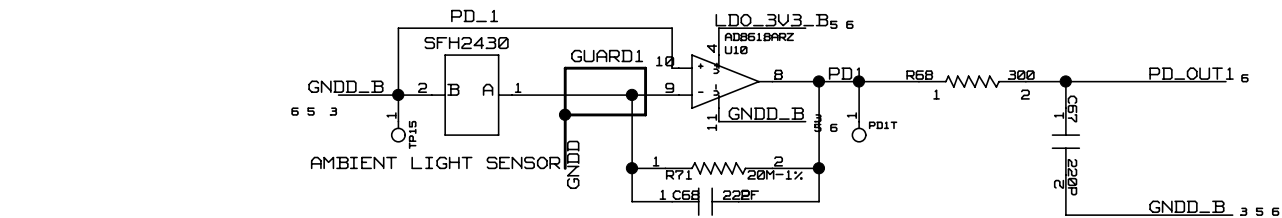


TITLE:	DAC MODULE	DATE:	2015-12-08
ENGINEER:		PAGE:	06



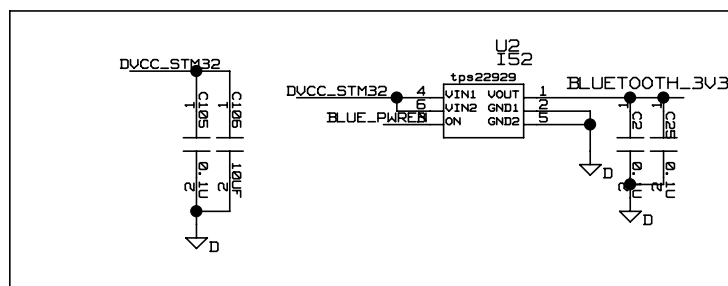
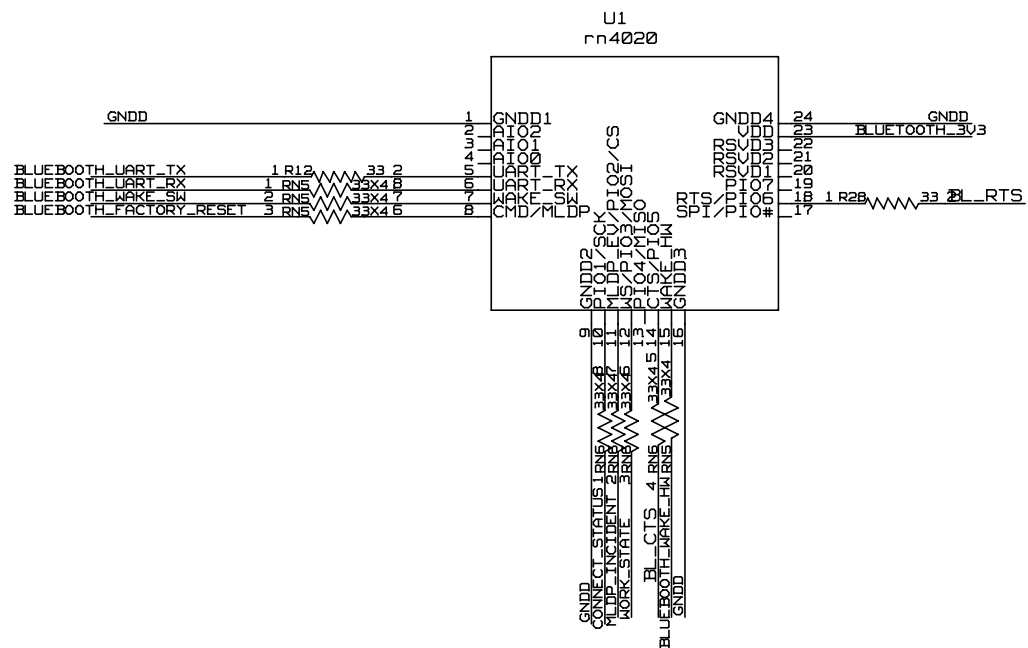
NOTED:
R73, R74, R75,
R88, R89, R90,
R109, R110, R111,
RESISTANCE TOLERANCE: 5%
TEMPERATURE COEFFICIENT: BELOW 100PPM

TITLE: ADC MODULE	DATE: 2015-12-08
ENGINEER:	PAGE: 07



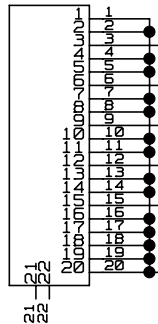
NOTED:
R71, R72, R94, R99, R45, R46
RESISTANCE TOLERANCE: 5%
TEMPERATURE COEFFICIENT: BELOW 100PPM

TITLE:	ADC MODULE	DATE:	2015-12-08
ENGINEER:		PAGE:	08

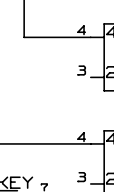
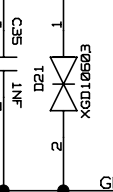
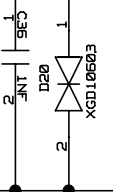
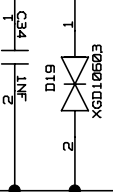
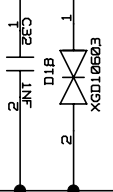
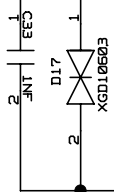


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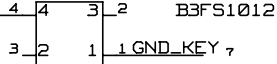
J12
20PIN_ZIF



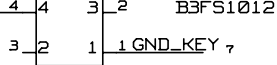
KEY_1
KEY_2
KEY_3
KEY_4
KEY_5
KEY_6
KEY_7
KEY_8
KEY_9
GND_KEY_7



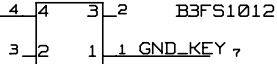
J3



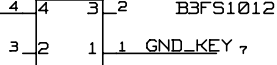
J2



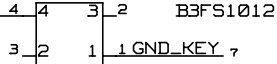
J4



J6



J5



TITLE:

KEYBOARD

DATE:

2015-12-08

ENGINEER:

PAGE:

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