LIBMSR Documentation

Libmsr Version 0.2.1

Welcome to the LIBMSR Documentation. Libmsr is an API designed to simplify the use of model specific registers, and now PCI configuration registers (CSRs), on Intel platforms. No longer do you have to read thousand page Intel documents which change for each architecture. Libmsr provides a simple interface through simple function calls and data structures. It will also figure out what registers are available for target architecture. Finally, optimizations greatly improve performance up to 8x on average. We recommend using Libmsr with MSR-SAFE and CSR-SAFE to prevent system instability.

LIBMSR is updated frequently, be sure to check the repository from time to time. We highly recommend that you use the latest version of LIBMSR. We have fixed many bugs that give corrupted or unreliable data.

If you find any bugs please report them to Scott Walker (see contacts info below).

Contacts

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New Features

- There are so many! See changelog.txt for more details.
- Highlights include: batch optimizations, PCI Configuration Register (CSR) support, tons of new counters, pain free build system

Known Bugs

- None at the moment, please report any to walker91@llnl.gov

Fixed Bugs

- Lots of these too! See changelog.txt for more details.

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