```
Date: January 16, 2019
                                                   display.sv
        module display (clk, z, HEX0, HEX1, HEX2, HEX3, HEX4, HEX5);
    1
           output logic [6:0] HEXO, HEX1, HEX2, HEX3, HEX4, HEX5;
input logic [4:0] z;
    3
    4
    5
           input logic clk;
    6
   7
           logic [6:0] E, M, P, t, y, F, U, L, one, two, three, four, five, six, seven, eight, nine,
         zero, n;
   8
    9
           assign E = 7'b0000110;
           assign M = 7'b0010101;
   10
           assign P = 7'b0001100:
   11
           assign t = 7'b0000111
   12
           assign y = 7'b0010001;
   13
           assign F = 7'b0001110;
   14
   15
           assign U = 7'b1000001;
           assign L = 7'b1000111
   16
           assign one = \sim 7'b0000110;
assign two = \sim 7'b1011011;
   17
   18
           assign three = \sim7'b1001111:
   19
           assign four = \sim 7'b1100110:
   20
           assign five = \sim7'b1101101;
   21
   22
                        = \sim 7 b11111101
           assign six
   23
           assign seven = \sim7'b0000111;
           assign eight = \sim7'b1111111;
   24
   25
           assign nine = ~7'b1101111;
           assign zero = ~7'b0111111;
   26
           assign n = 7'b111111111;
   27
   28
           always_comb begin
   29
   30
               case (z)
                  5'b00000: begin
   31
   32
                     HEX5 = E; HEX4 = M; HEX3 = P; HEX2 = t; HEX1 = y; HEX0 = zero;
   33
                  5'b00001: begin
   35
                     HEX5 = n; HEX4 = n; HEX3 = n; HEX2 = n; HEX1 = zero; HEX0 = one;
   36
                  end
   37
                  5'b00010: begin
   38
                     HEX5 = n; HEX4 = n; HEX3 = n; HEX2 = n; HEX1 = zero; HEX0 = two;
   39
                  end
   40
                   'b00011: begin
   41
                     HEX5 = n; HEX4 = n; HEX3 = n; HEX2 = n; HEX1 = zero; HEX0 = three;
   42
   43
                  5'b00100: begin
   44
                     HEX5 = n; HEX4 = n; HEX3 = n; HEX2 = n; HEX1 = zero; HEX0 = four;
   45
   46
                  5'b00101: begin
   47
                     HEX5 = n; HEX4 = n; HEX3 = n; HEX2 = n; HEX1 = zero; HEX0 = five;
   48
                  end
   49
                  5'b00110: begin
   50
                     HEX5 = n; HEX4 = n; HEX3 = n; HEX2 = n; HEX1 = zero; HEX0 = six;
   51
   52
                   'b00111: begin
   53
                     HEX5 = n; HEX4 = n; HEX3 = n; HEX2 = n; HEX1 = zero; HEX0 = seven;
   54
   55
                  5'b01000: begin
                     HEX5 = n; HEX4 = n; HEX3 = n; HEX2 = n; HEX1 = zero; HEX0 = eight;
   56
   57
                  end
   58
                   'b01001: begin
   59
                     HEX5 = n; HEX4 = n; HEX3 = n; HEX2 = n; HEX1 = zero; HEX0 = nine;
   60
                  end
   61
                  5'b01010: begin
   62
                     HEX5 = n; HEX4 = n; HEX3 = n; HEX2 = n; HEX1 = one; HEX0 = zero;
   63
                  end
   64
                  5'b01011: begin
   65
                     HEX5 = n; HEX4 = n; HEX3 = n; HEX2 = n; HEX1 = one; HEX0 = one;
   66
                  end
   67
                  5'b01100: begin
   68
                     HEX5 = n; HEX4 = n; HEX3 = n; HEX2 = n; HEX1 = one; HEX0 = two;
   69
                  end
   70
                  5'b01101: begin
   71
                     HEX5 = n; HEX4 = n; HEX3 = n; HEX2 = n; HEX1 = one; HEX0 = three;
                  end
                   'b01110: begin
                     HEX5 = n; HEX4 = n; HEX3 = n; HEX2 = n; HEX1 = one; HEX0 = four;
```

end

```
5'b01111: begin
                   HEX5 = n; HEX4 = n; HEX3 = n; HEX2 = n; HEX1 = one; HEX0 = five;
 78
                end
                5'b10000: begin
 80
                   HEX5 = n; HEX4 = n; HEX3 = n; HEX2 = n; HEX1 = one; HEX0 = six;
 81
                end
 82
                5'b10001: begin
 83
                   HEX5 = n; HEX4 = n; HEX3 = n; HEX2 = n; HEX1 = one; HEX0 = seven;
 84
                end
 85
                5'b10010: begin
 86
                   HEX5 = n; HEX4 = n; HEX3 = n; HEX2 = n; HEX1 = one; HEX0 = eight;
 87
 88
                 'b10011: begin
 89
                   HEX5 = n; HEX4 = n; HEX3 = n; HEX2 = n; HEX1 = one; HEX0 = nine;
 90
                end
 91
                 'b10100: begin
 92
                   HEX5 = n; HEX4 = n; HEX3 = n; HEX2 = n; HEX1 = two; HEX0 = zero;
 93
                end
 94
                5'b10101: begin
 95
                   HEX5 = n; HEX4 = n; HEX3 = n; HEX2 = n; HEX1 = two; HEX0 = one;
 96
                end
 97
                5'b10110: begin
 98
                   HEX5 = n; HEX4 = n; HEX3 = n; HEX2 = n; HEX1 = two; HEX0 = two;
 99
                end
100
                 'b10111: begin
101
                   HEX5 = n; HEX4 = n; HEX3 = n; HEX2 = n; HEX1 = two; HEX0 = three;
102
103
                 'b11000: begin
                   HEX5 = n; HEX4 = n; HEX3 = n; HEX2 = n; HEX1 = two; HEX0 = four;
104
105
106
                5'b11001: begin
107
                   HEX5 = F; HEX4 = U; HEX3 = L; HEX2 = L; HEX1 = two; HEX0 = five;
108
109
                default: begin
                   HEX5 = F; HEX4 = U; HEX3 = L; HEX2 = L; HEX1 = two; HEX0 = five;
110
111
112
             endcase
         end
113
114
115
      endmodule
116
      module display_testbench();
117
118
         logic [6:0] HEXO, HEX1, HEX2, HEX3, HEX4, HEX5;
119
120
         logic [4:0] z;
         logic clk;
121
122
123
         display dut (.clk, .z, .HEX0, .HEX1, .HEX2, .HEX3, .HEX4, .HEX5);
124
125
         parameter CLOCK_PERIOD = 100;
126
         initial clk = 1;
         always begin
127
128
             #(CLOCK_PERIOD / 2);
129
             c1k = \sim c1k;
130
         end
131
132
         initial begin
                         @(posedge clk);
133
134
                         @(posedge clk);
135
                         @(posedge clk);
                         @(posedge clk);
136
                z = 5'b00000;
137
                                @(posedge clk);
                @(posedge clk);
138
139
                z = 5'b00001;
                                @(posedge clk);
                @(posedge clk);
z = 5'b00010;
                                @(posedge clk);
                @(posedge clk);
                                @(posedge clk);
                z = 5'b00011:
                @(posedge clk);
                z = 5'b00100;
                                @(posedge clk);
145
146
                z = 5'b00101;
                                @(posedge clk);
                      'b00110;
147
                z = 5
                                @(posedge clk);
                    5'b00111;
148
                                @(posedge clk);
                z = 5'b01000;
149
                                @(posedge clk);
```

```
150
151
152
                                                     z = 5'b01001;
z = 5'b01010;
z = 5'b01011;
z = 5'b01100;
                                                                                                           @(posedge clk);
@(posedge clk);
@(posedge clk);
153
                                                                                                            @(posedge clk)
                                                     z = 5'b01101;
z = 5'b01110;
z = 5'b01111;
                                                                                                           @(posedge clk);
@(posedge clk);
@(posedge clk);
154
155
156
                                                                                                          @(posedge clk);
                                                     z = 5'b10000;
z = 5'b10001;
z = 5'b10010;
z = 5'b10011;
157
158
159
160
                                                     z = 5'b10011;
z = 5'b10100;
z = 5'b10101;
z = 5'b10111;
z = 5'b1001;
z = 5'b11001;
z = 5'b11010;
z = 5'b11010;
161
162
163
164
165
166
167
168
                                                     z = 5'b11100;
z = 5'b11101;
z = 5'b11110;
z = 5'b11111;
                                                                                                           @(posedge clk);
@(posedge clk);
@(posedge clk);
@(posedge clk);
169
170
171
172
173
                                            $stop;
174
175
                                 end
176
                      endmodule
177
```