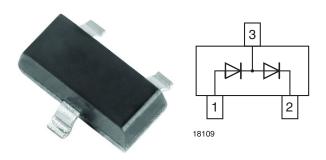


Vishay Semiconductors

Small Signal Switching Diode, Dual in Series



LINKS TO ADDITIONAL RESOURCES









MECHANICAL DATA

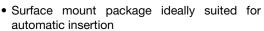
Case: SOT-23

Weight: approx. 8.8 mg Packaging codes / options:

18/10K per 13" reel (8 mm tape), 10K/box 08/3K per 7" reel (8 mm tape), 15K/box

FEATURES

- · Fast switching speed
- High conductance





RoHS

- · Connected in series
- AEC-Q101 qualified available
- Base P/N-E3 RoHS-compliant, commercial grade
- Base P/N-HE3 RoHS-compliant, AEC-Q101 qualified
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

PARTS TABLE					
PART	ORDERING CODE	CIRCUIT CONFIGURATION	TYPE MARKING	REMARKS	
BAV99	BAV99-E3-08 or BAV99-E3-18	Dual serial	JE	Tape and reel	
	BAV99-HE3-08 or BAV99-HE3-18	Duai Seriai	JE		

PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Non repetitive peak reverse voltage		V _{RM}	100	
Repetitive peak reverse voltage = working peak reverse voltage = DC blocking voltage		$V_{RRM} = V_{RWM} = V_{R}$	70	V
Peak forward surge current	t _p = 1 s	1	1	А
reak lorward surge current	t _p = 1 μs	I _{FSM}	4.5	^
Average forward current	Half wave rectification with resistive load and f ≥ 50 MHz, on ceramic substrate 10 mm x 8 mm x 0.7 mm	I _{F(AV)}	150	mA
Forward current	ward current On ceramic substrate 10 mm x 8 mm x 0.7 mm		l _F 250	
Power dissipation	On ceramic substrate 10 mm x 8 mm x 0.7 mm	P _{tot}	300	mW

THERMAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL VALUE		UNIT	
Junction ambient	On ceramic substrate 10 mm x 8 mm x 0.7 mm	R _{thJA} 430		K/W	
Junction and storage temperature range		$T_j = T_{stg}$	-55 to +150	°C	
Operating temperature range		T _{op}	-55 to +150	°C	



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ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
	I _F = 1 mA	V _F			0.715	V
Forward voltage	I _F = 10 mA				0.855	V
Forward voltage	I _F = 50 mA				1	V
	I _F = 150 mA				1.25	V
	V _R = 70 V	I _R			2500	nA
Reverse current	V _R = 70 V, Tj = 150 °C				50	μA
	V _R = 25 V, Tj = 150 °C				30	μΑ
Diode capacitance	$V_R = 0$, $f = 1$ MHz	C _D			1.5	pF
Reverse recovery time	I_F = 10 mA to I_R = 1 mA, V_R = 6 V, R_L = 100 Ω	t _{rr}			6	ns

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

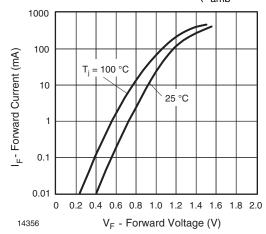


Fig. 1 - Forward Current vs. Forward Voltage

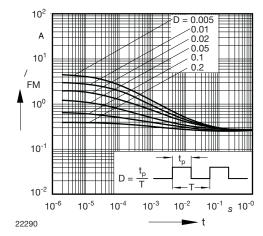
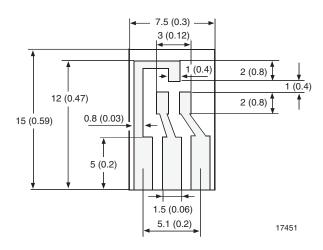


Fig. 2 - Peak forward current $f_{FM} = f(t_p)$

LAYOUT FOR RthJA TEST

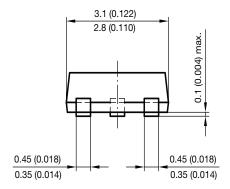
Thickness:

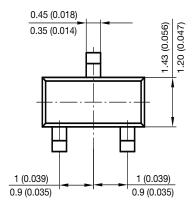
Fiberglass 1.5 mm (0.059 inches) Copper leads 0.3 mm (0.012 inches)



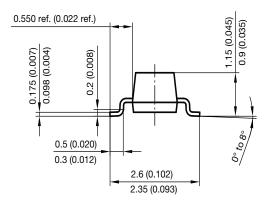


PACKAGE DIMENSIONS in millimeters (inches): SOT-23

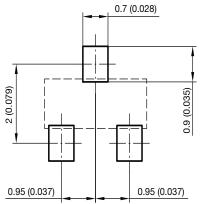




Document no.: 6.541-5014.01-4 Rev. 8 - Date: 23. Sep. 2009 17418



Foot print recommendation:





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