

Title		
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A		
Date:	10/27/2016	Sheet of
File:	\\.\BMS.SchDoc	Drawn By:

A

A

B

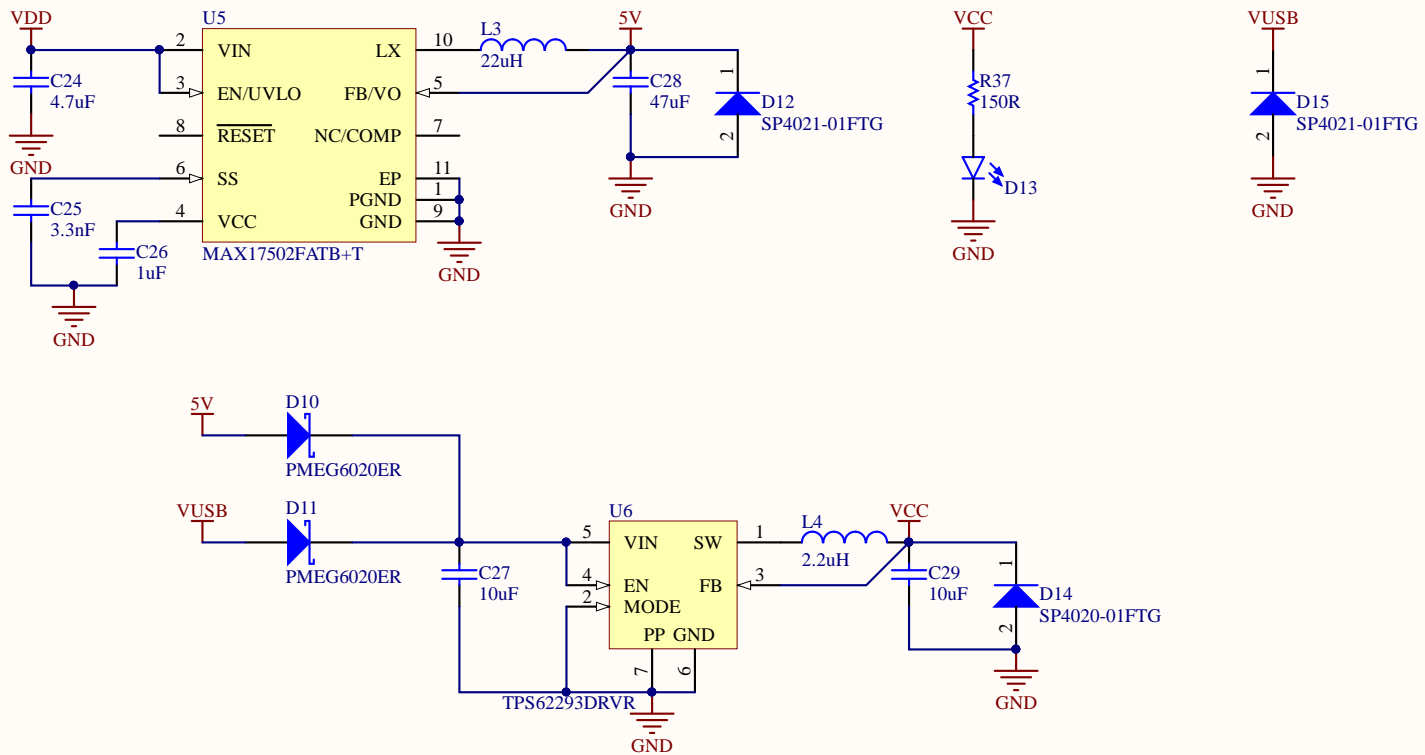
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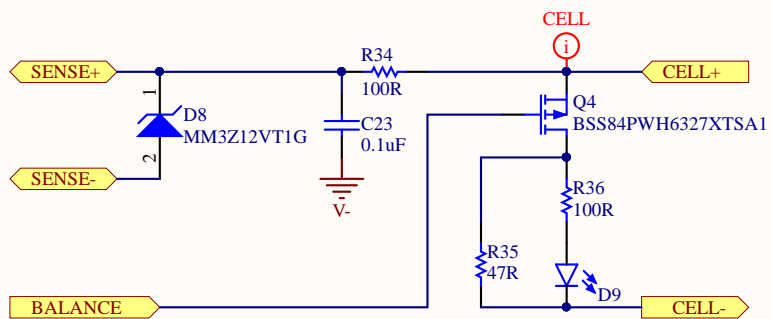
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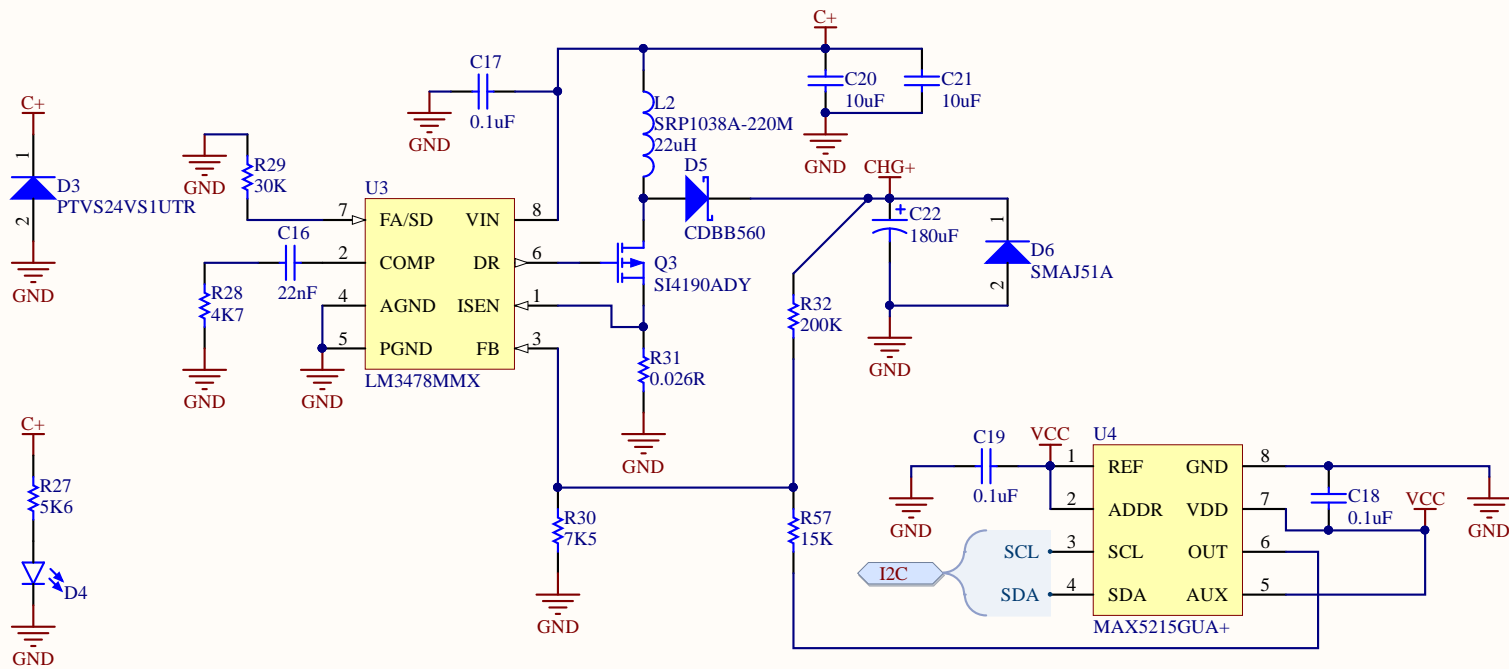
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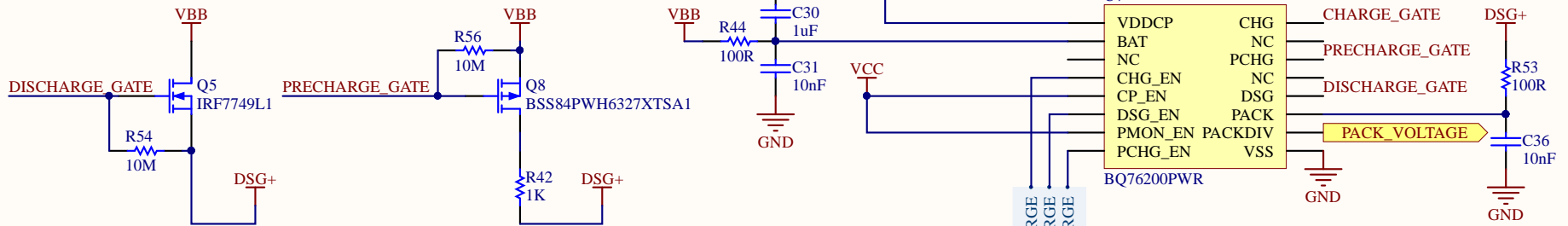
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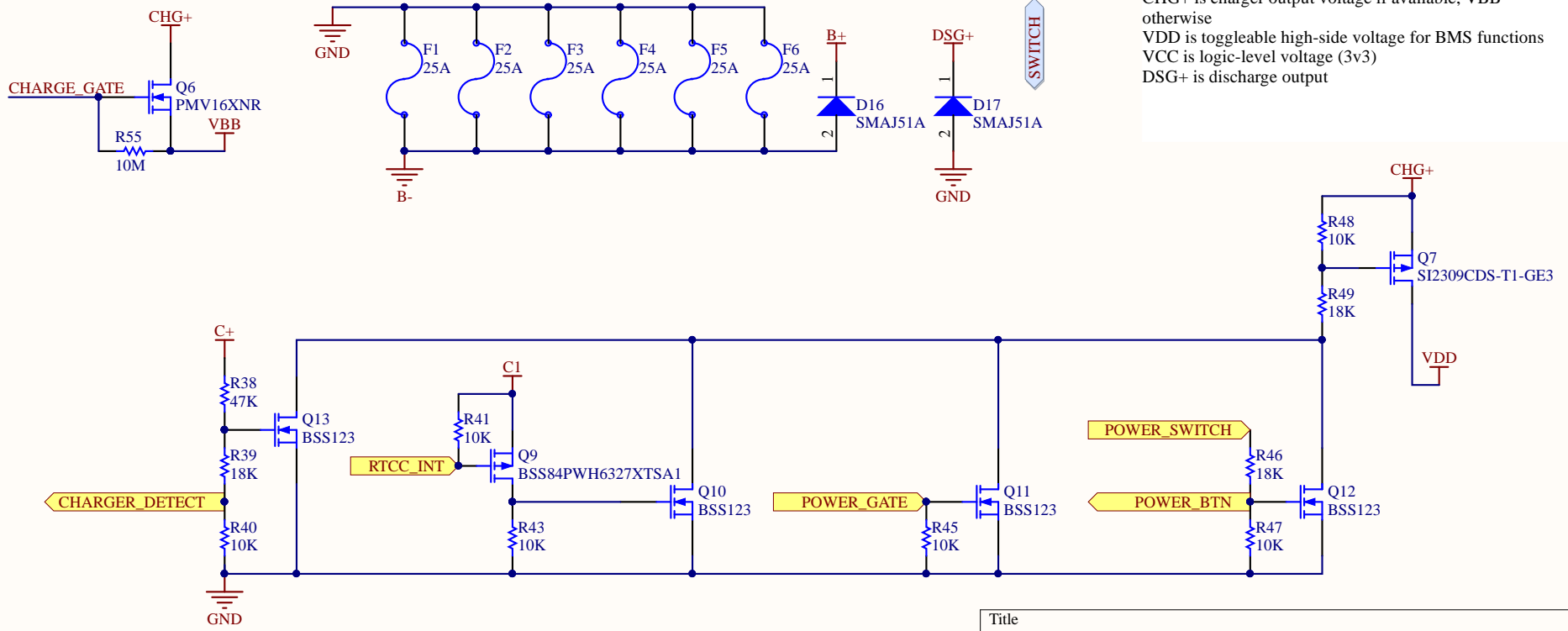


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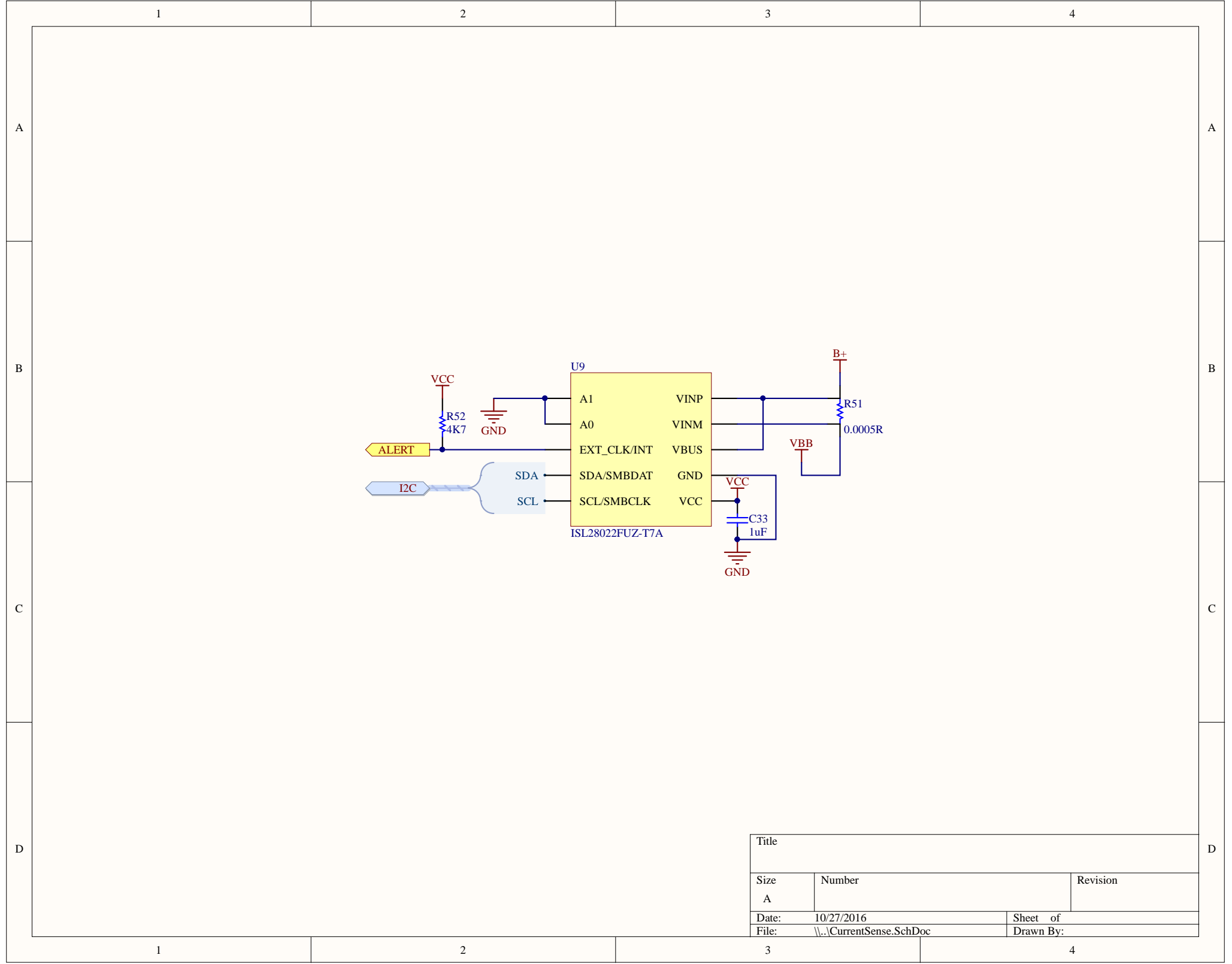


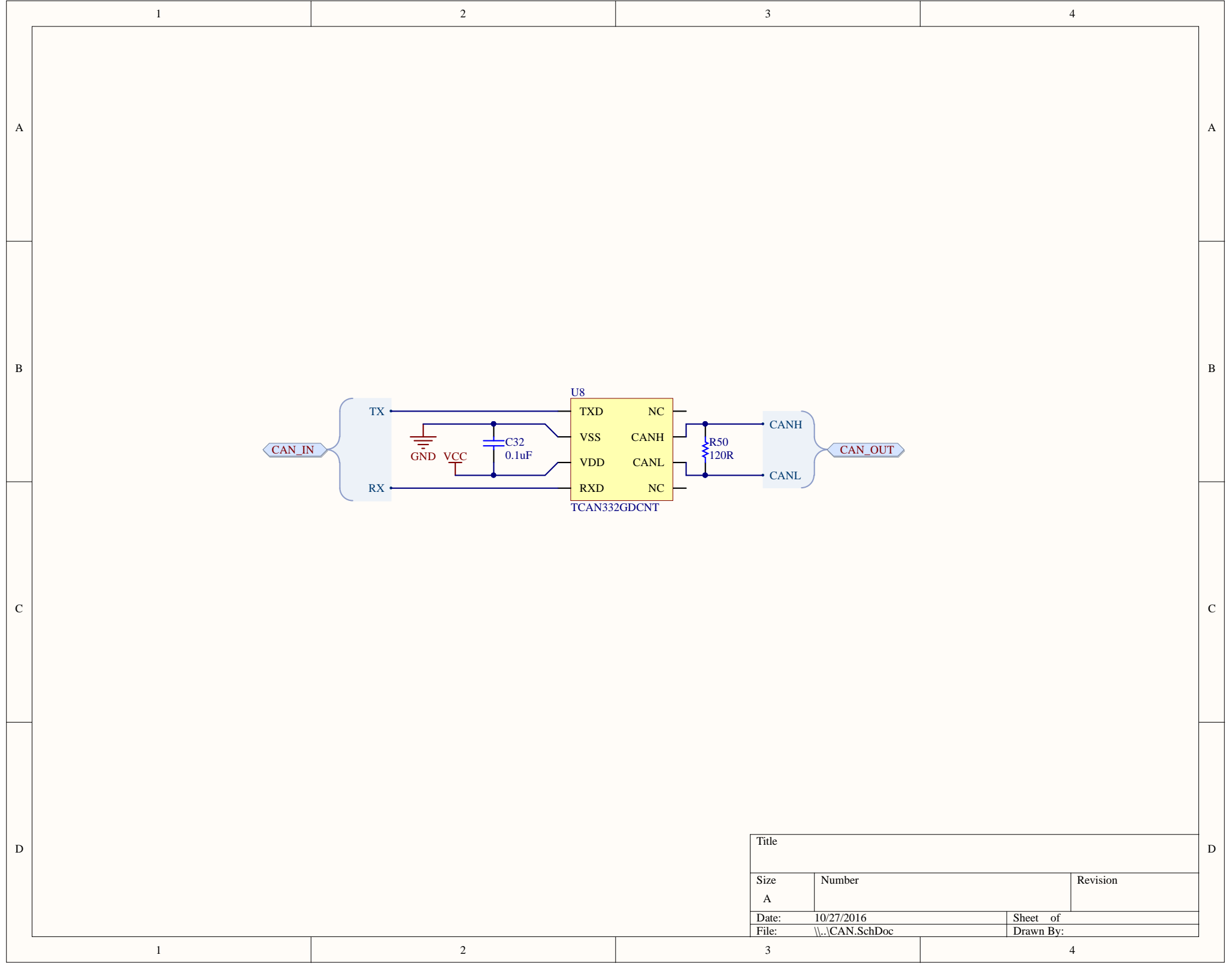
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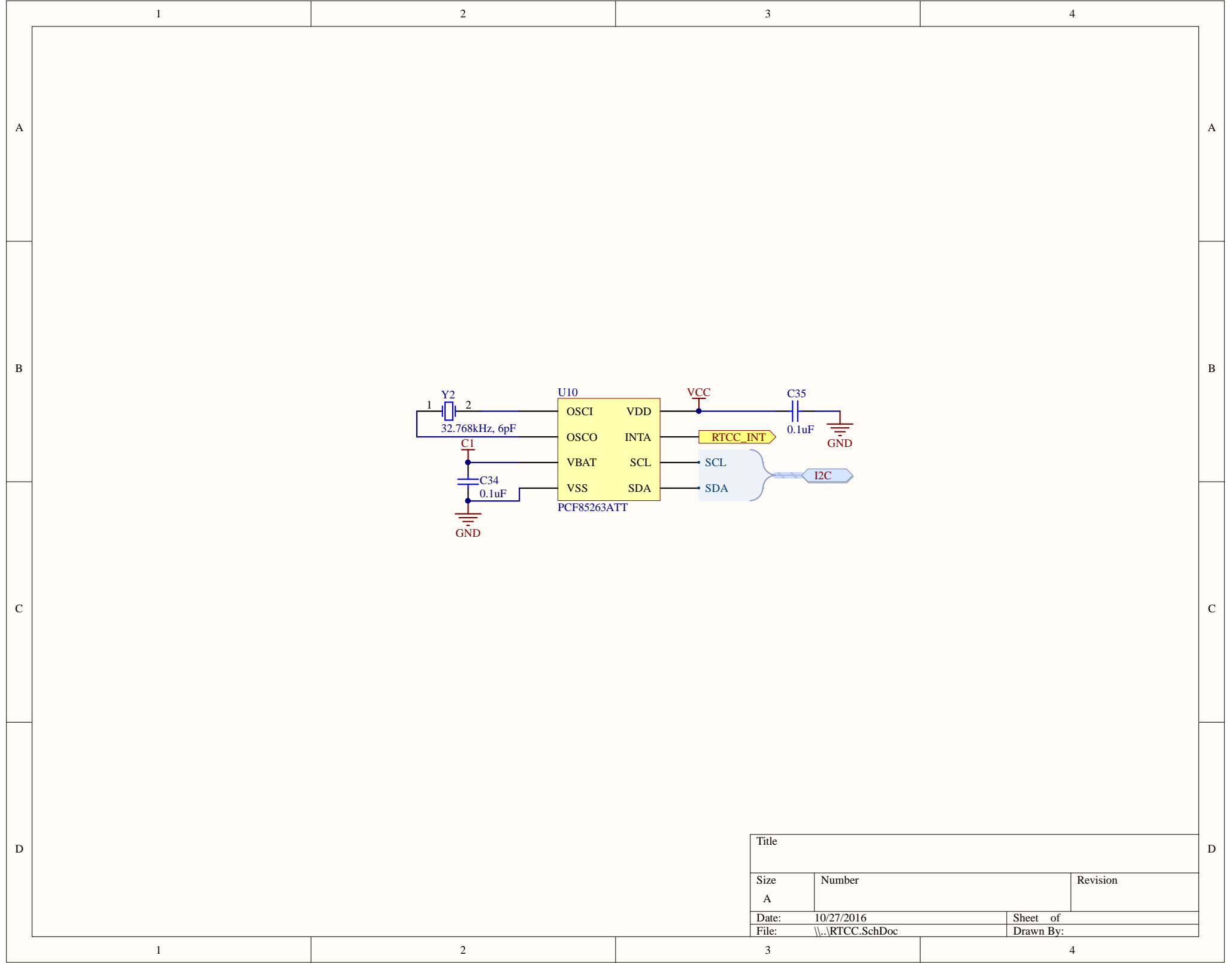
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 B+ is tied to battery lead
 VBB is high-side voltage, after current sense
 CHG+ is charger output voltage if available, VBB otherwise
 VDD is toggleable high-side voltage for BMS functions
 VCC is logic-level voltage (3v3)
 DSG+ is discharge output



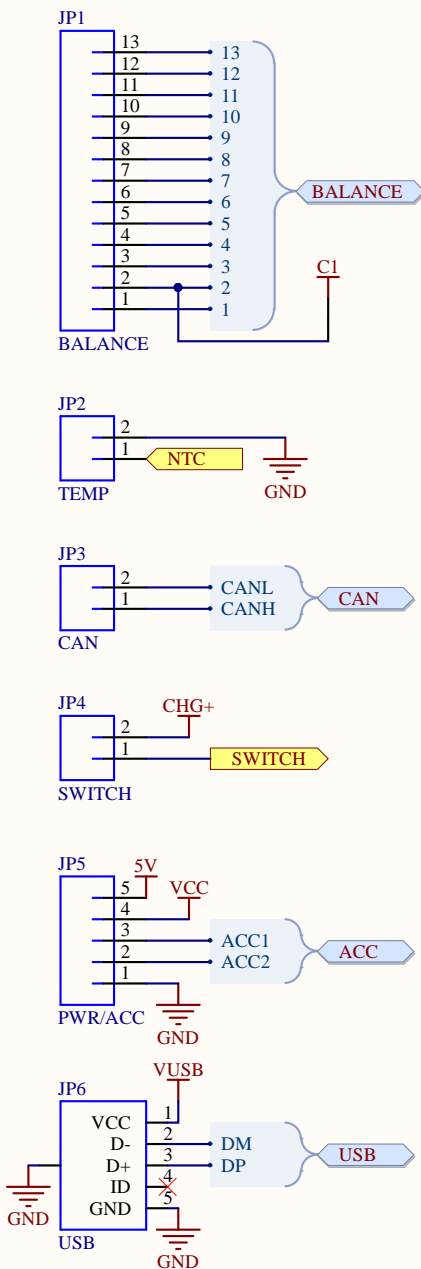
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