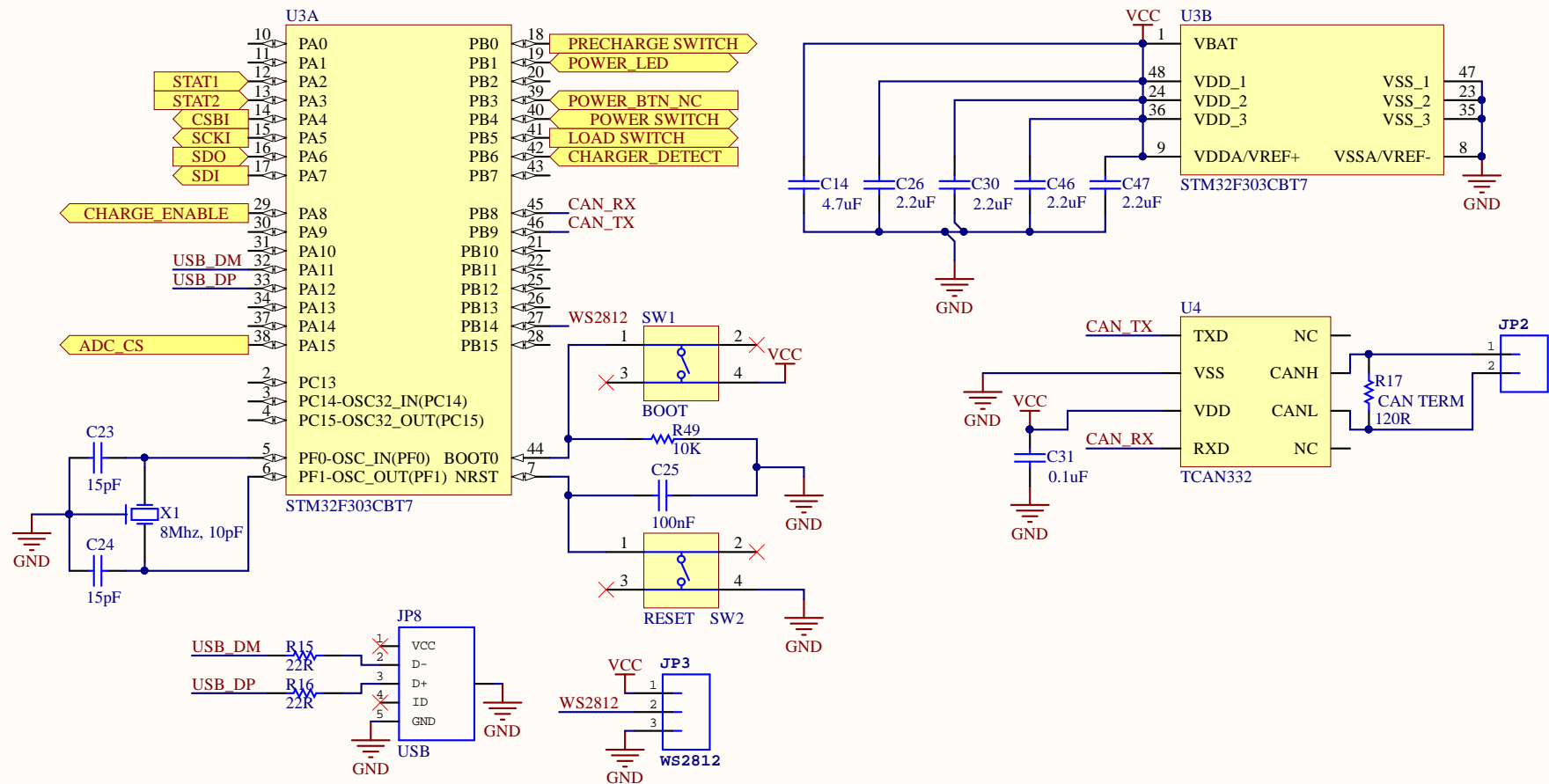


Title			
Battman Overview			
Size	Number		Revision
A			3.0
Date:	8/20/2016	Sheet	of
File:	C:\Users\...\BMS.SchDoc	Drawn By:	Raphael Chang & Brent Yi



Title BMS Logic		
Size A	Number 8/20/2016	Revision 3.0
Date:	8/20/2016	Sheet of
File:	C:\Users\...\MCU.SchDoc	Drawn By: Brent Yi

Power Ports:

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B+ is tied to battery lead

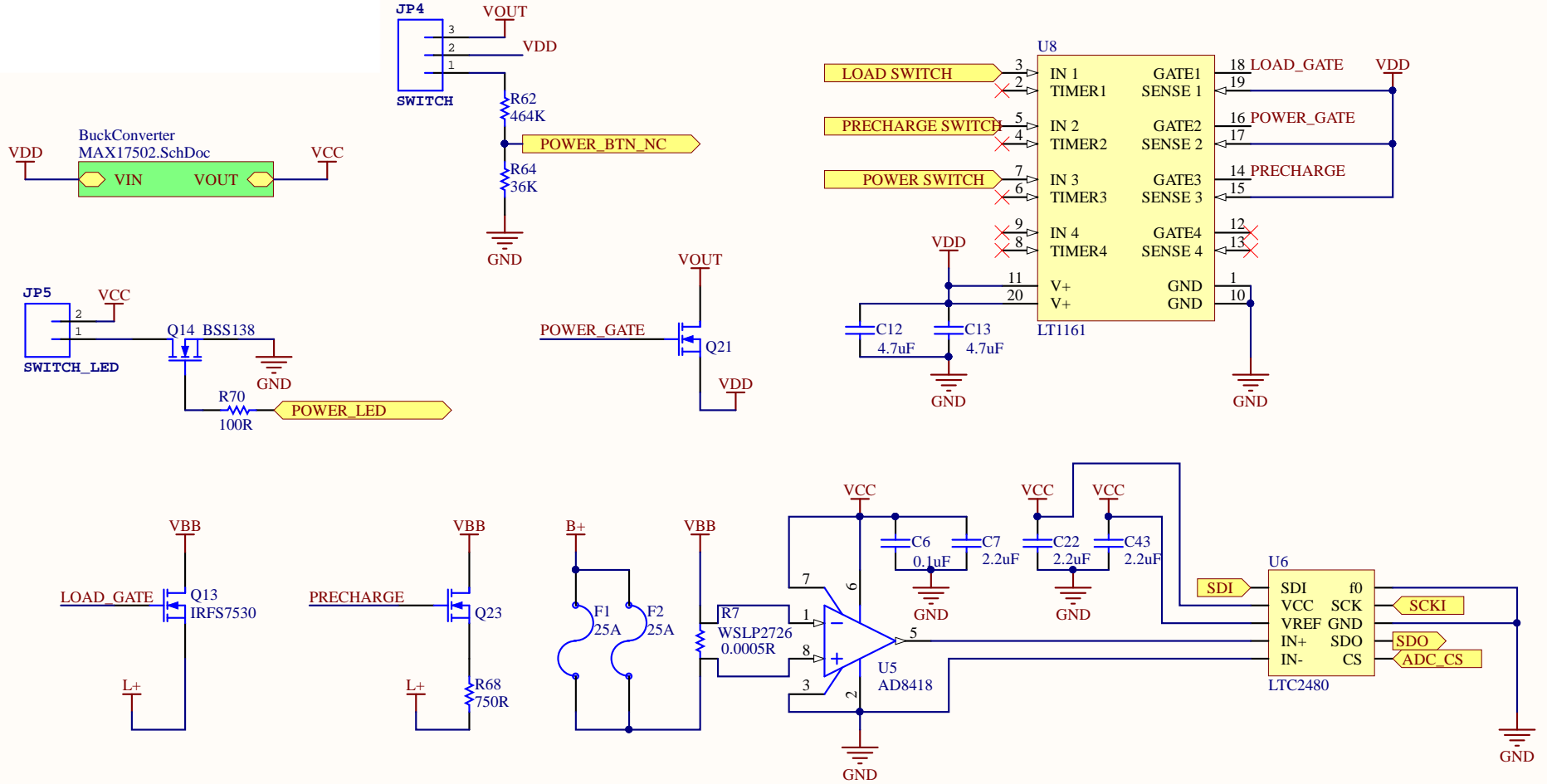
VBB is high-side voltage, after fusing & current sense

VOUT is charger input voltage if available, VBB otherwise

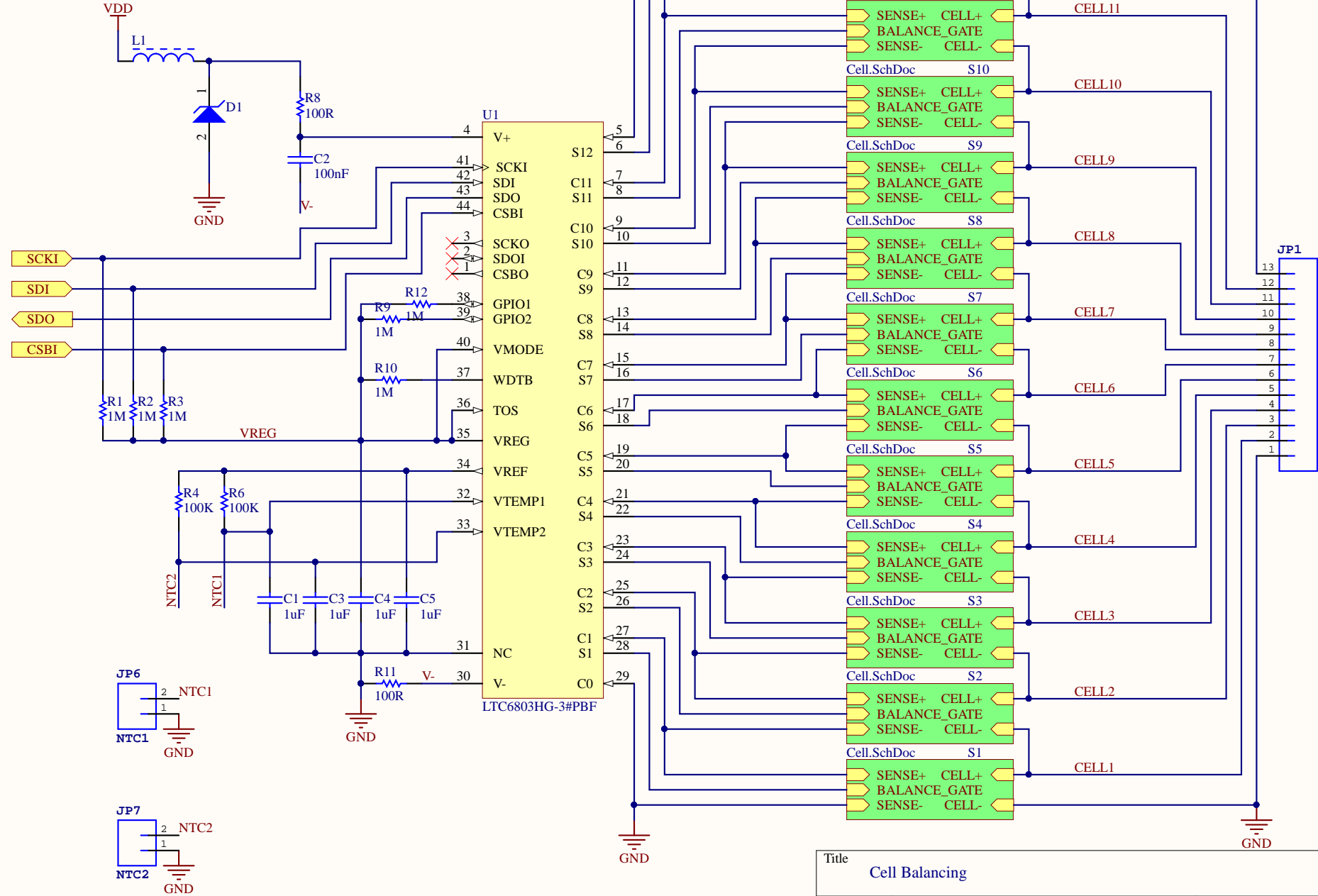
VDD is toggleable high-side voltage for BMS functions

VCC is logic-level voltage (3v3)

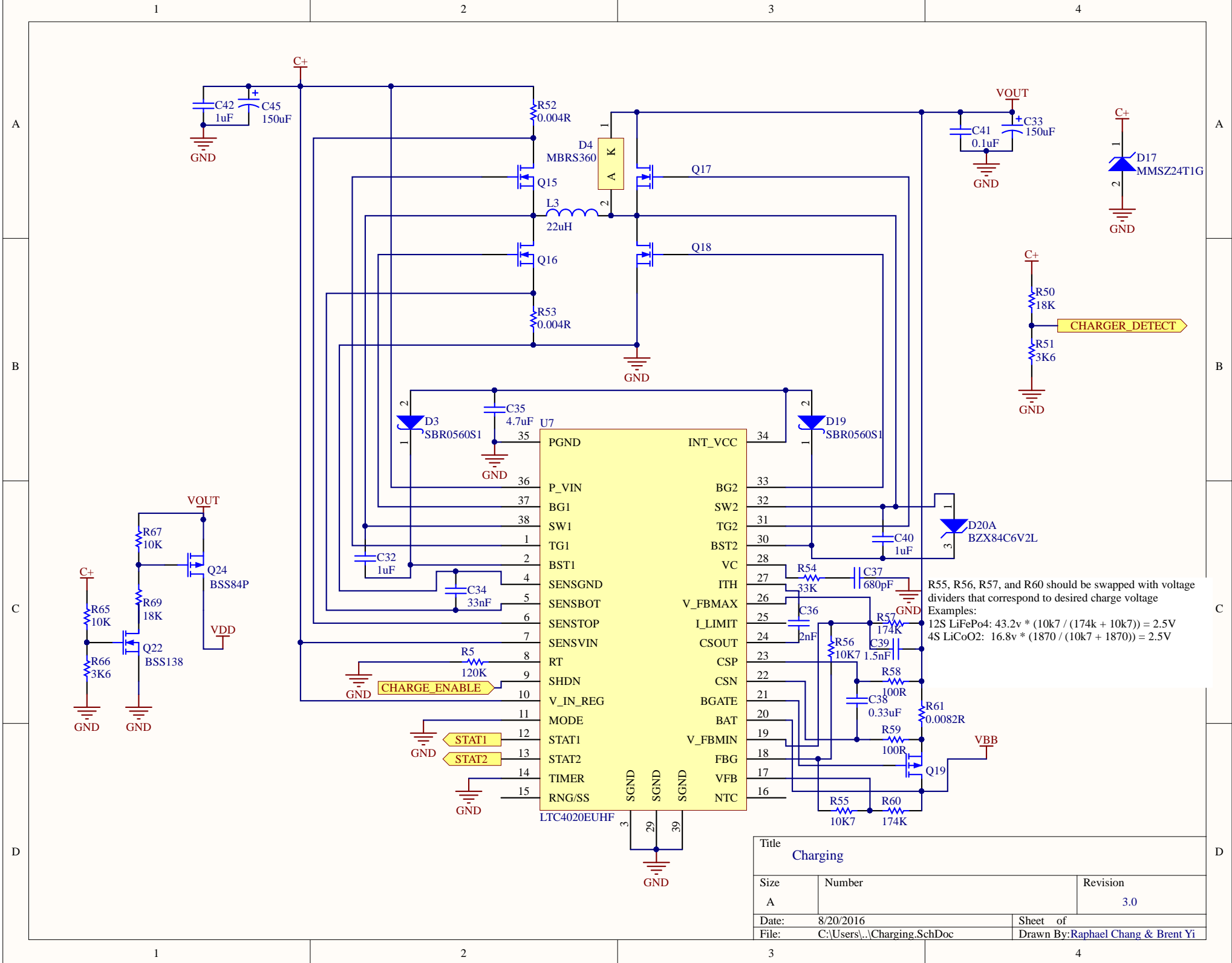
L+ is load output

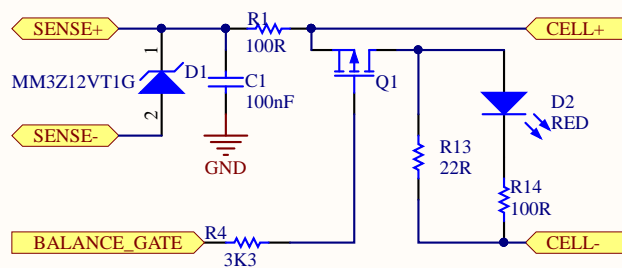


Title		
BMS Power		
Size	Number	Revision
A		3.0
Date:	8/20/2016	Sheet of
File:	C:\Users\...\Power.SchDoc	Drawn By: Raphael Chang & Brent Yi

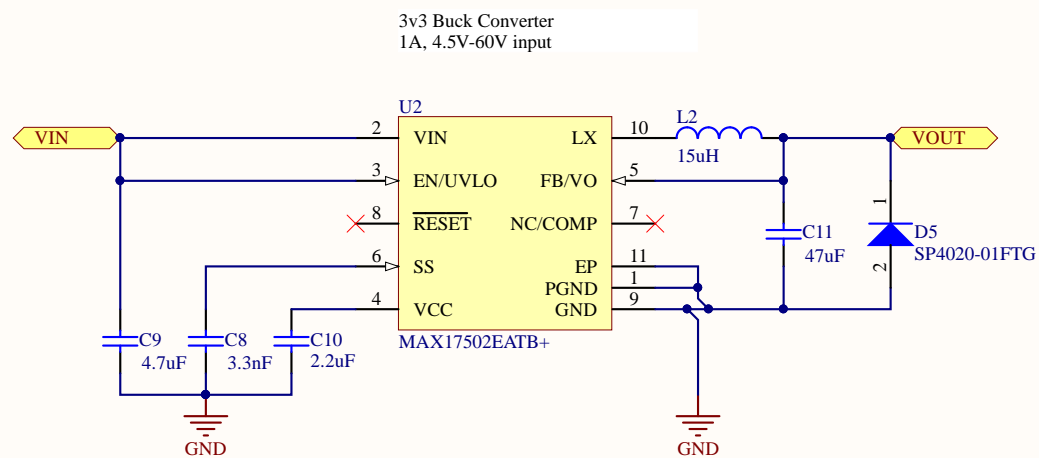


Title		
Cell Balancing		
Size	Number	Revision
A		3.0
Date:	8/20/2016	Sheet of
File:	C:\Users\...\LTC6803.SchDoc	Drawn By: Raphael Chang & Brent Yi





Title			Cell Balancing Module
Size	Number	Revision	
A		3.0	
Date:	8/20/2016	Sheet of	
File:	C:\Users\...\Cell.SchDoc	Drawn By:	Brent Yi



Title		
3V3 Buck Converter		
Size	Number	Revision
A		3.0
Date:	8/20/2016	Sheet of
File:	C:\Users\...\MAX17502.SchDoc	Drawn By: Brent Yi

