

NOTE: This is a “Work In Progress”; implement at your own risk.

Sheet: OSv4\_logic

Logic

File: OSv4\_logic.sch

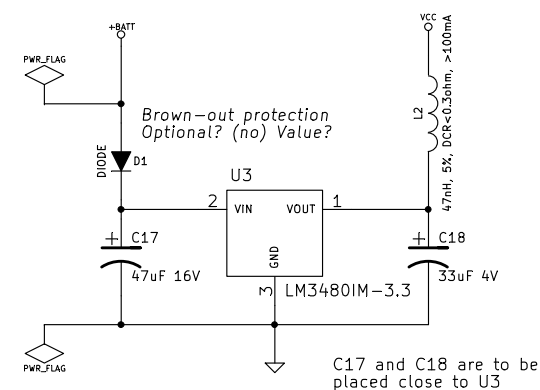
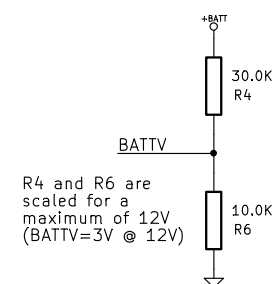
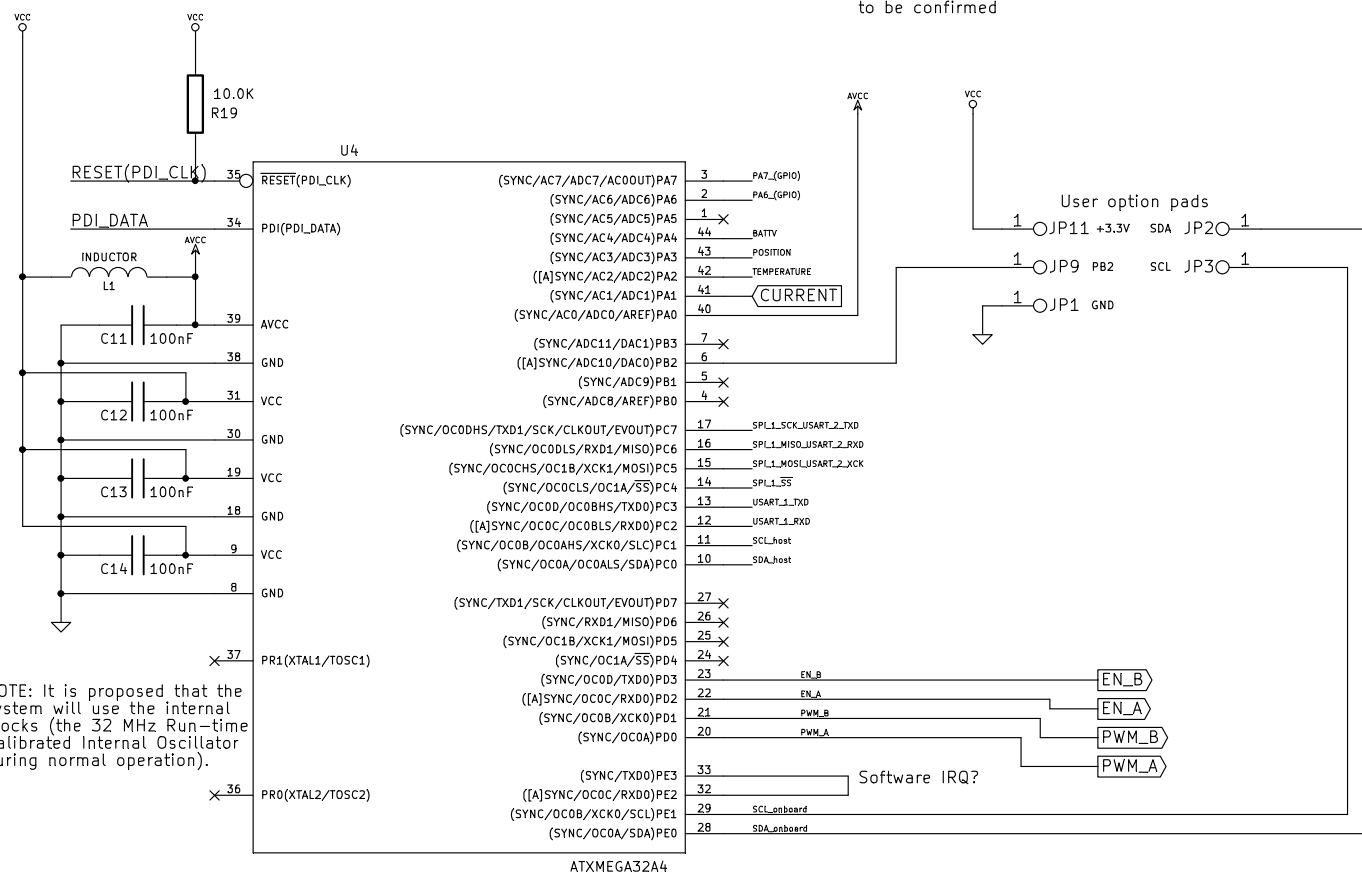
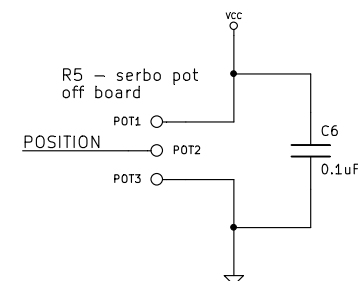
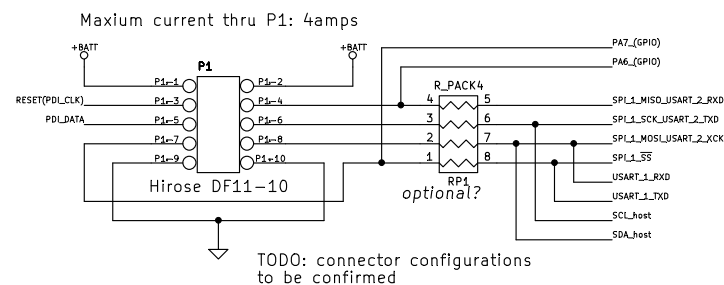
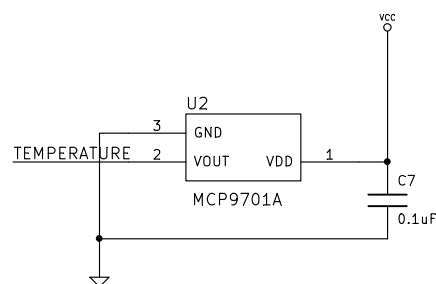
Sheet: OSv4\_H-bridge

H-bridge

File: OSv4\_H-bridge.sch

File: OSv4.sch		
Sheet: /		
Title: OpenServo v4 preliminary work-up, © OpenServo project 2010		
Size: A4	Date: 19 apr 2010	Rev:
KiCad E.D.A.	eeschema (20100406 SVN-R2508)-final	Id: 1/3

**NOTE: This is a "Work In Progress"; implement at your own risk.**



NOTE: Decoupling capacitors to be placed close to the device for each supply pin pair in a signal group.

File: OSv4\_logic.sch

Sheet: /OSv4\_logic/

Title: OpenServo v4 preliminary work-up, © OpenServo project 2010

Size: A4	Date: 19 apr 2010
----------	-------------------

KiCad E.D.A. eeschema (20100406 SVN-R2508)-final

Rev: 0.0

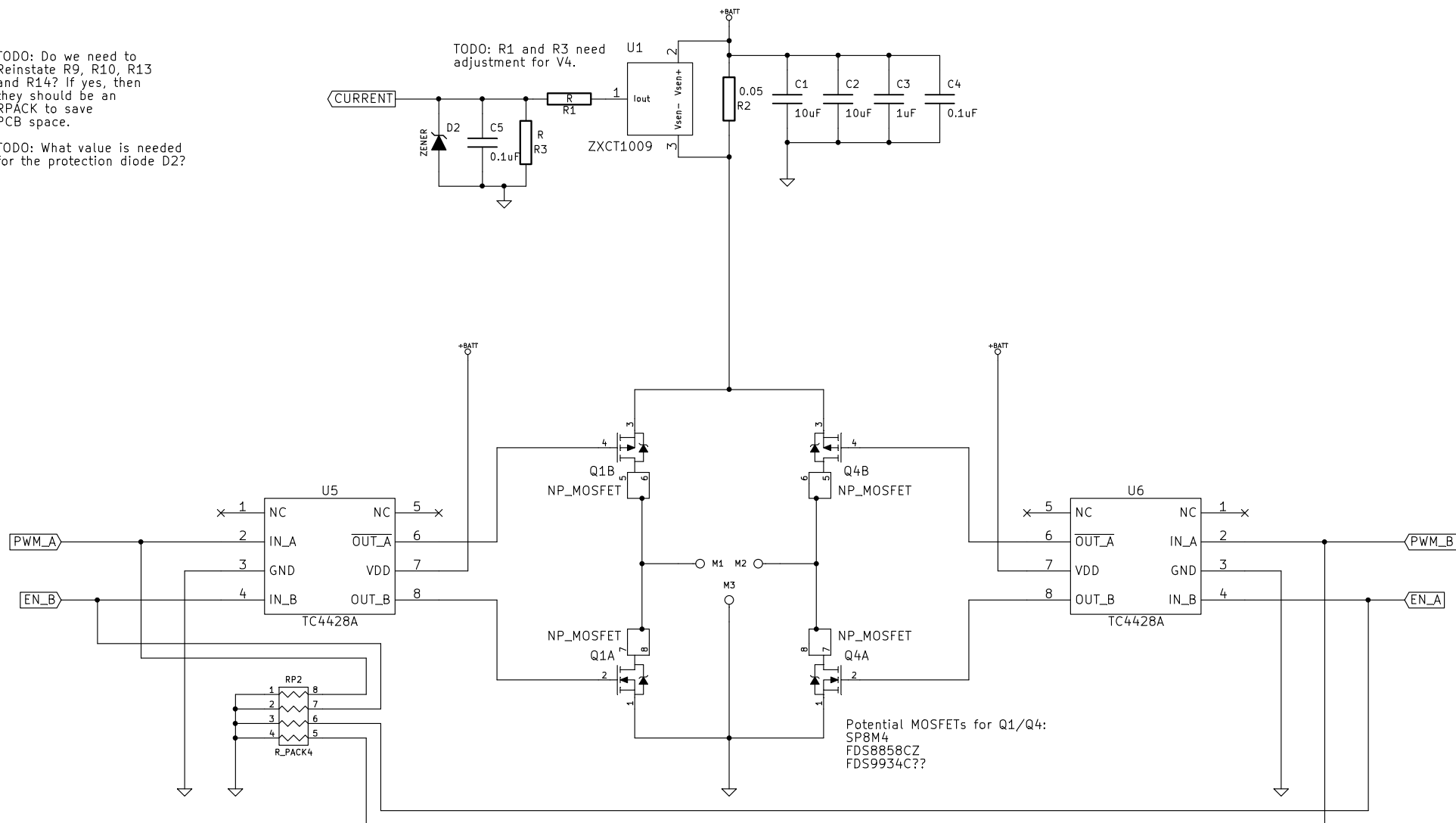
Id: 2/3

**NOTE: This is a "Work In Progress"; implement at your own risk.**

TODO: Do we need to Reinststate R9, R10, R13 and R14? If yes, then they should be an RPACK to save PCB space.

TODO: What value is needed for the protection diode D2?

TODO: R1 and R3 need adjustment for V4.



Potential MOSFETs for Q1/Q4:  
SP8M4  
FDS8858CZ  
FDS9934C??

File: OSv4_H-bridge.sch		
Sheet: /OSv4_H-bridge/		
Title: OpenServo v4 preliminary work-up, © OpenServo project 2010		
Size: A4	Date: 19 apr 2010	Rev: 0.0
KiCad E.D.A. eeschema (20100406 SVN-R2508)-final		Id: 3/3