

NOTE: This is a "Work In Progress"; implement at your own risk.

Sheet: OSv4_logic

Logic

File: OSv4_logic.sch

Sheet: OSv4_H-bridge

H-bridge

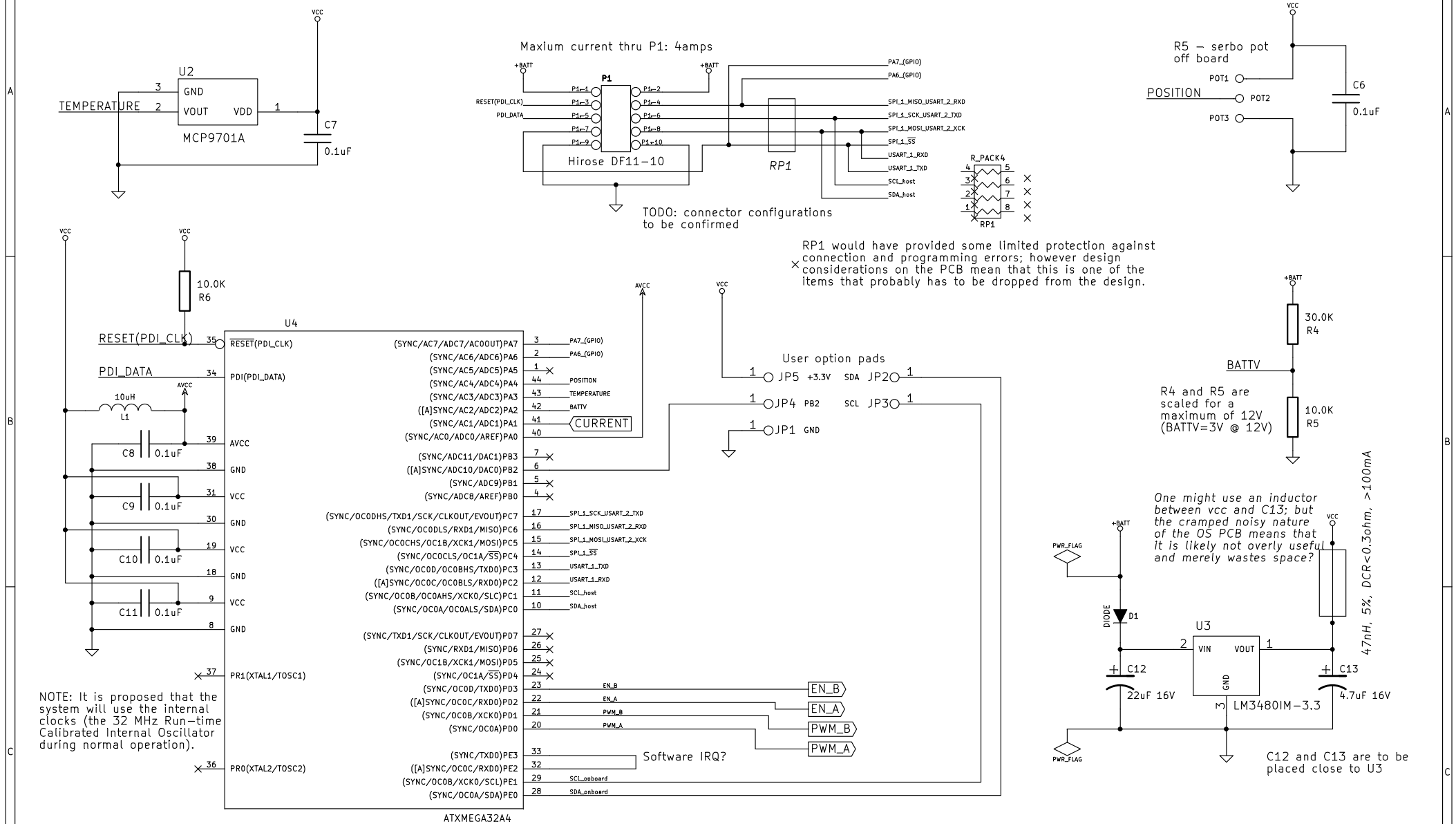
File: OSv4_H-bridge.sch

NOTE: This is the latest schematic and does not match the older PCB and other items which you may have previously downloaded from the same location as this document– the PCB design is being redone.

Note also that this is most definitely a "Work In Progress" that is in a state of flux... It is presented for review purposes only and does not represent an end product!

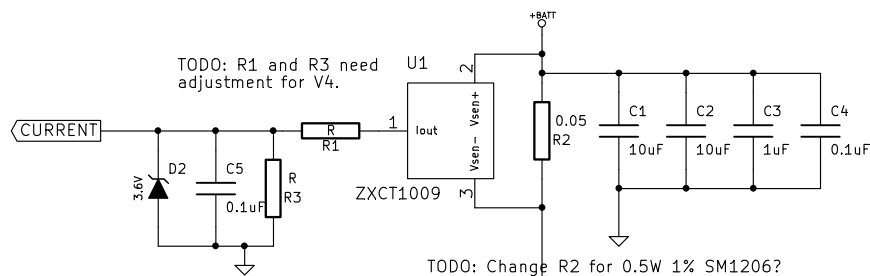
File: OSv4.sch		
Sheet: /		
Title: OpenServo v4 preliminary work-up, © OpenServo project 2010		
Size: A4	Date: 8 may 2010	Rev:
KiCad E.D.A.	eeschema (20100406 SVN-R2508)–final	Id: 1/3

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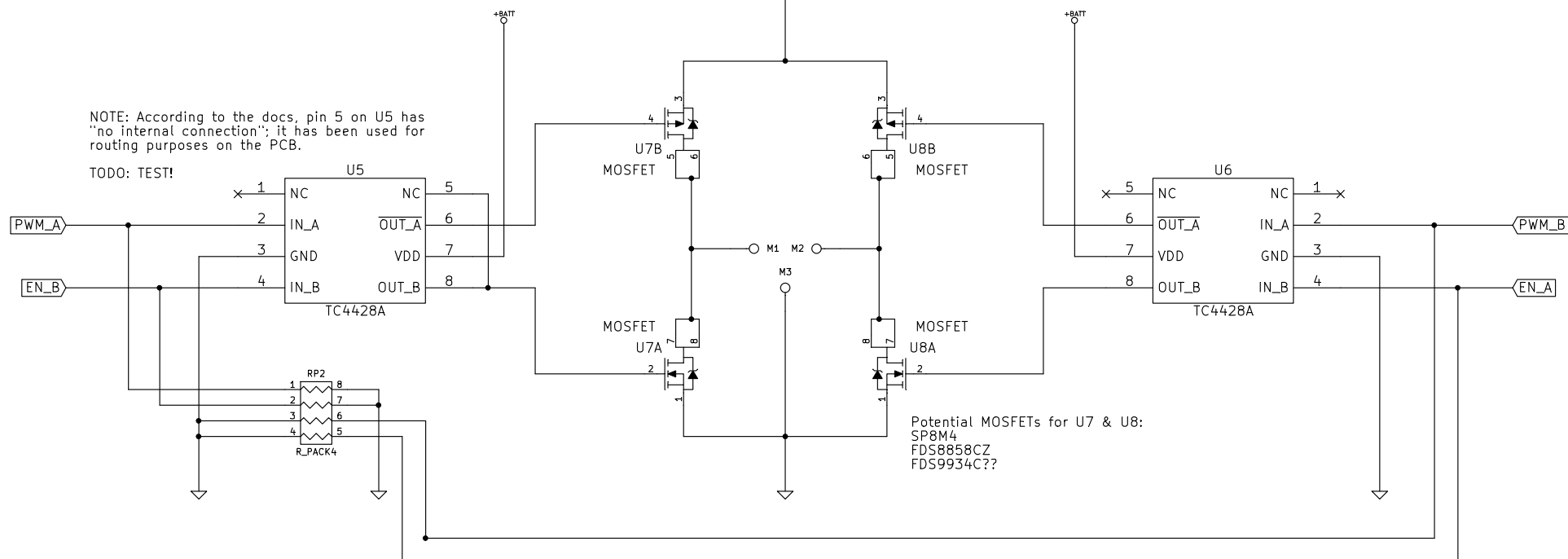
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U5 & U6 (TC4428A): Originally the intention was to use the MSOP-8 package which would allow more room on the PCB for other things. However, this packaging only appears to be available when purchased in large quantities; which is not workable for a project where there is an expectation to be able to produce low-count runs.

NOTE: According to the docs, pin 5 on U5 has "no internal connection"; it has been used for routing purposes on the PCB.

TODO: TEST!



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