

**NOTE: This is a "Work In Progress"; implement at your own risk.**

Sheet: OSv4\_logic

Logic

File: OSv4\_logic.sch

Sheet: OSv4\_H-bridge

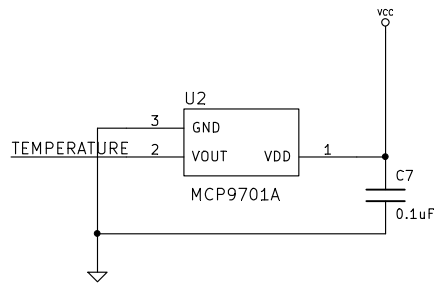
H-bridge

File: OSv4\_H-bridge.sch

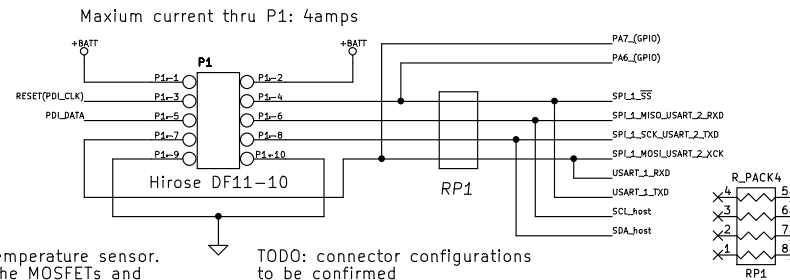
**NOTE: This is most definitely a "Work In Progress" that is in a state of flux... It is presented for review purposes only and does not represent an end product!**

File: OSv4.sch		
Sheet: /		
Title: OpenServo v4 preliminary work-up, © OpenServo project 2010		
Size: A4	Date: 4 jul 2010	Rev:
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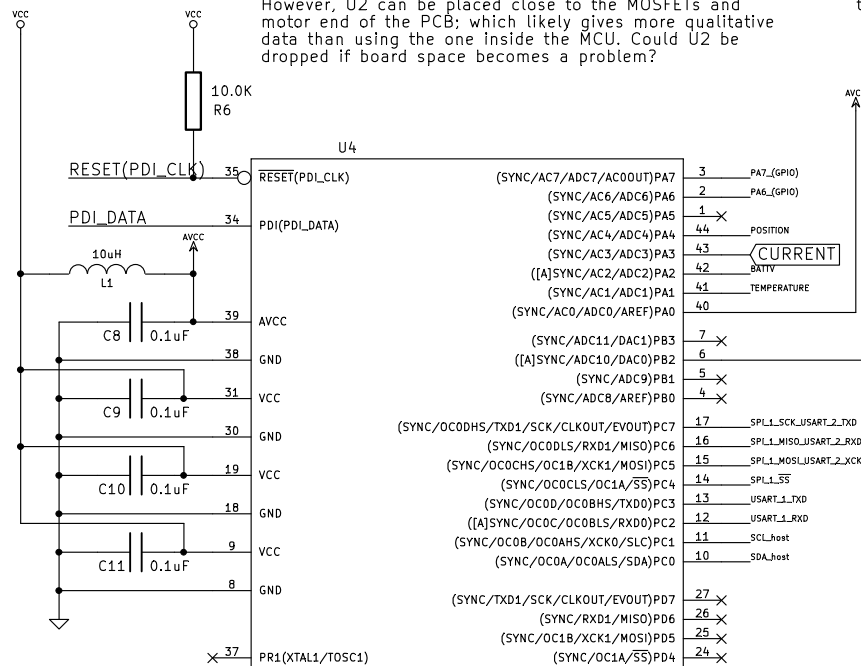
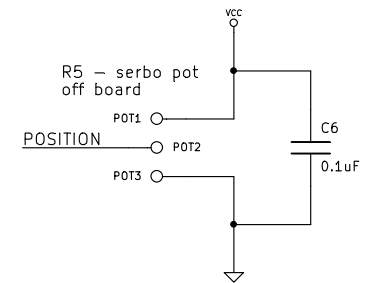


NOTE: The XMEGA has an integrated temperature sensor. However, U2 can be placed close to the MOSFETs and motor end of the PCB; which likely gives more qualitative data than using the one inside the MCU. Could U2 be dropped if board space becomes a problem?



TODO: connector configurations to be confirmed

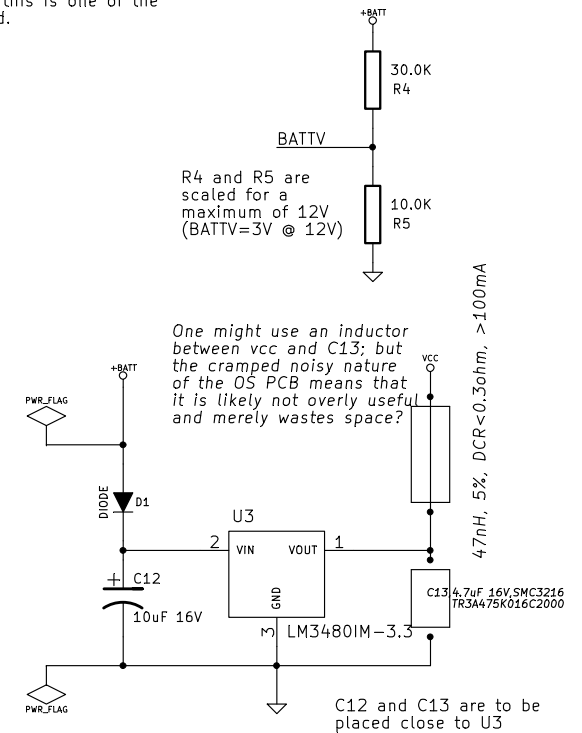
RP1 would have provided some limited protection against connection and programming errors; however design considerations on the PCB mean that this is one of the items that probably has to be dropped.



NOTE: It is proposed that the system will use the internal clocks (the 32 MHz Run-time Calibrated Internal Oscillator during normal operation).

Software IRQ?

NOTE: Decoupling capacitors to be placed close to the device for each supply pin pair in a signal group.



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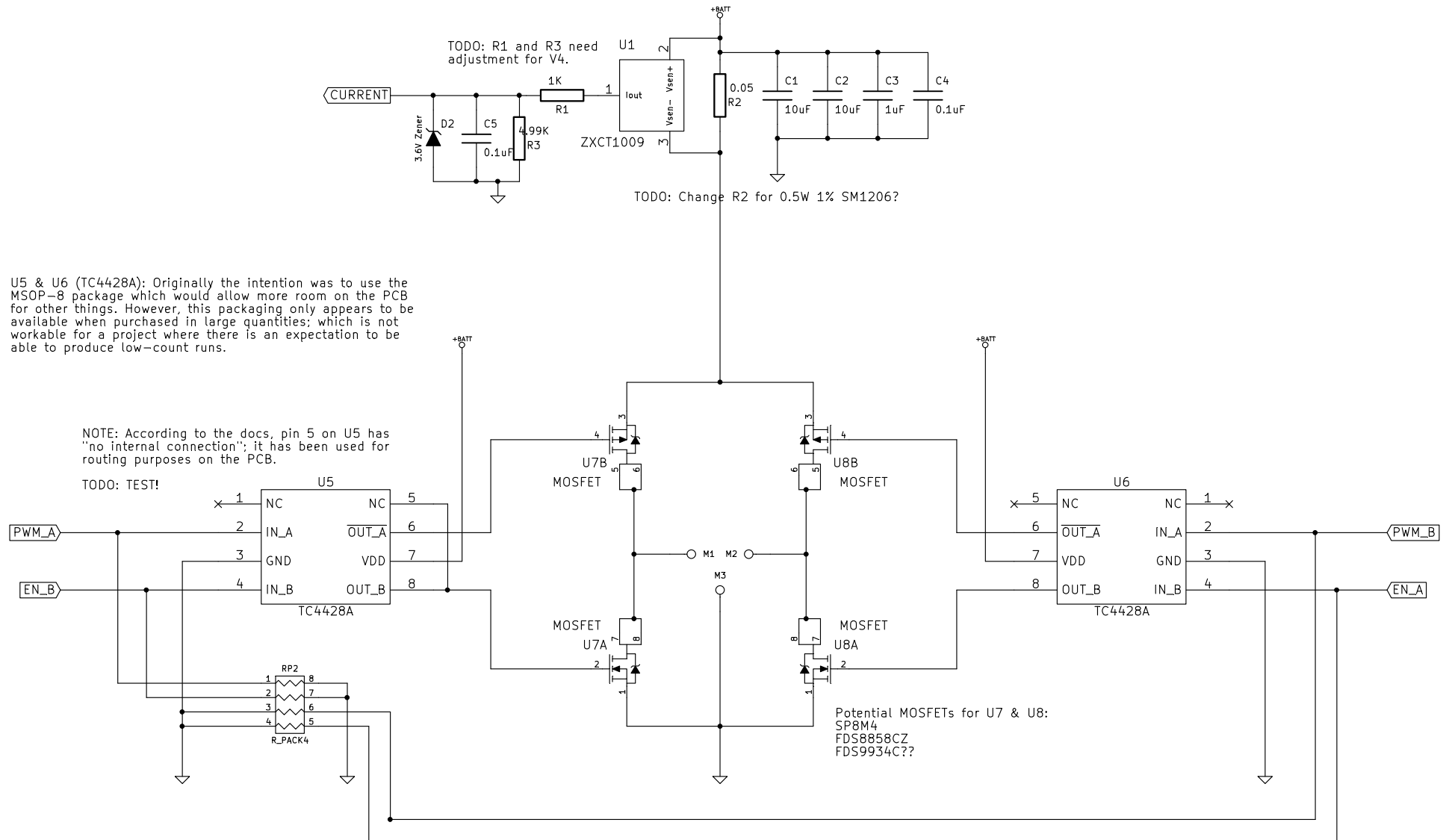
Size: A4 Date: 4 jul 2010

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