

NOTE: This is a “Work In Progress”; implement at your own risk.

Sheet: OSv4_logic

Logic

File: OSv4_logic.sch

Sheet: OSv4_H-bridge

H-bridge

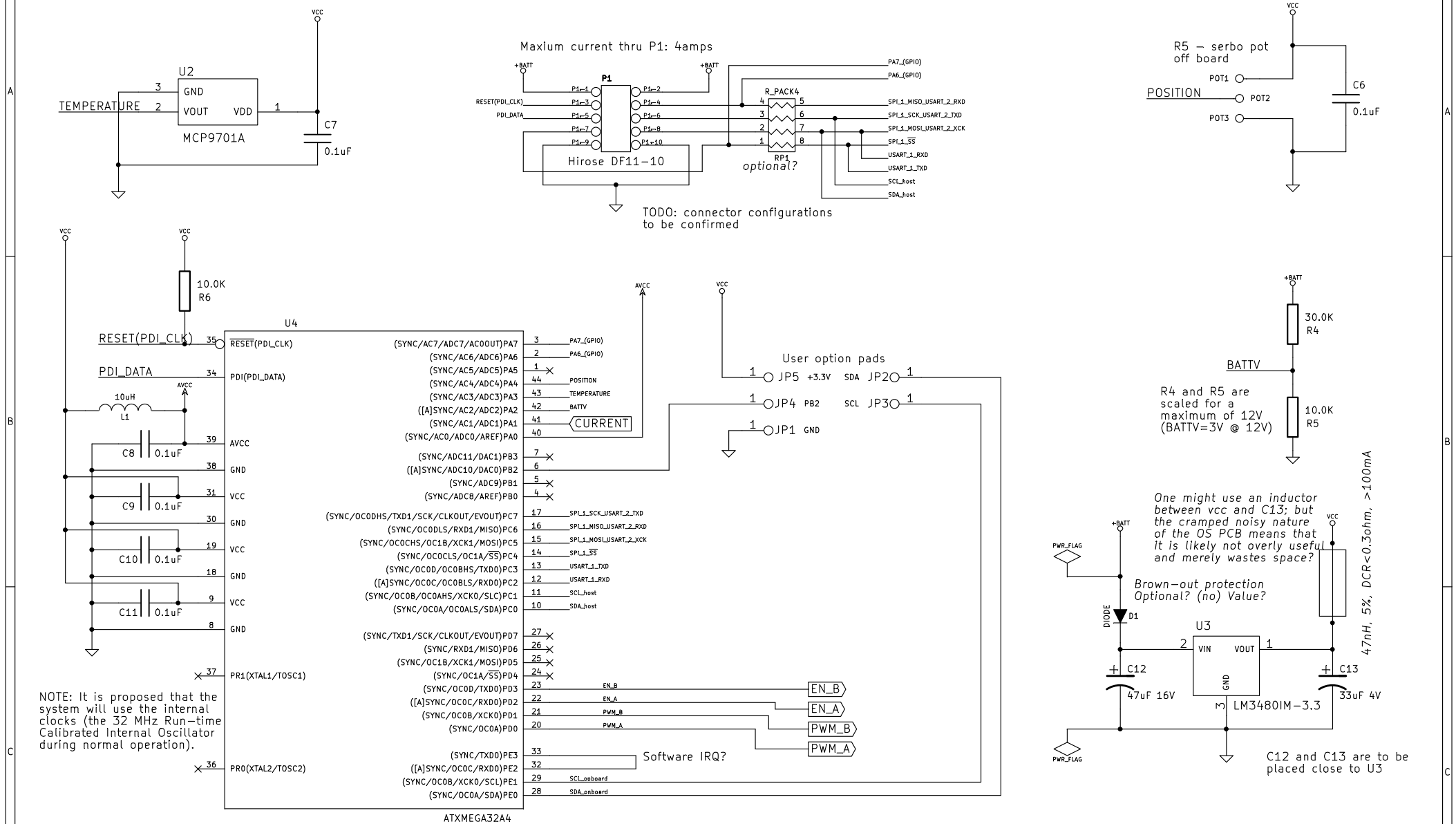
File: OSv4_H-bridge.sch

NOTE: This is the latest schematic and does not match the PCB and other items which you may currently find in the same location as this document– the PCB design is being redone.

Note also that this is most definitely a “Work In Progress” that is in a state of flux... It is presented for review purposes only and does not represent an end product!

File: OSv4.sch		
Sheet: /		
Title: OpenServo v4 preliminary work-up, © OpenServo project 2010		
Size: A4	Date: 7 may 2010	Rev:
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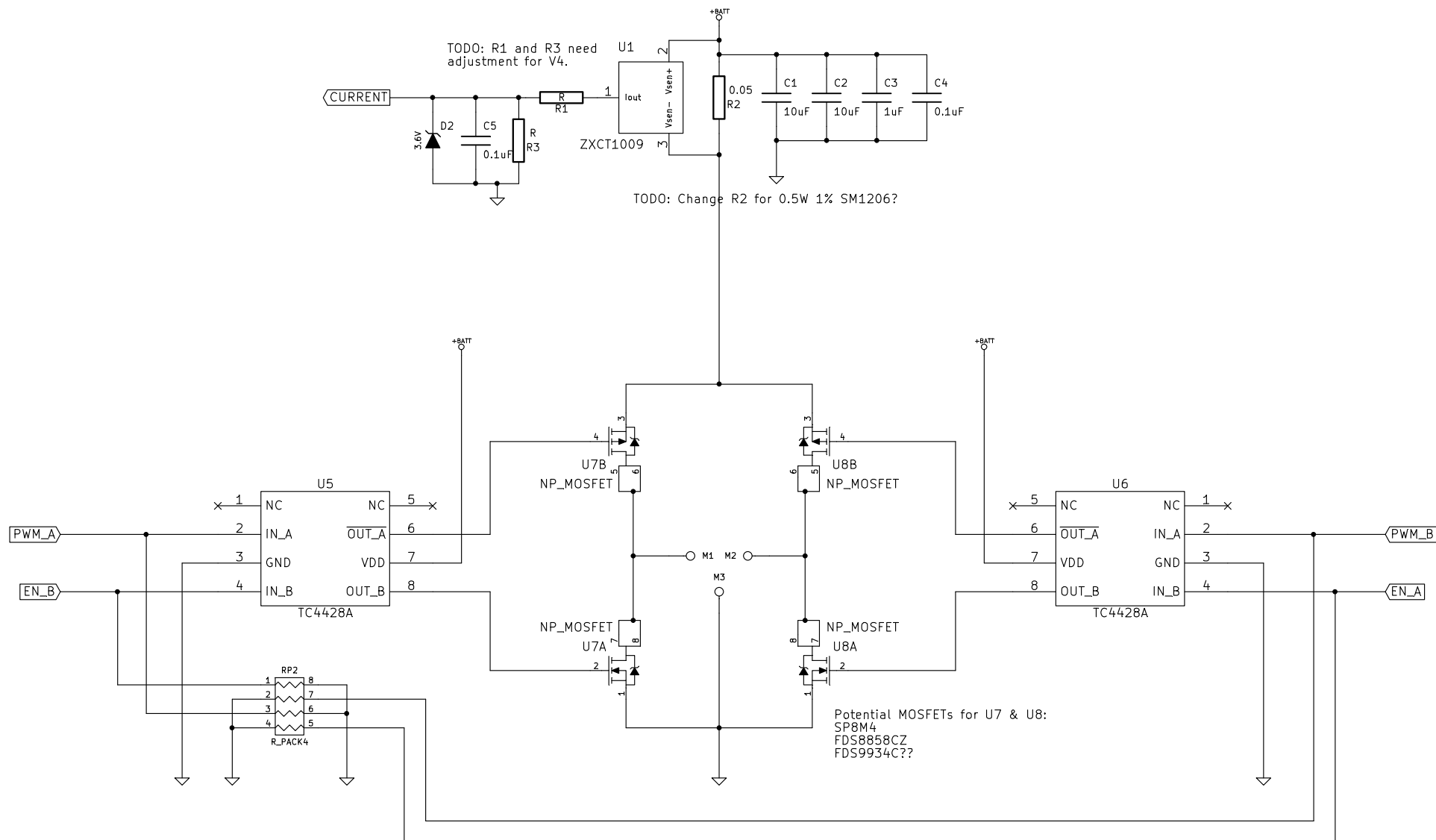
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NOTE: Decoupling capacitors to be placed close to the device for each supply pin pair in a signal group.

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