

NOTE: This is a “Work In Progress”; implement at your own risk.

Sheet: OSv4_logic

Logic

File: OSv4_logic.sch

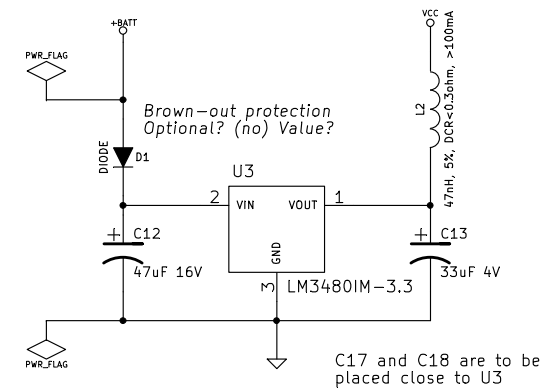
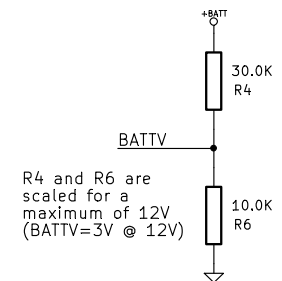
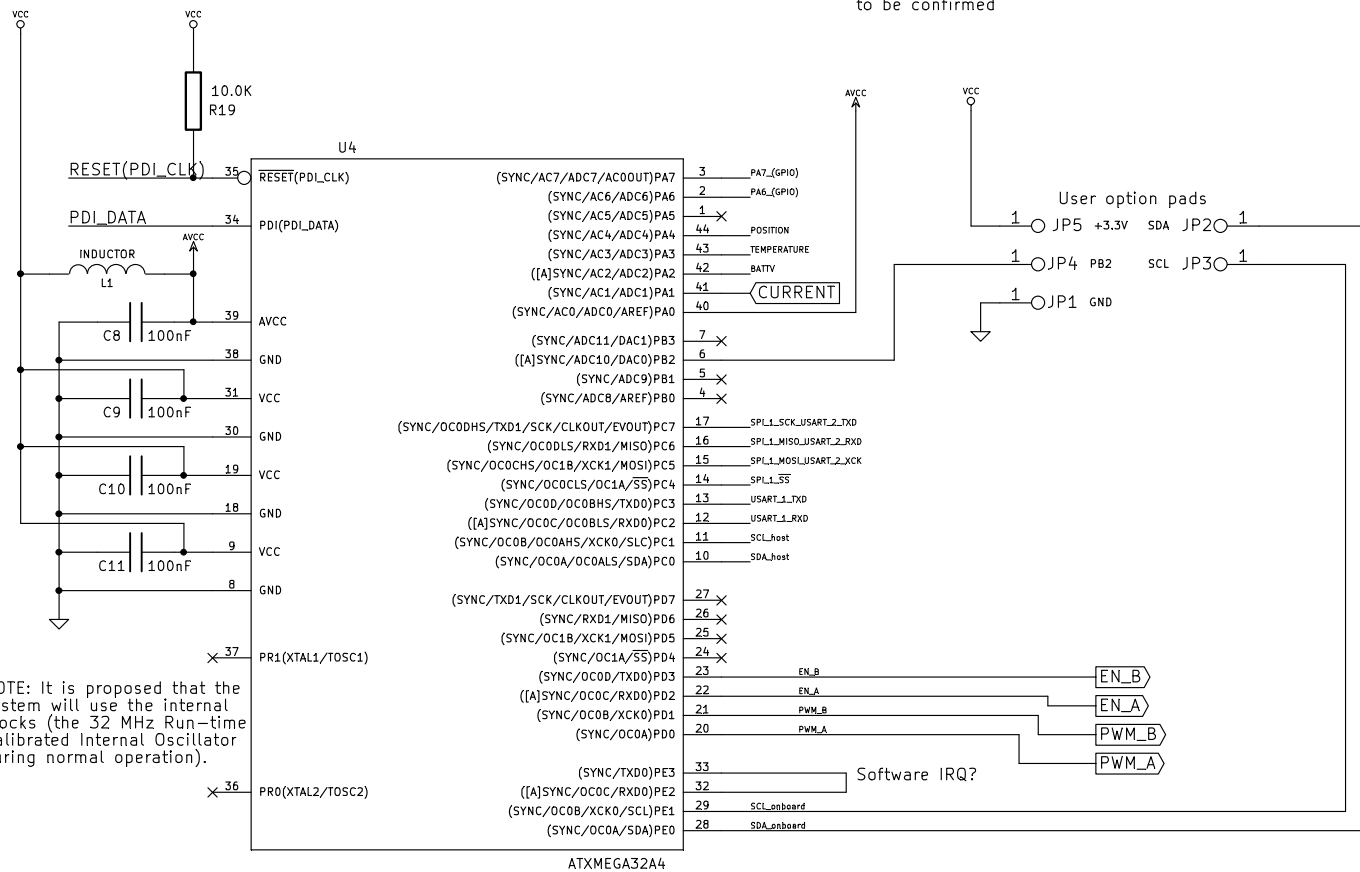
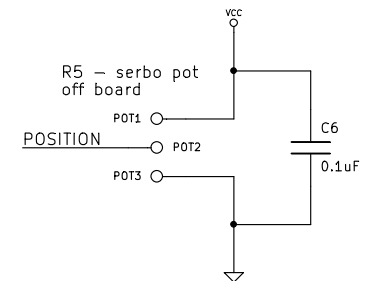
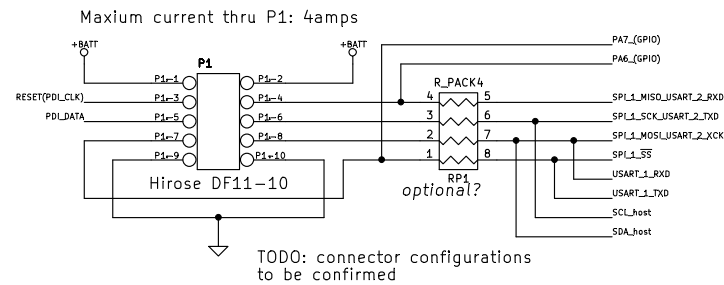
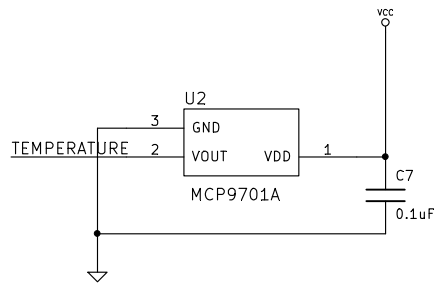
Sheet: OSv4_H-bridge

H-bridge

File: OSv4_H-bridge.sch

File: OSv4.sch		
Sheet: /		
Title: OpenServo v4 preliminary work-up, © OpenServo project 2010		
Size: A4	Date: 4 may 2010	Rev:
KiCad E.D.A.	eeschema (20100406 SVN-R2508)-final	Id: 1/3

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NOTE: Decoupling capacitors to be placed close to the device for each supply pin pair in a signal group.

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KiCad E.D.A.	eeschema (20100406 SVN-R2508)-final
	Rev: 0.0
	Id: 2/3

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TODO: Do we need to Reinststate R9, R10, R13 and R14? If yes, then they should be an RPACK to save PCB space.

TODO: What value is needed for the protection diode D2?

TODO: R1 and R3 need adjustment for V4.

Potential MOSFETs for Q1/Q4:
 SP8M4
 FDS8858CZ
 FDS9934C??

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TODO: What value is needed for the protection diode D2?

TODO: R1 and R3 need
adjustment for V4.

U1

lout

T1009

Potential MOSFETs for Q1/Q4:
SP8M4
FDS8858CZ
FDS9934C??