NOTE: This is a "Work In Progress"; implement at your own risk.

Sheet: OSv4_logic

Logic

File: OSv4_logic.sch

Sheet: OSv4_H-bridge

H-bridge

File: OSv4_H-bridge.sch

NOTE: This is the latest schematic and does not match the older PCB and other items which you may have previously downloaded from the same location as this document- the PCB design is being redone.

Note also that this is most definitely a "Work In Progress" that is in a state of flux... It is presented for review purposes only and does not represent an end product!

File: OSv4.sch		
Sheet: /		
Title: OpenServo v4 preliminary work-up, © OpenServo project 2010		
Size: A4	Date: 8 may 2010	**Rev:**
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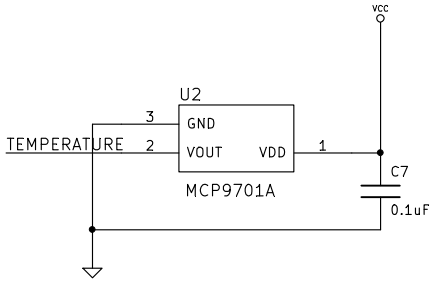
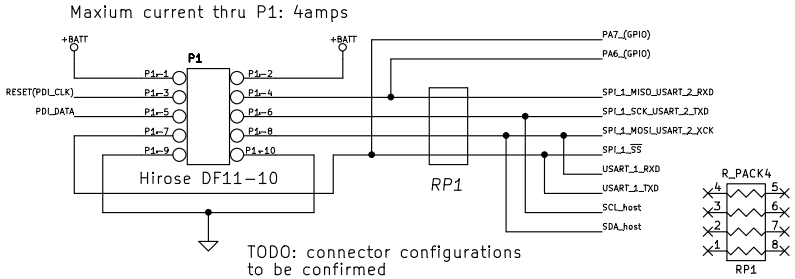
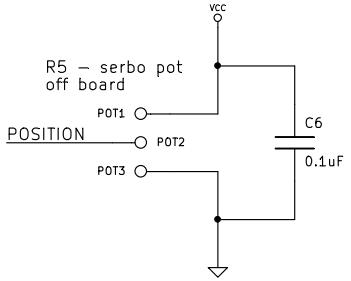
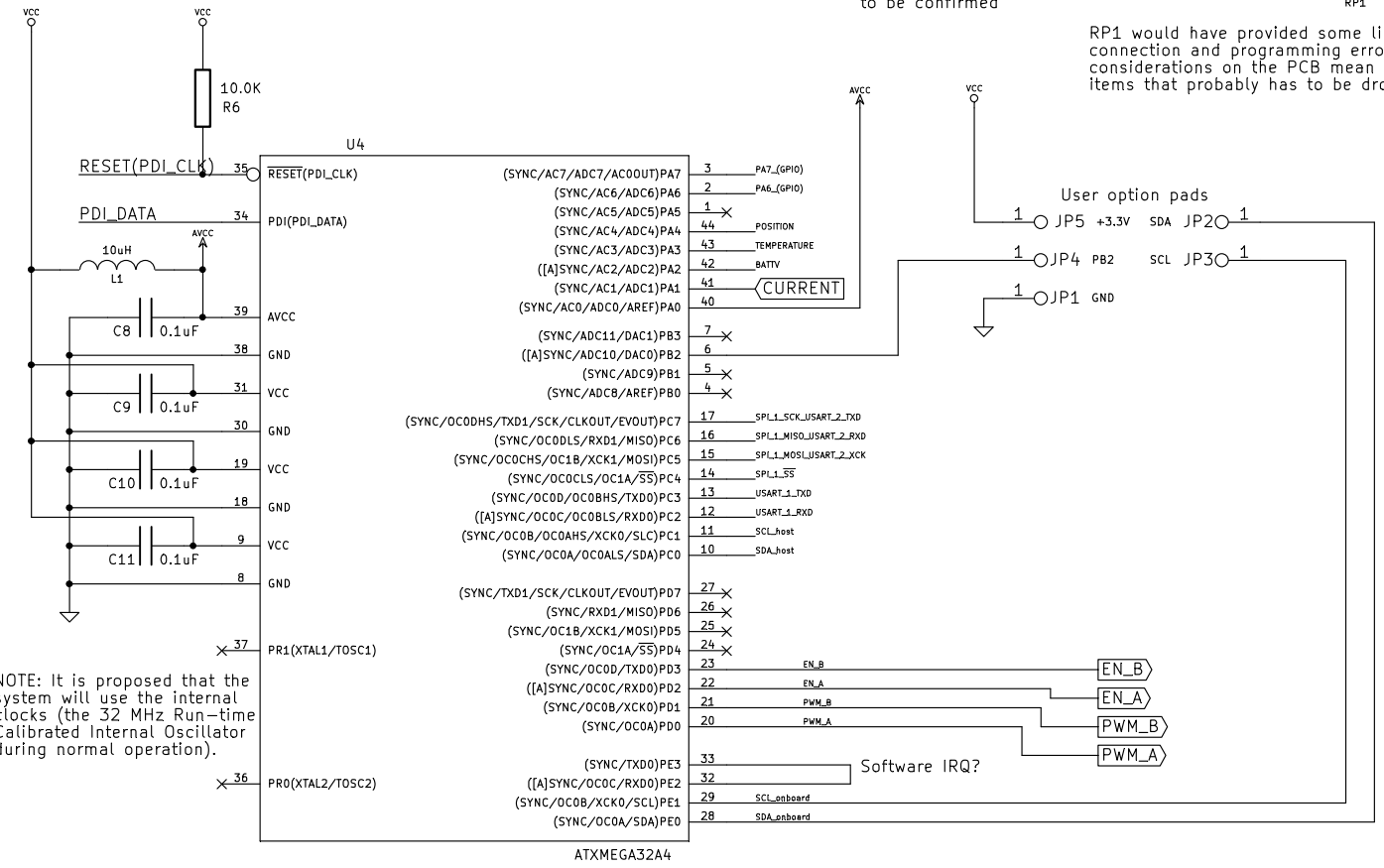
NOTE: This is a "Work In Progress"; implement at your own risk.

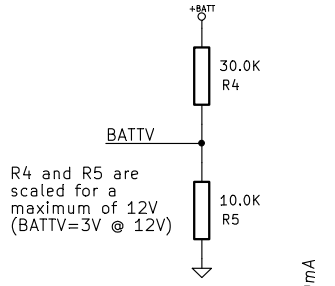


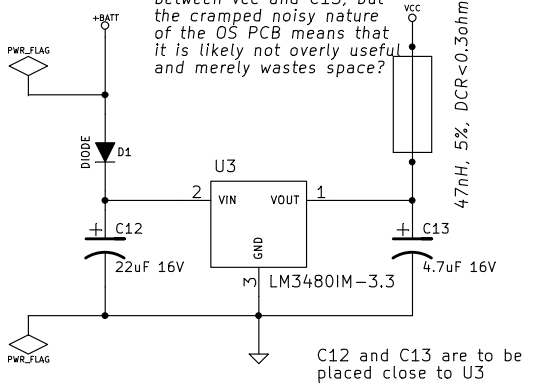




RP1 would have provided some limited protection against connection and programming errors; however design considerations on the PCB mean that this is one of the items that probably has to be dropped from the design.







NOTE: Decoupling capacitors to be placed close to the device for each supply pin pair in a signal group.

File: OSv4_logic.sch		
Sheet: /OSv4_logic/		
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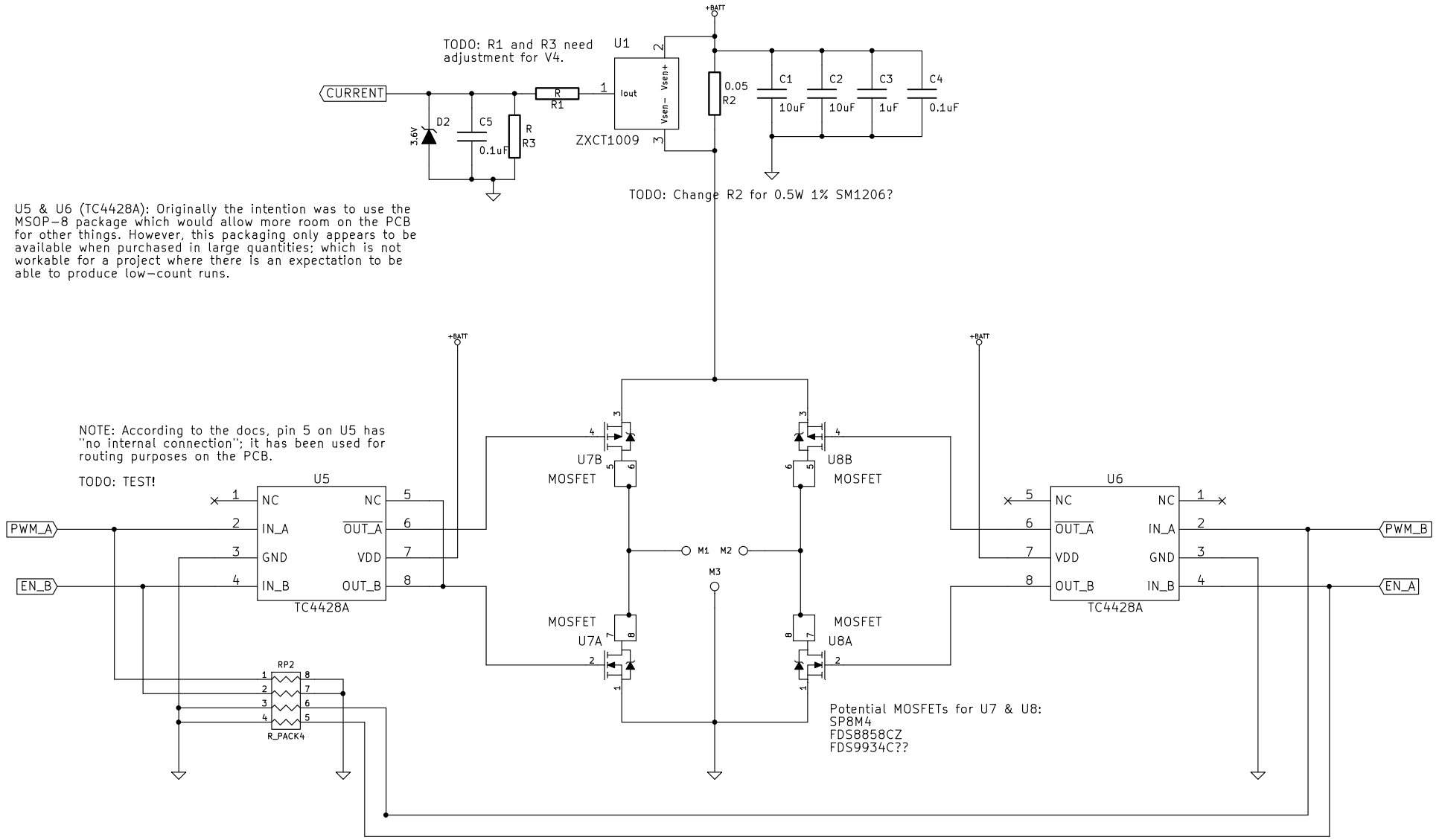

File: OSv4_H-bridge.sch		
Sheet: /OSv4_H-bridge/		
Title: OpenServo v4 preliminary work-up, © OpenServo project 2010		
Size: A4	Date: 8 may 2010	**Rev: 0.0**
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