Mnemonic Symbol	Format						Sample
Bit #	**31..26**	**25..21**	**20..16**	**15..11**	**10..6**	**5..0**	
R-type	**op**	**rs**	**rt**	**rd**	**shamt**	**func**	
add	000000	rs	rt	rd	0	100000	add $1,$2,$3
addu	000000	rs	rt	rd	0	100001	addu $1,$2,$3
sub	000000	rs	rt	rd	0	100010	sub $1,$2,$3
subu	000000	rs	rt	rd	0	100011	subu $1,$2,$3
and	000000	rs	rt	rd	0	100100	and $1,$2,$3
or	000000	rs	rt	rd	0	100101	or $1,$2,$3
xor	000000	rs	rt	rd	0	100110	xor $1,$2,$3
nor	000000	rs	rt	rd	0	100111	nor $1,$2,$3
slt	000000	rs	rt	rd	0	101010	slt $1,$2,$3
sltu	000000	rs	rt	rd	0	101011	sltu $1,$2,$3
sll	000000	0	rt	rd	shamt	000000	sll $1,$2,10
srl	000000	0	rt	rd	shamt	000010	srl $1,$2,10
sra	000000	0	rt	rd	shamt	000011	sra $1,$2,10
sllv	000000	rs	rt	rd	0	000100	sllv $1,$2,$3
srlv	000000	rs	rt	rd	0	000110	srlv $1,$2,$3
srav	000000	rs	rt	rd	0	000111	srav $1,$2,$3
jr	000000	rs	0	0	0	001000	jr $31
Bit #	**31..26**	**25..21**	**20..16**	**15..0**			
I-type	**op**	**rs**	**rt**	**immediate**			
addi	001000	rs	rt	Immediate(- ~ +)			addi $1,$2,100
addiu	001001	rs	rt	Immediate(- ~ +)			addiu $1,$2,100
andi	001100	rs	rt	Immediate(0 ~ +)			andi $1,$2,10
ori	001101	rs	rt	Immediate(0 ~ +)			andi $1,$2,10
xori	001110	rs	rt	Immediate(0 ~ +)			andi $1,$2,10
lw	100011	rs	rt	Immediate(- ~ +)			lw $1,10($2)
sw	101011	rs	rt	Immediate(- ~ +)			sw $1,10($2)
beq	000100	rs	rt	Immediate(- ~ +)			beq $1,$2,10
bne	000101	rs	rt	Immediate(- ~ +)			bne $1,$2,10
slti	001010	rs	rt	Immediate(- ~ +)			slti $1,$2,10
sltiu	001011	rs	rt	Immediate(- ~ +)			sltiu $1,$2,10
lui	001111	00000	rt	Immediate(- ~ +)			Lui $1, 10
Bit #	**31..26**	**25..0**					
J-type	**op**	**Index**					
j	000010	address					j 10000
jal	000011	address					jal 10000