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## I2C address(7bit):

### 1.I2C0 POWER:

RK808 0x1b  
CW2013 0x62  
BQ24296 0x6b  
BQ27230 0x55

### 2.I2C1 Sensor:

LSM330TR G:0x6a A:0x1e  
MPU6050C 0x68  
CM3218 0x10,0x0c  
MMA8452Q 0X1d  
LIS3DH 0X19  
LSM303D 0X1d

### 3.I2C2 Audio Codec:

ES8323 0x10

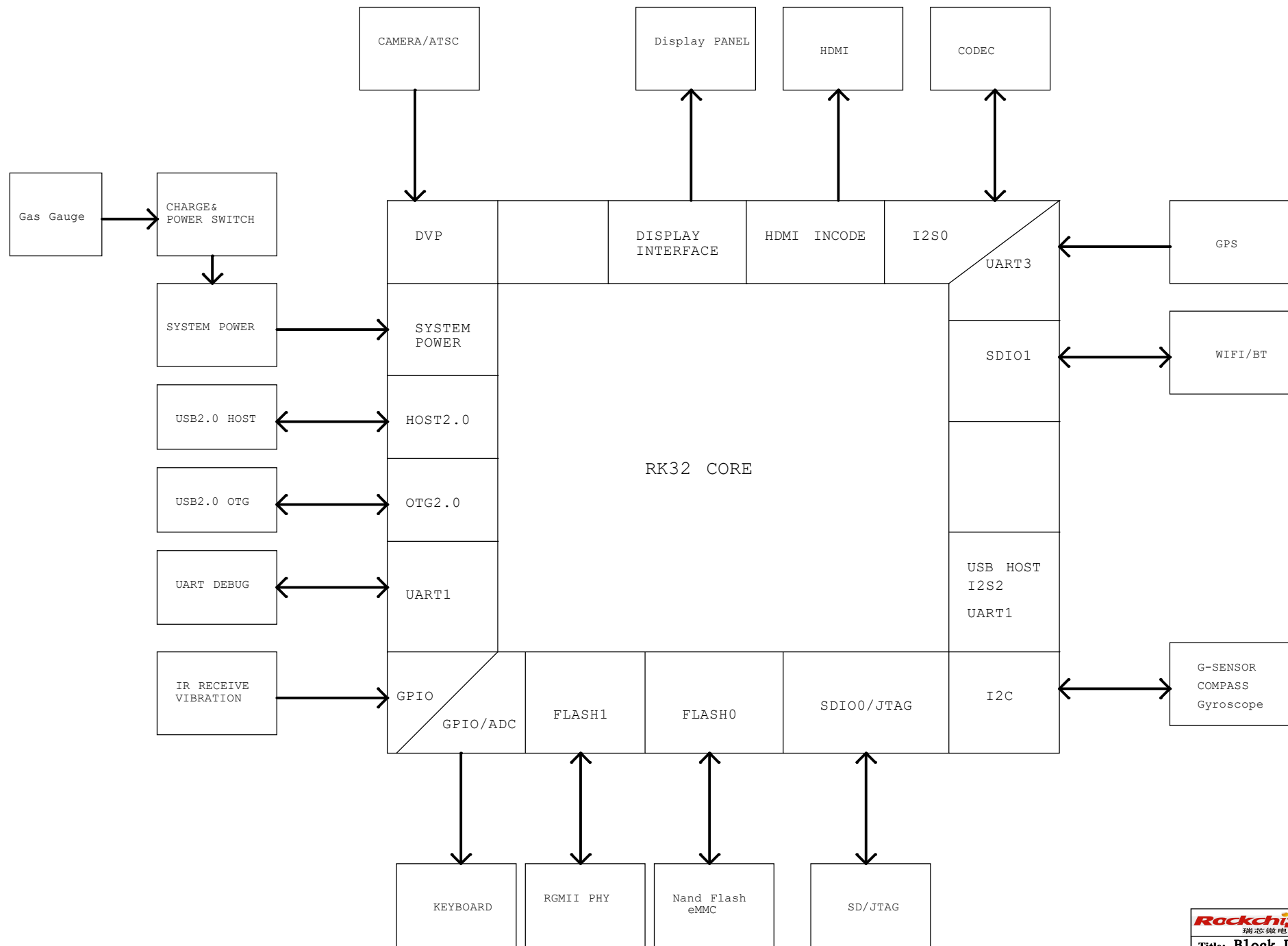
### 4.I2C3 Camera:

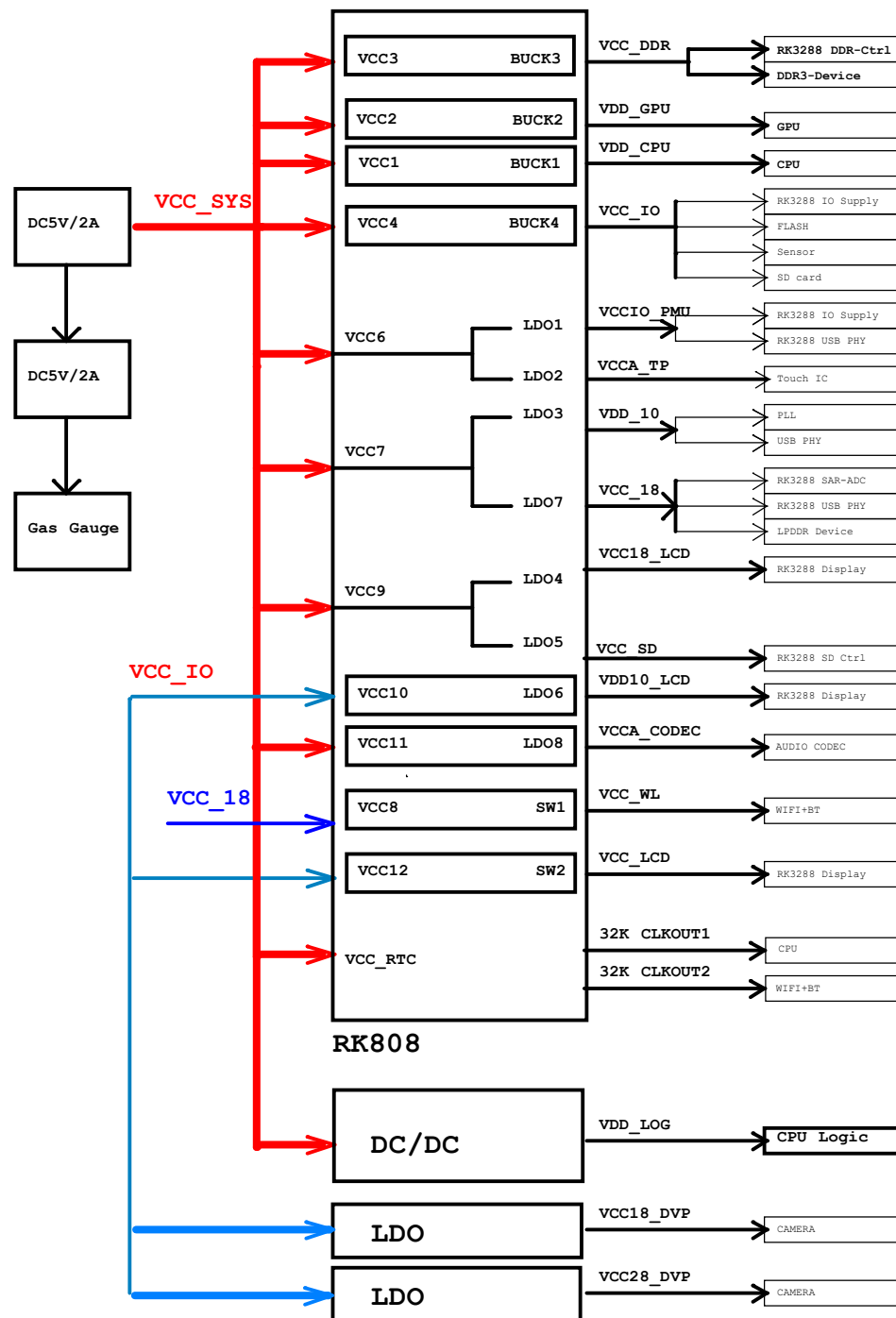
OV2659 0x30  
OV8825 0x36

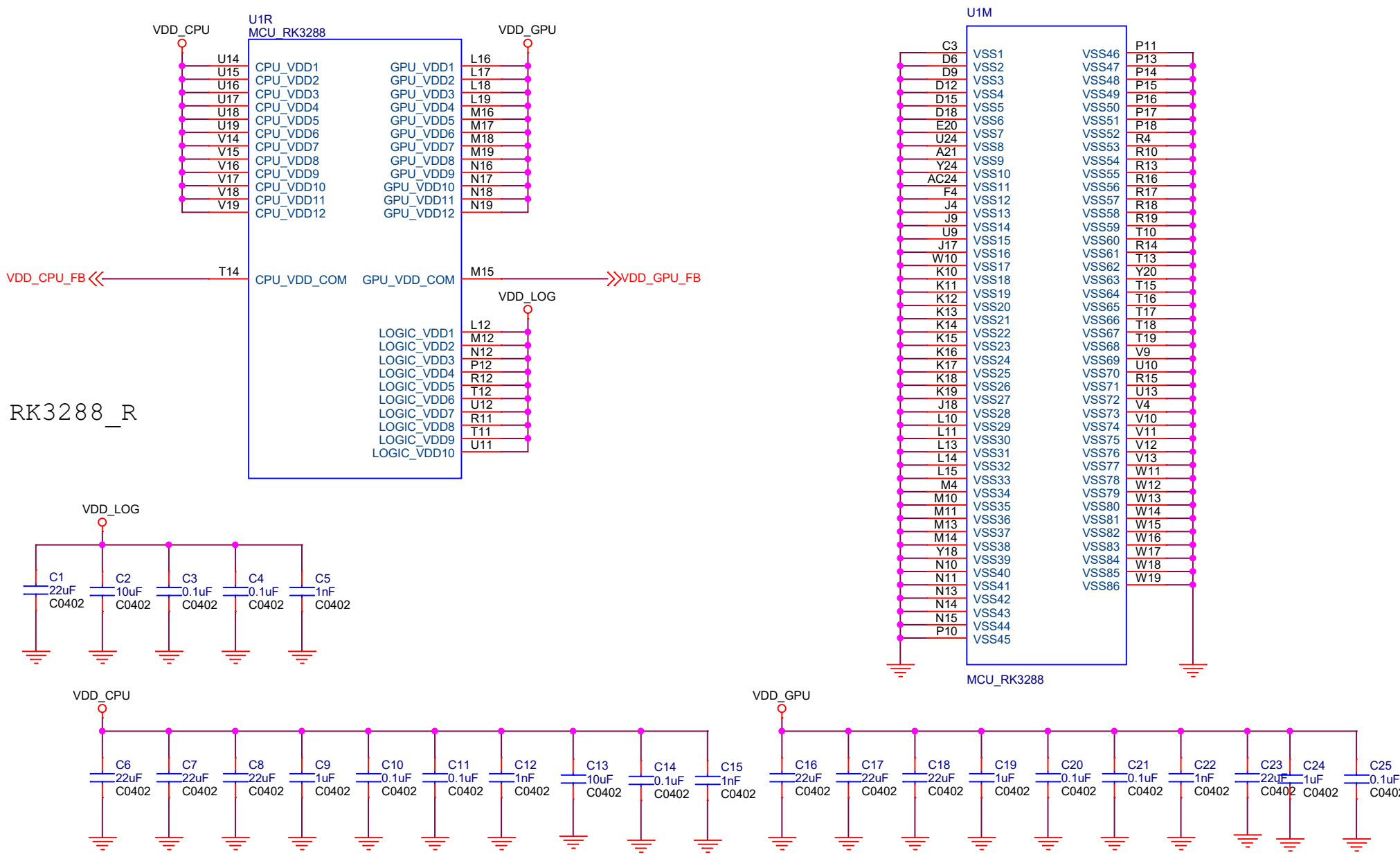
### 5.I2C4 Touch:

CT363 0x1b



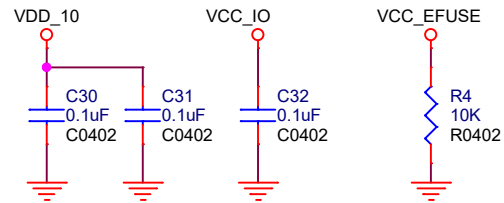
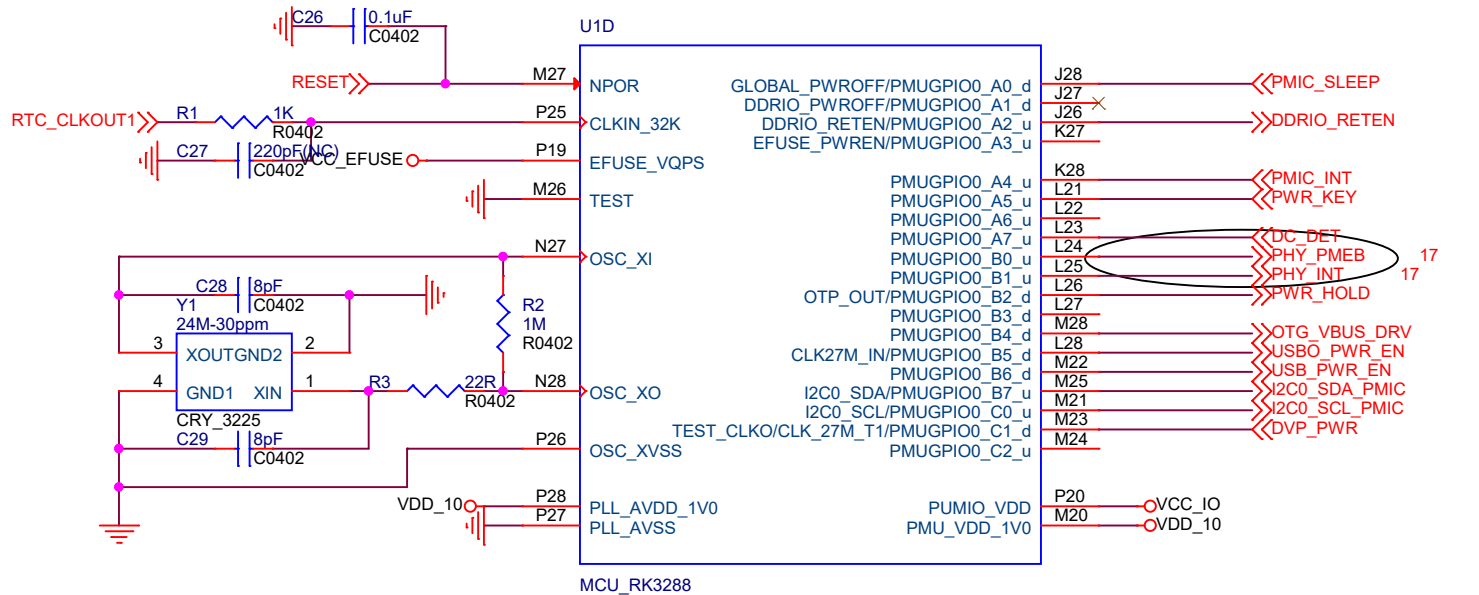




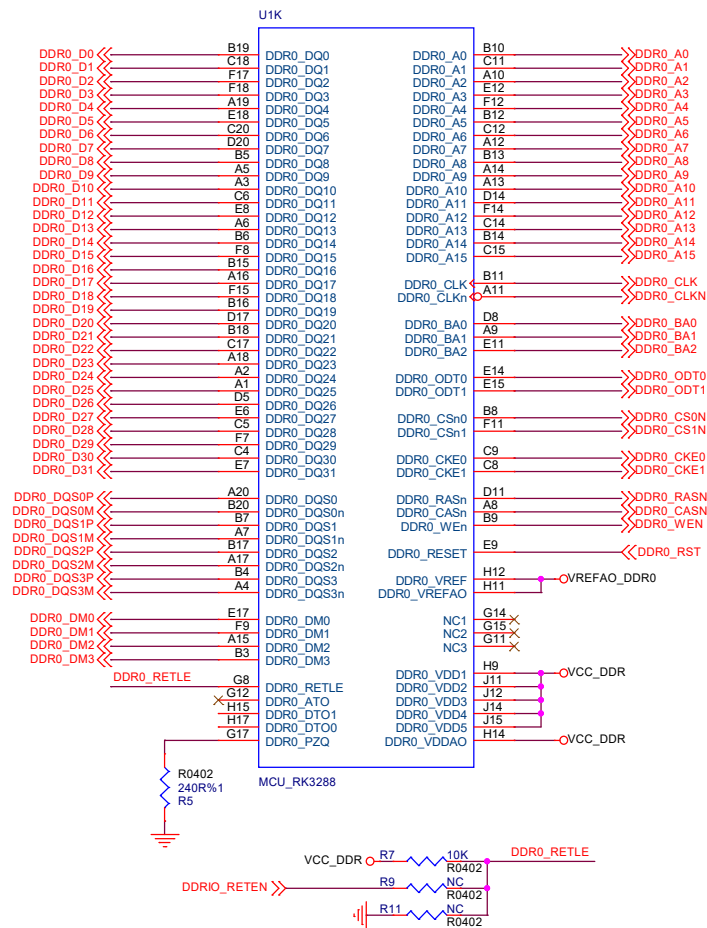


RK3288\_R

RK3288 FILTER

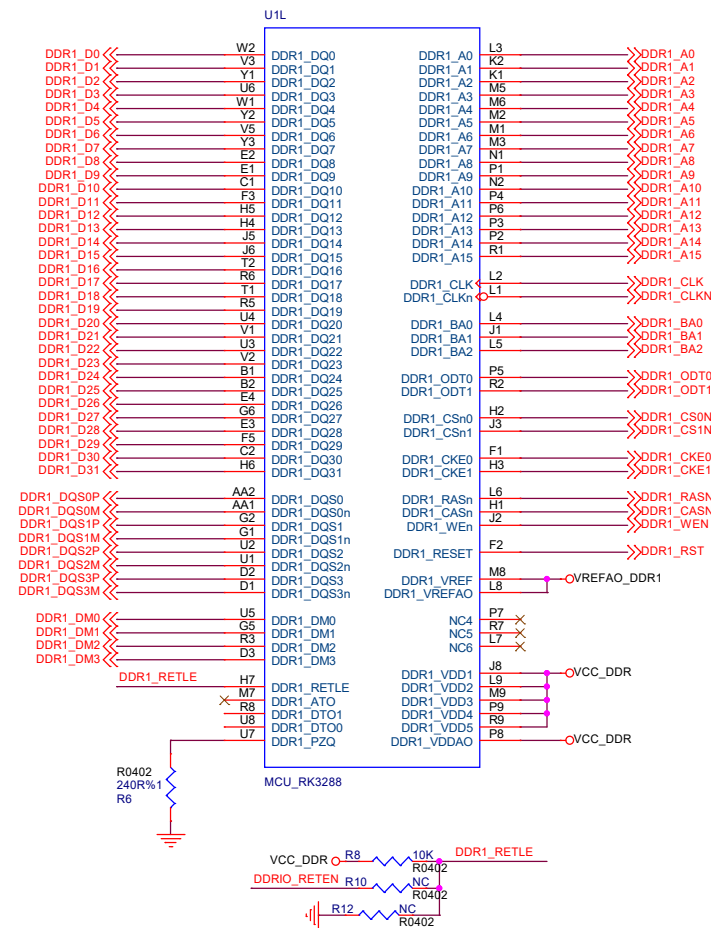


# RK3288 DDR Channel-0



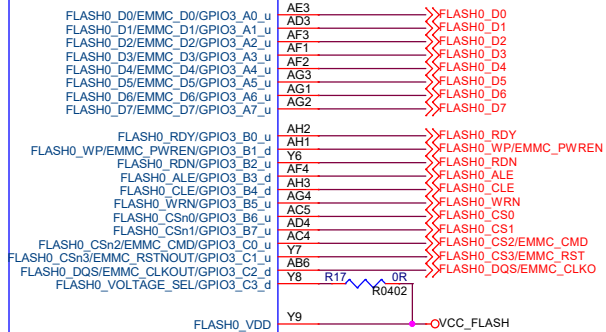
RK3288\_K

# RK3288 DDR Channel-1

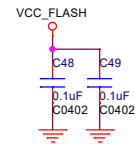
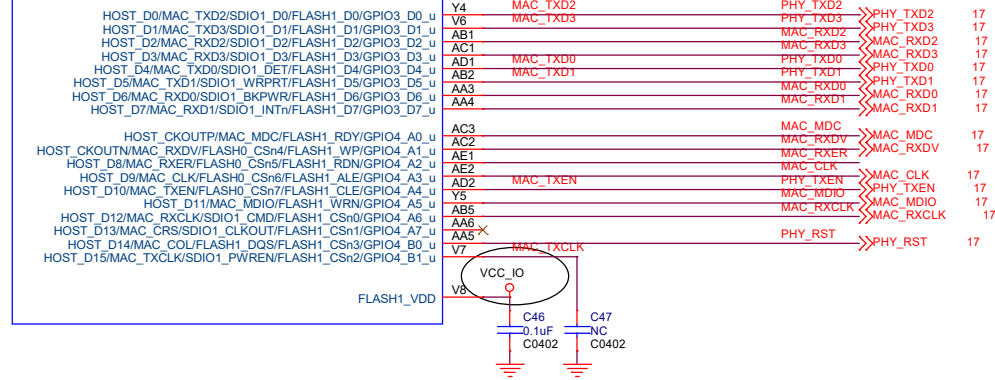


RK3288\_L

U1P  
MCU RK3288

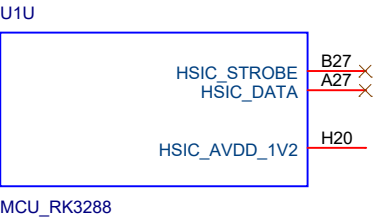
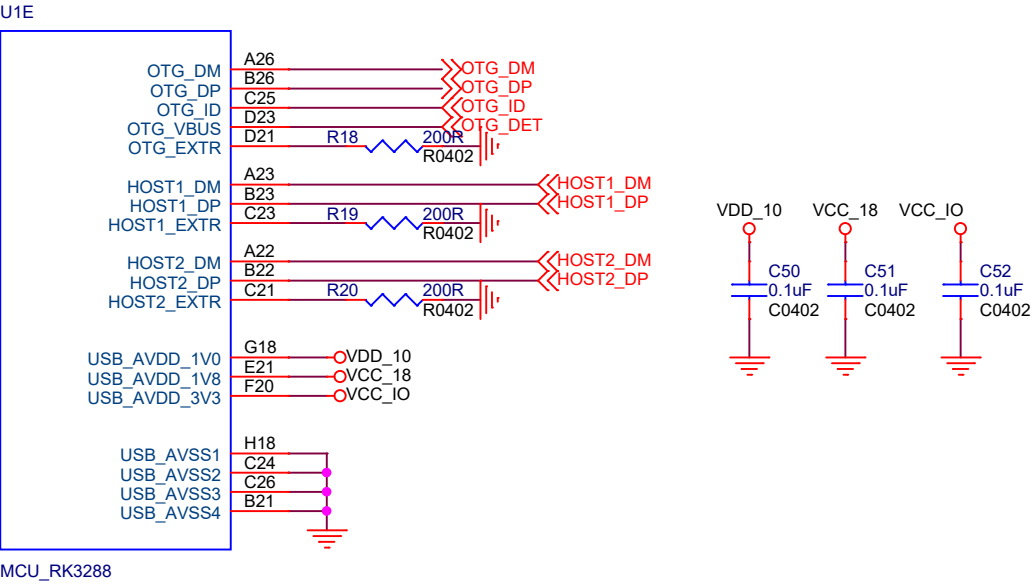


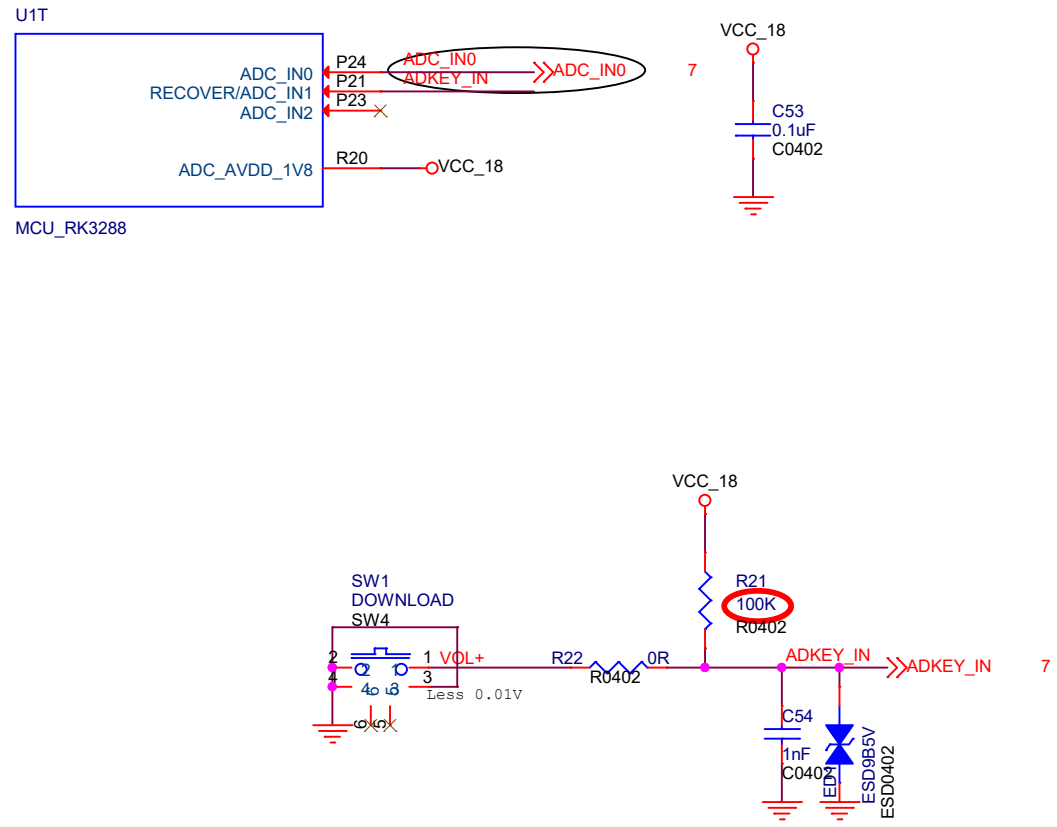
U1Q  
MCU RK3288




Note: VCC\_IO 3.0V (固定为3.0V) ;  
VCC\_FLASH (可能为1.8V 或 3V  
控制的FLASH管脚相连) .

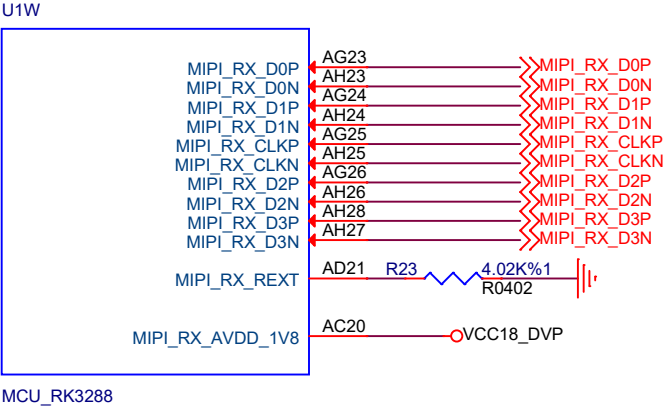
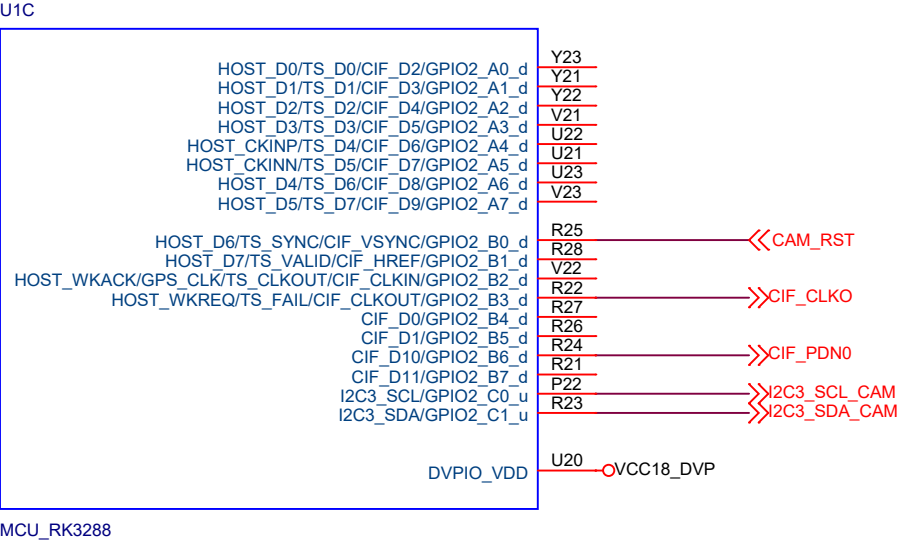


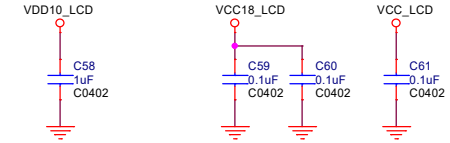
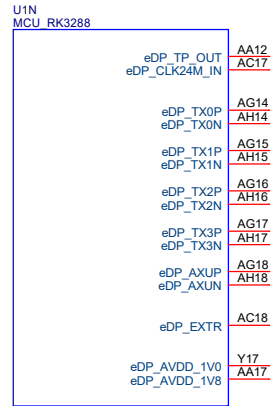
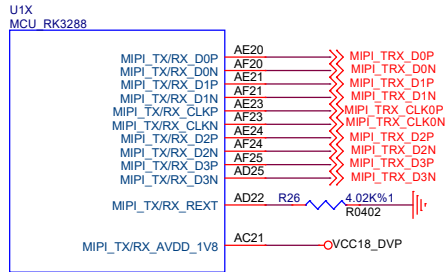
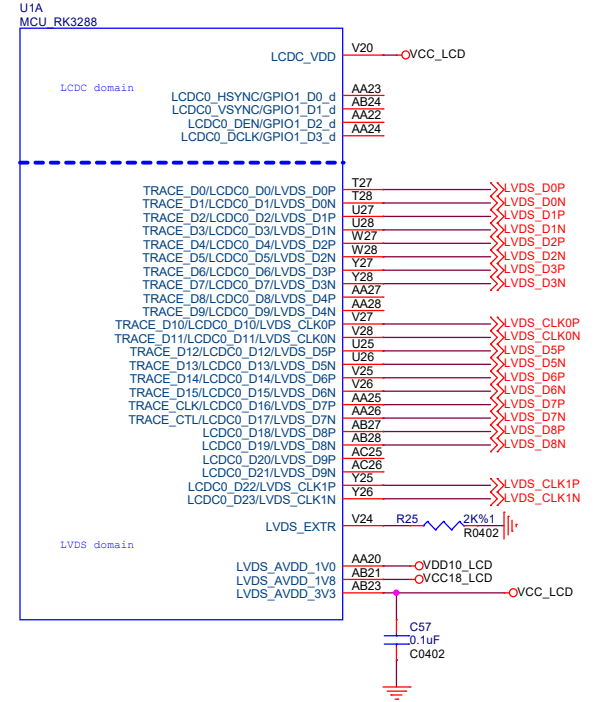
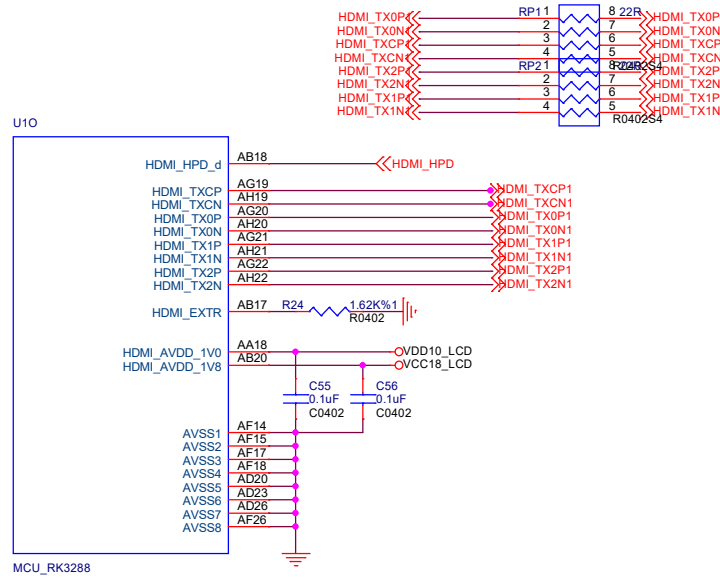
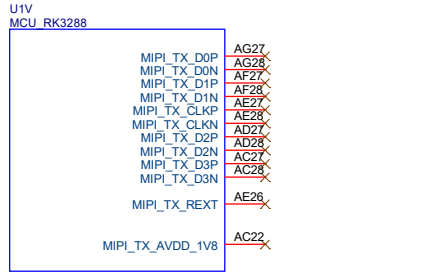




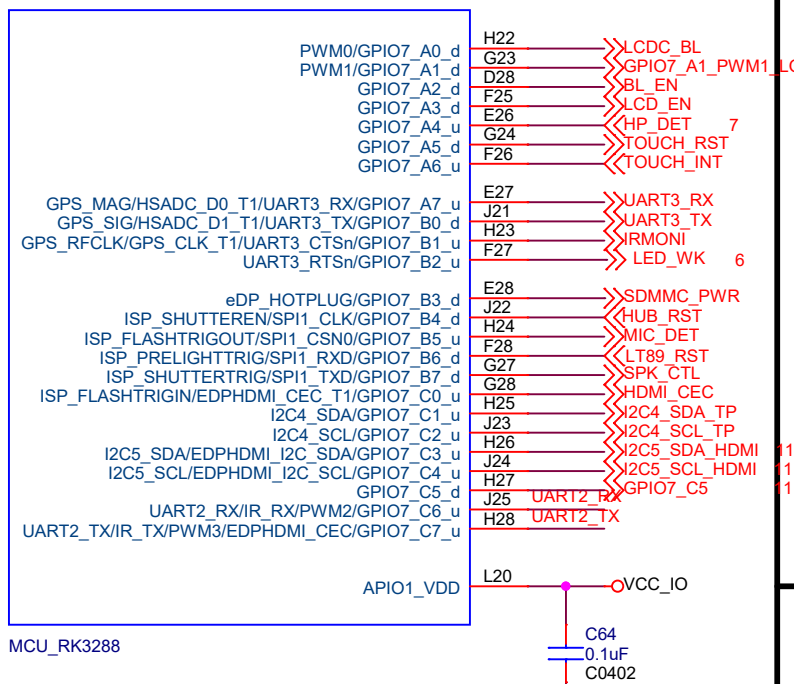
KEY BAORD

 瑞芯微电子		福州瑞芯微电子	
Title: RK3288 SARADC/Key Board			
File: RK3288_V10			REV: V0.1
Create Date: Wednesday, February 12, 2014		Page Num: 10	
Modify Date: Tuesday, May 07, 2019		Page Total: 34	





U1F



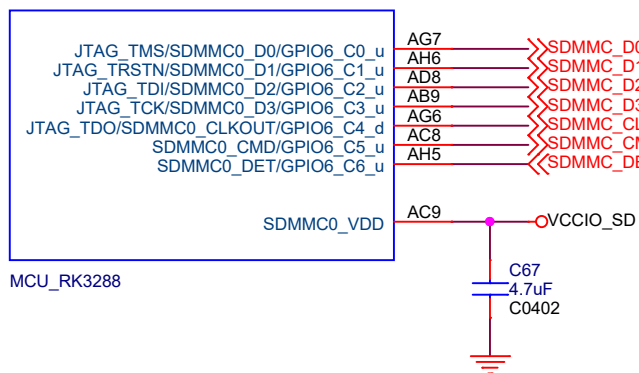
MCU\_RK3288

UART1\_RX 13  
UART1\_TX 13  
UART1\_RTS 13  
UART1\_CTS 13

UART3\_RX 13  
UART3\_TX 13

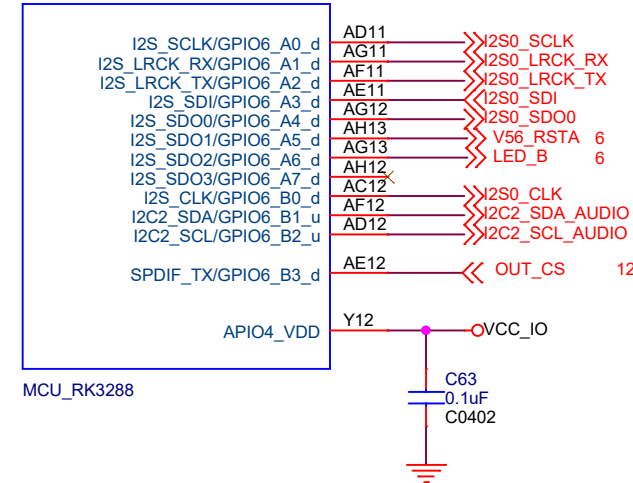
UART2\_RX 12,13,14  
UART2\_TX 12,13,14

U1B



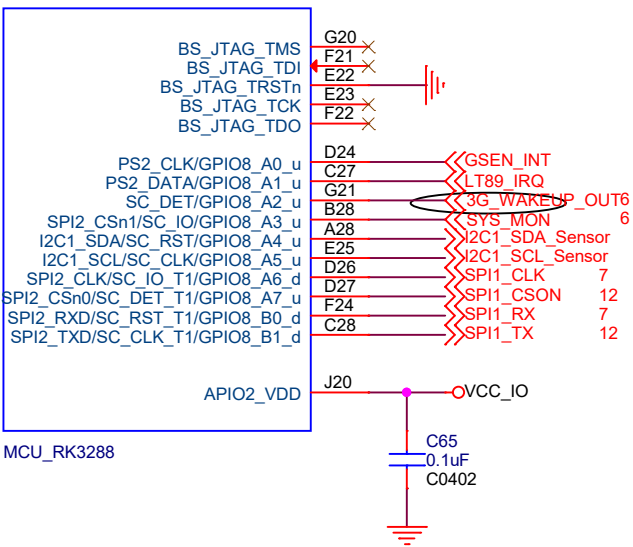
MCU\_RK3288

LOGU1I



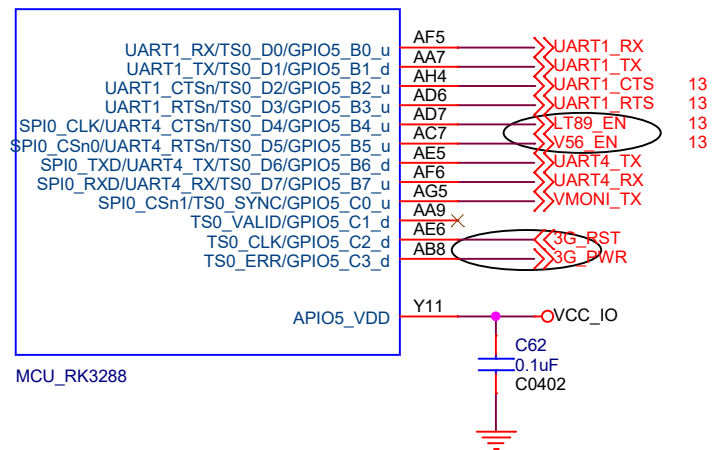
MCU\_RK3288

U1G



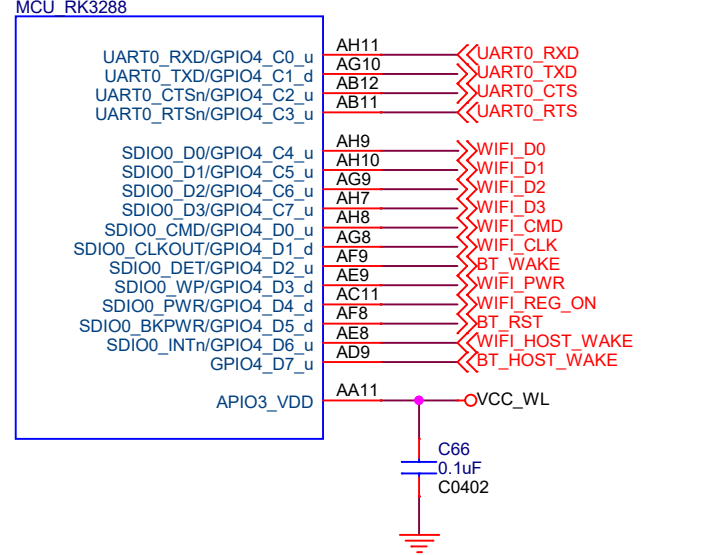
MCU\_RK3288

U1J



MCU\_RK3288

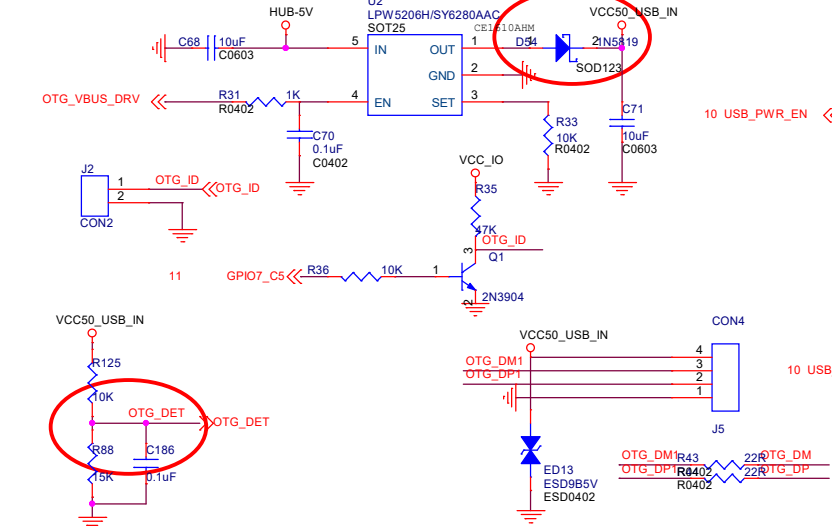
U1H



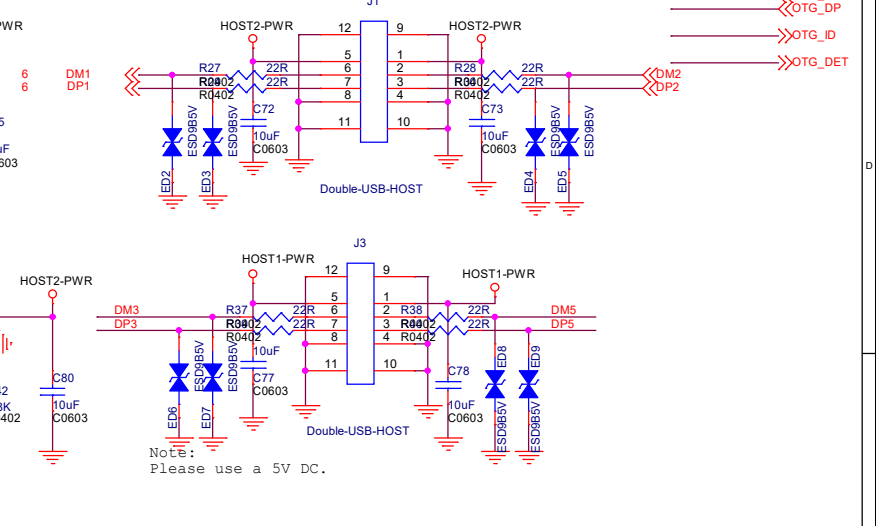
MCU\_RK3288

 瑞芯微电子		福州瑞芯微电子	
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File: RK3288_V10		REV: V0.1	
Create Date: Monday, December 30, 2013		Page Num: 13	
Modify Date: Thursday, May 09, 2019		Page Total: 34	

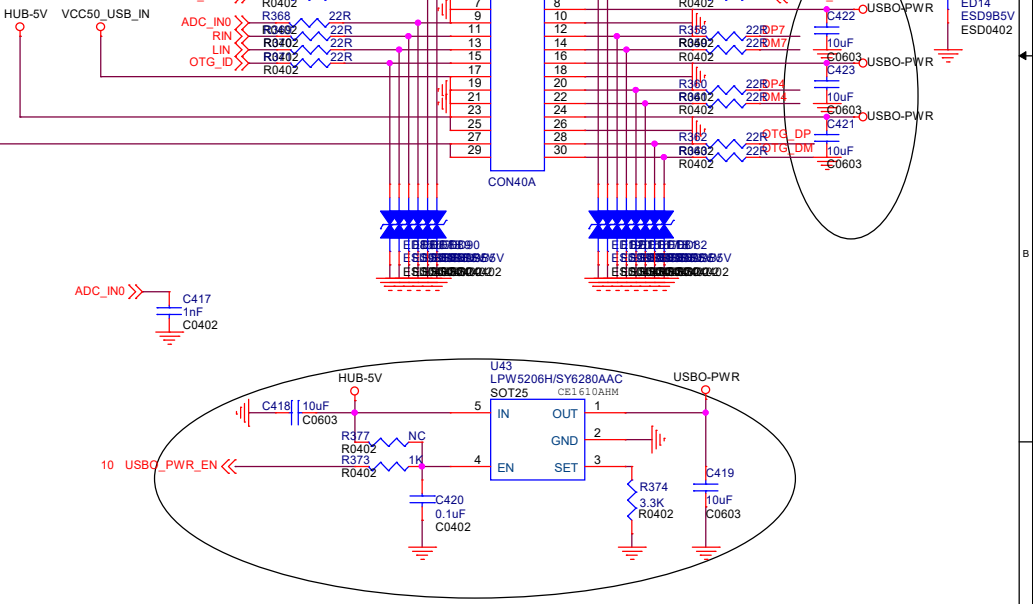
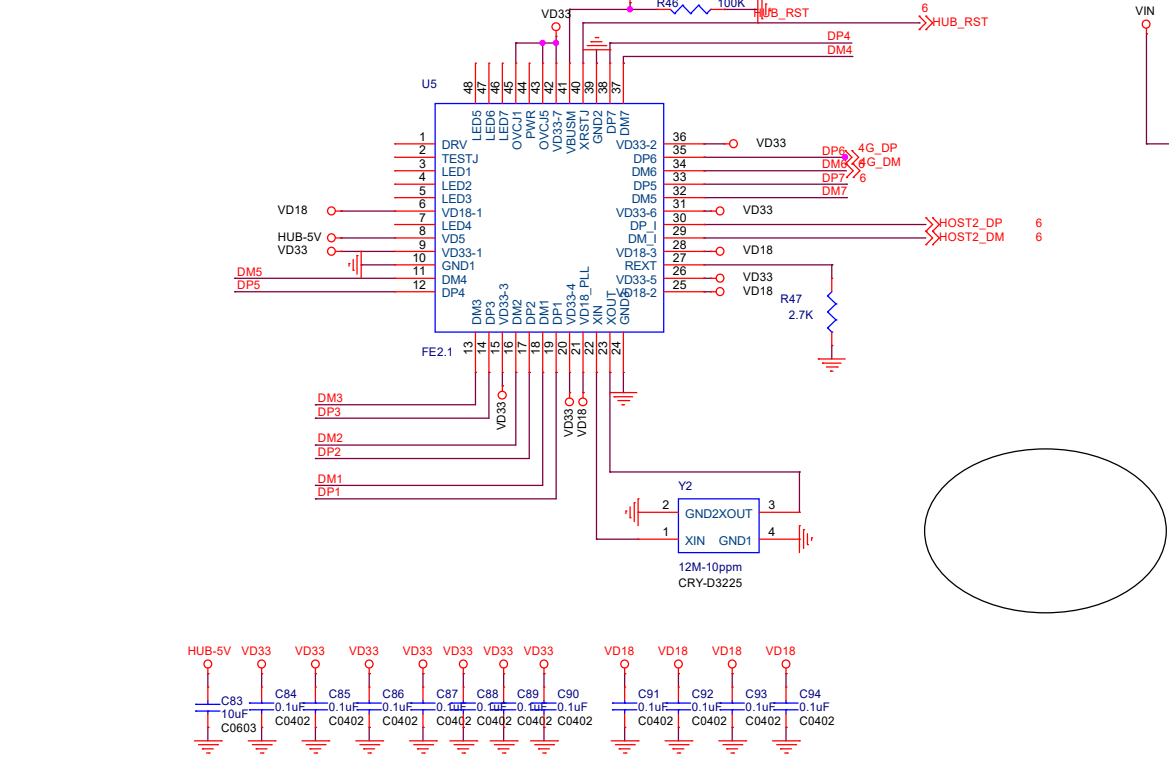
HOST And Device Switch



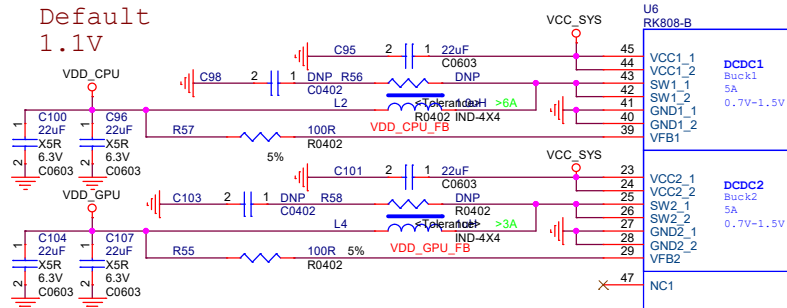
USB OTG CONNECTOR



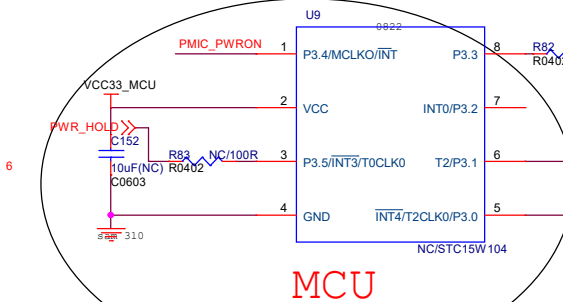
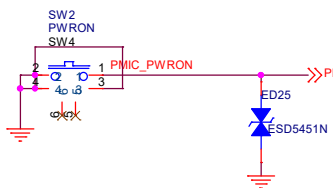
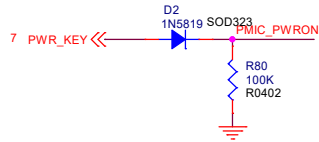
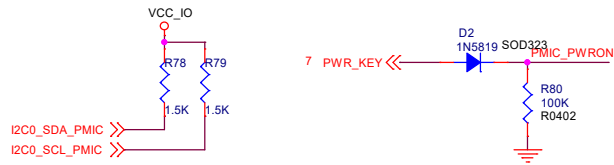
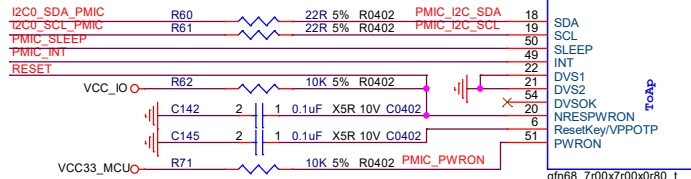
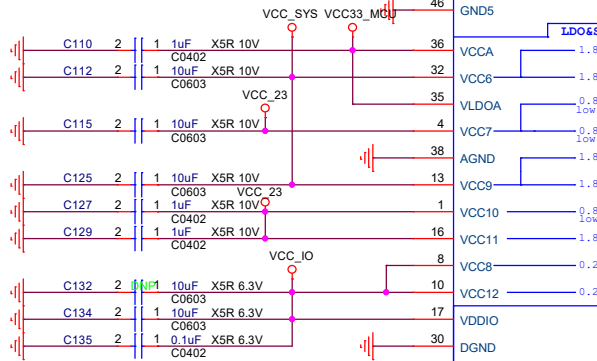
USB HUB



Default  
1.1V



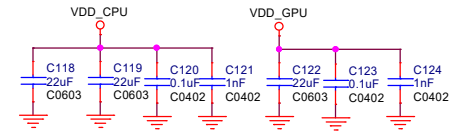
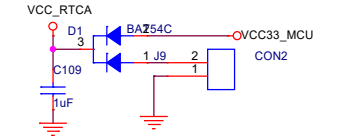
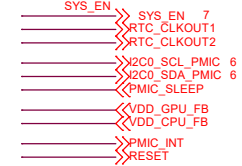
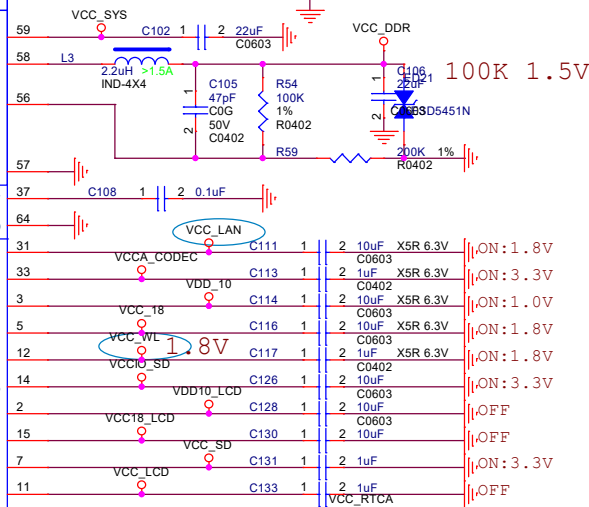
Default  
1.1V



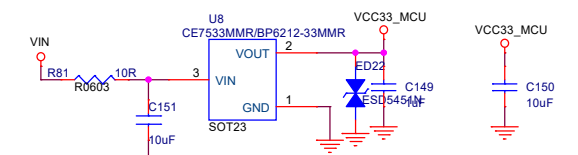
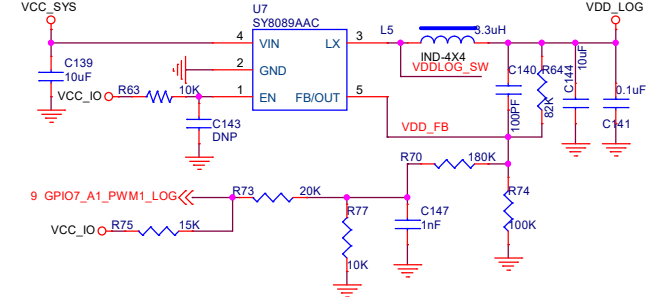
MCU

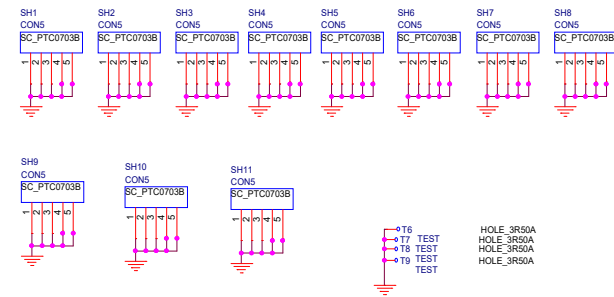
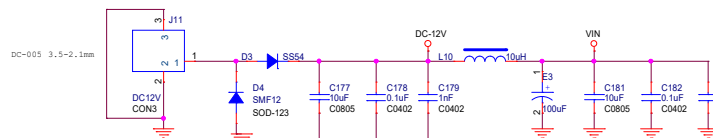
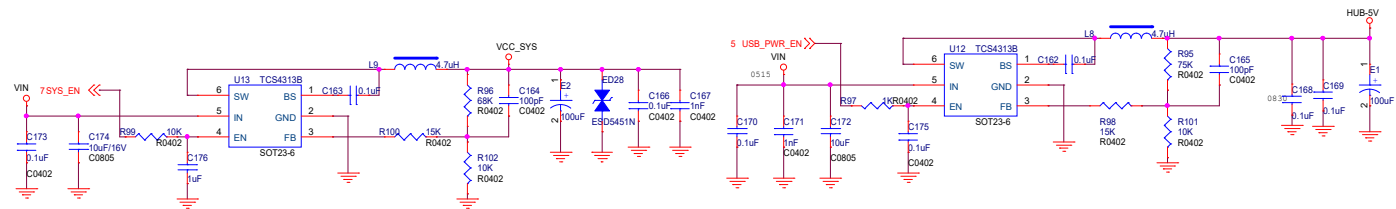
DDR Type	OutPut voltage	R2261	R2256
DDR3	1.5V	100K	200K
DDR3L	1.358V	43K	120K
LPDDR2/3	1.25V	49.9K	200K

Default  
3.3V

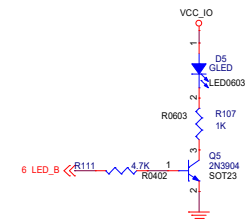
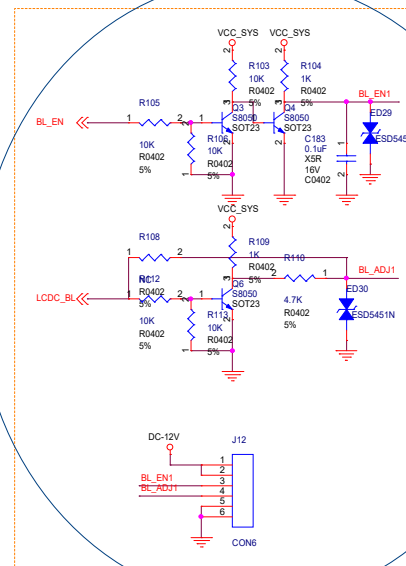
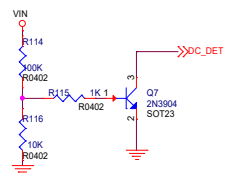


LOG POWER 0.86V-1.36V  
default:1.0V



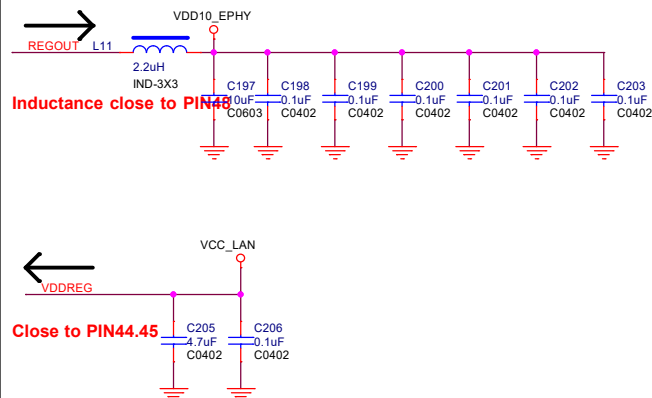
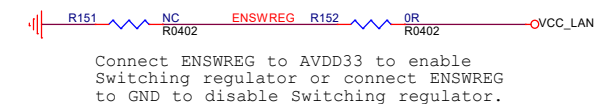
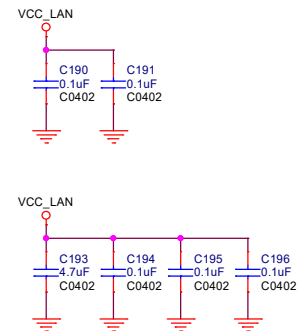
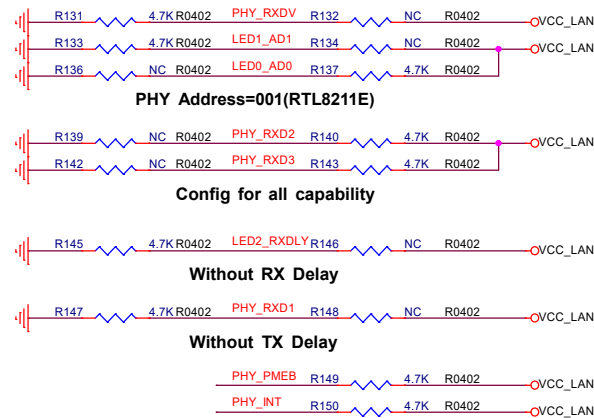
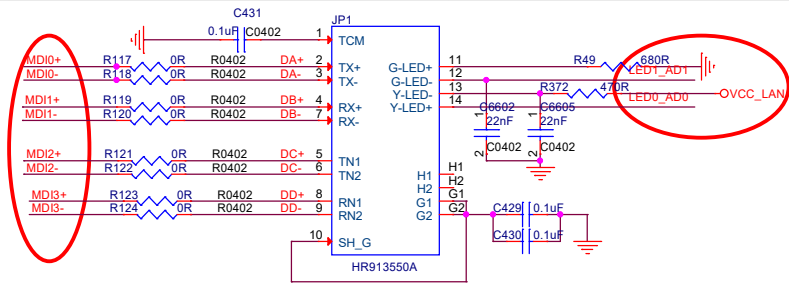
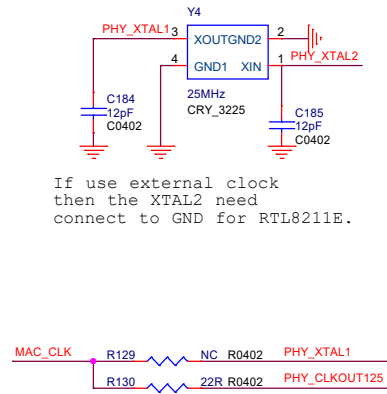
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DC IN



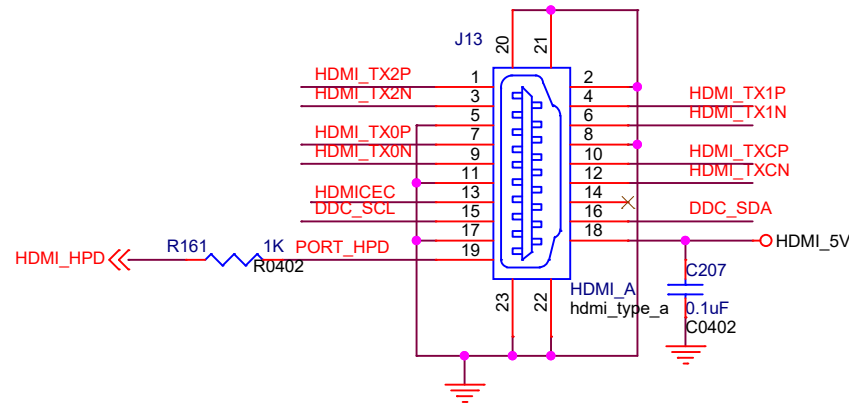
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Date:	Friday, May 10, 2019		Sheet 16 of 34



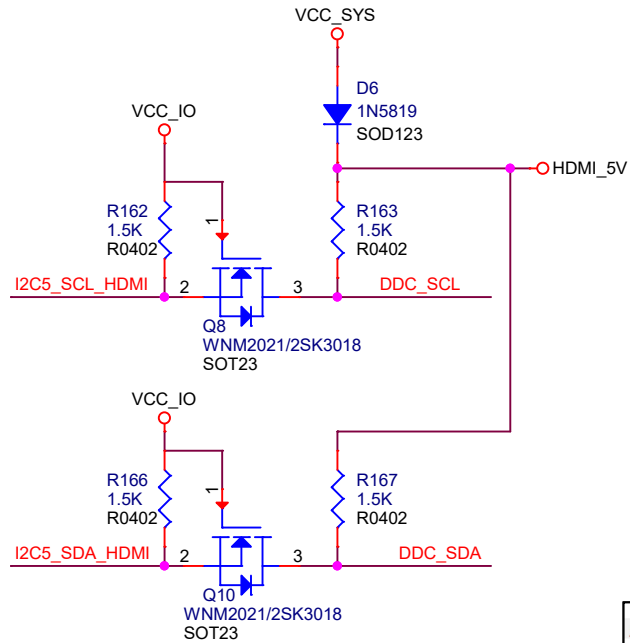
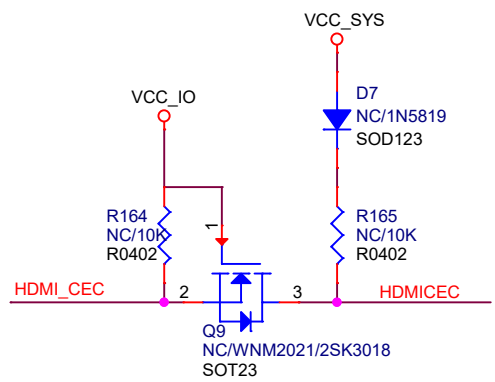
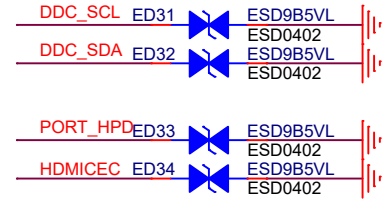
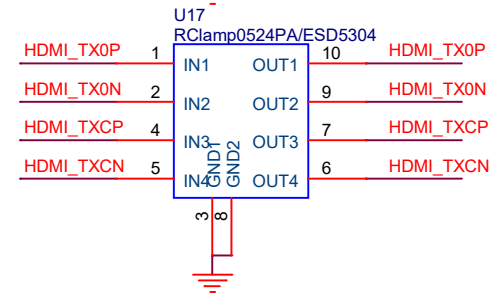
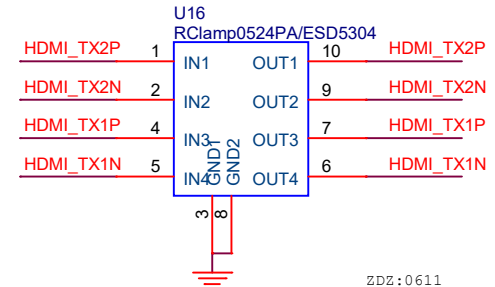


HDMI\_TX0P >>>  
HDMI\_TX0N >>>  
HDMI\_TX1P >>>  
HDMI\_TX1N >>>  
HDMI\_TX2P >>>  
HDMI\_TX2N >>>  
HDMI\_TXCP >>>  
HDMI\_TXCN >>>

HDMI\_CEC <<<  
11 I2C5\_SDA\_HDMI <<< I2C5\_SDA\_HDMI  
11 I2C5\_SCL\_HDMI <<< I2C5\_SCL\_HDMI



## HDMI OUT





福州瑞芯微电子

Title: HDMI Port

File: RK3288\_V10

REV: V0.1

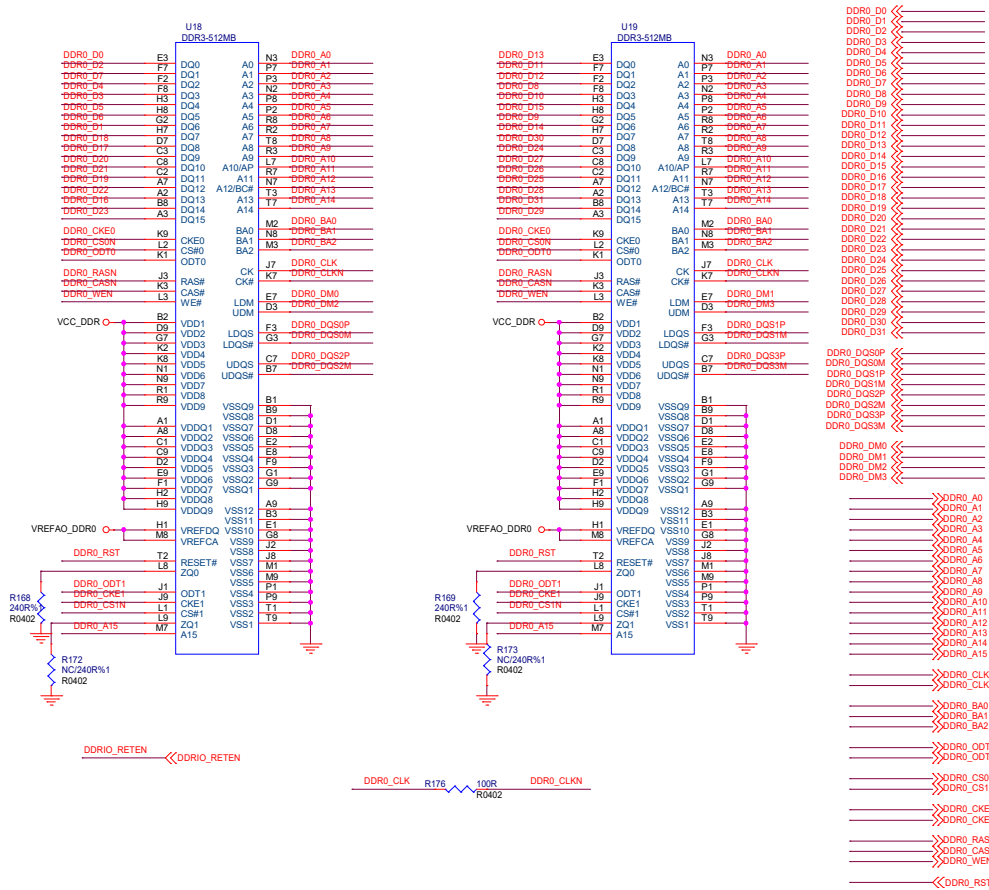
Create Date: Wednesday, April 10, 2019

Page Num: 18

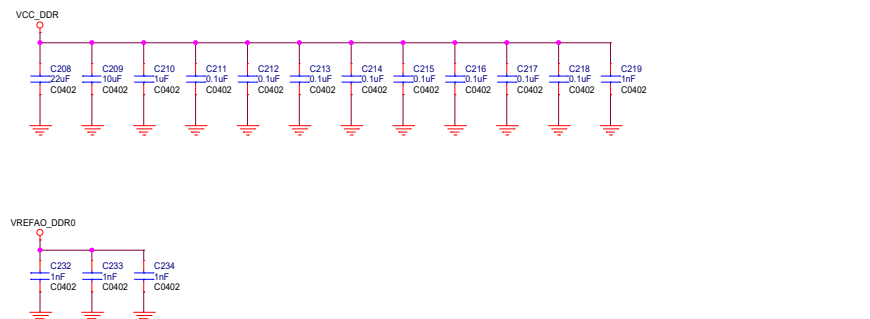
Modify Date: Tuesday, May 07, 2019

Page Total: 34

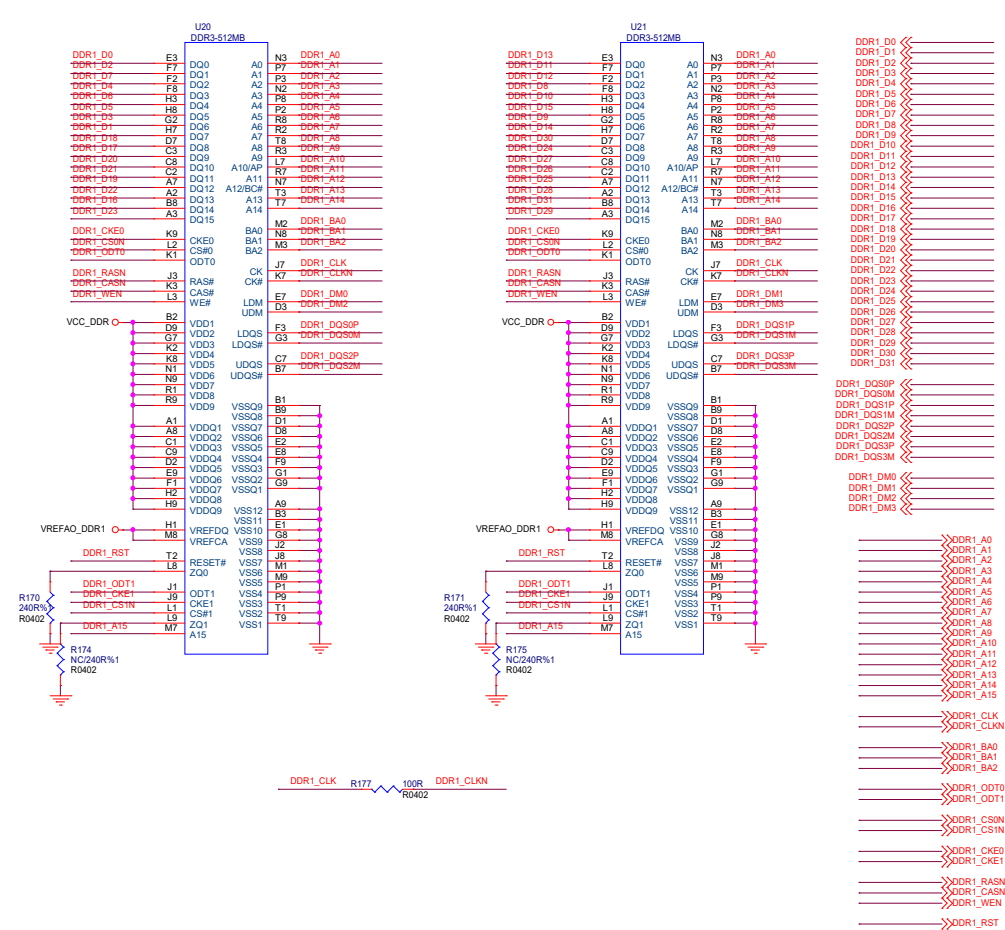
## DDR3 Channel-0



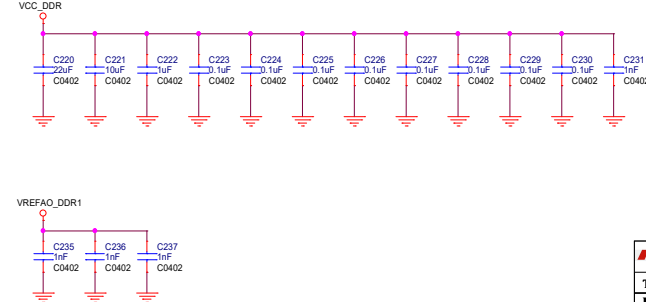
DDR0 FILTER



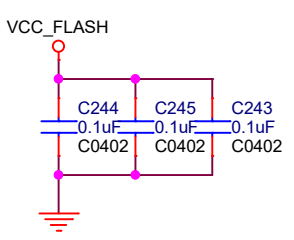
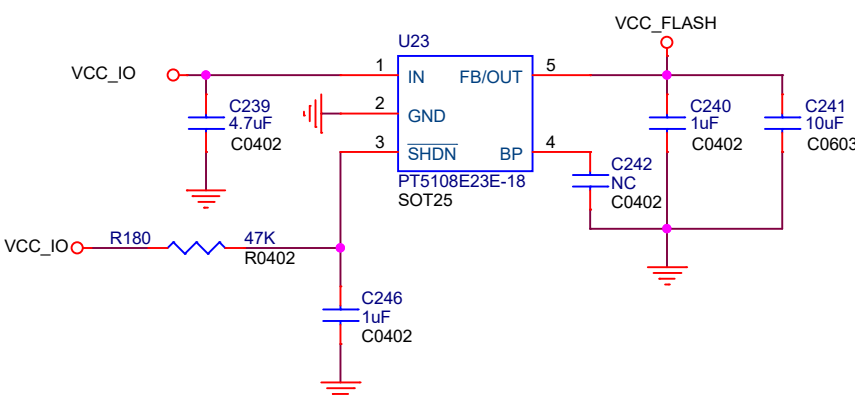
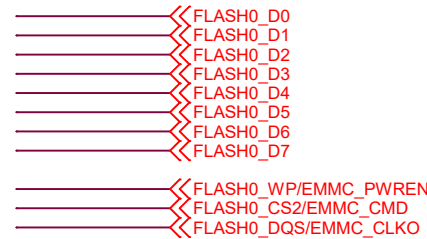
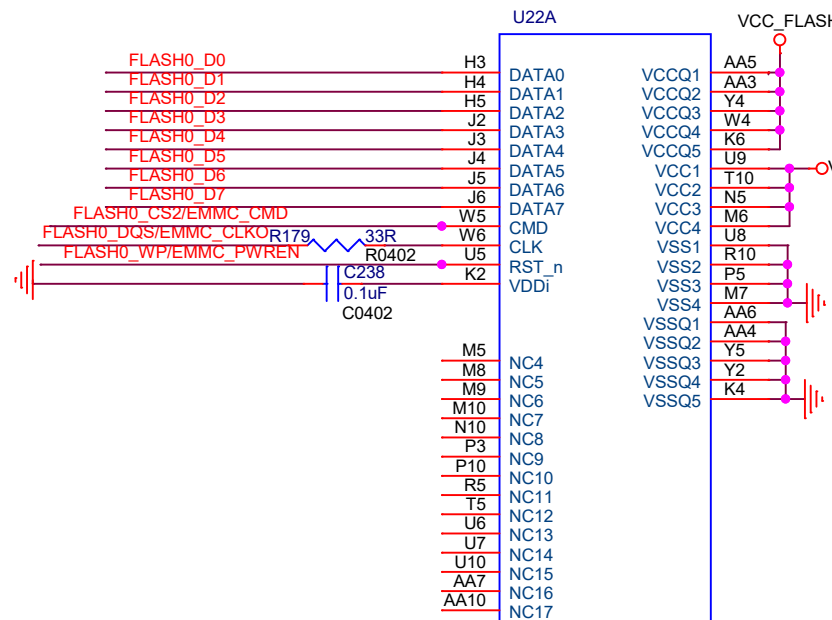
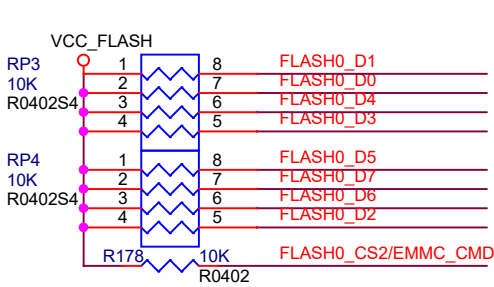
## DDR3 Channel-1



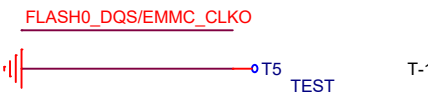
DDR1 FILTER



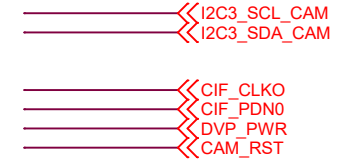
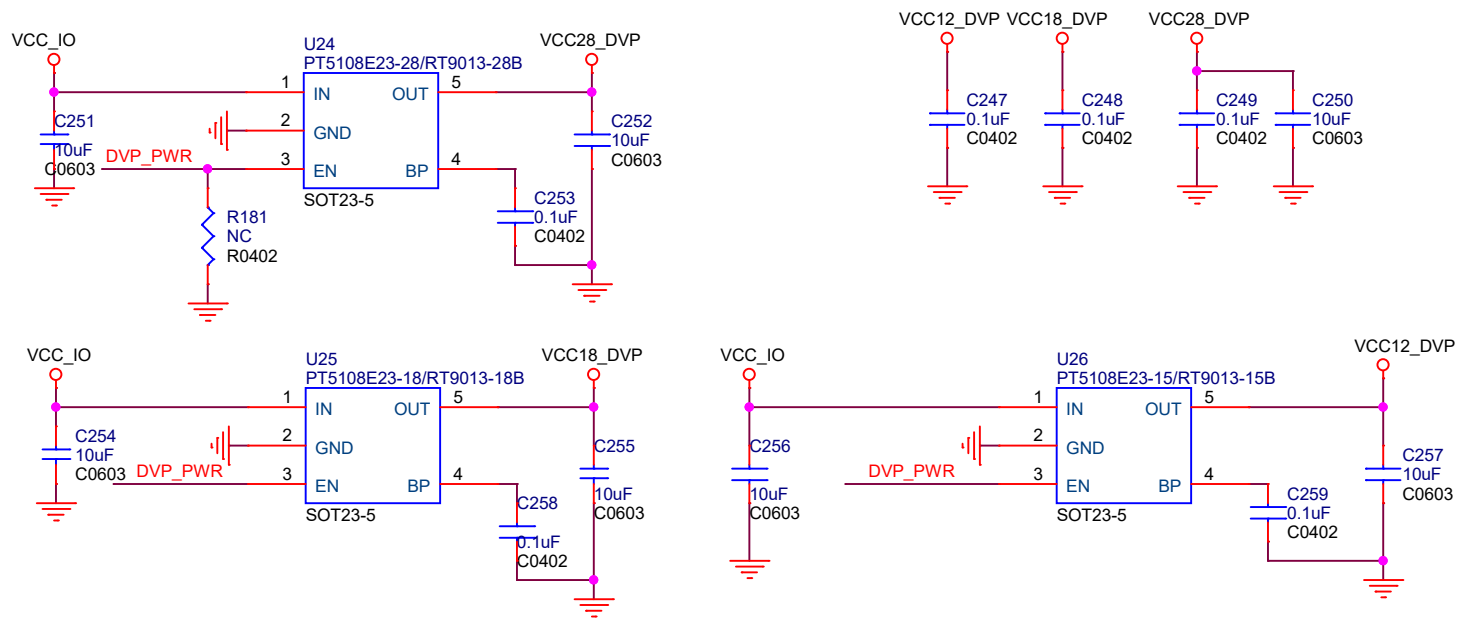
INAND FLASH



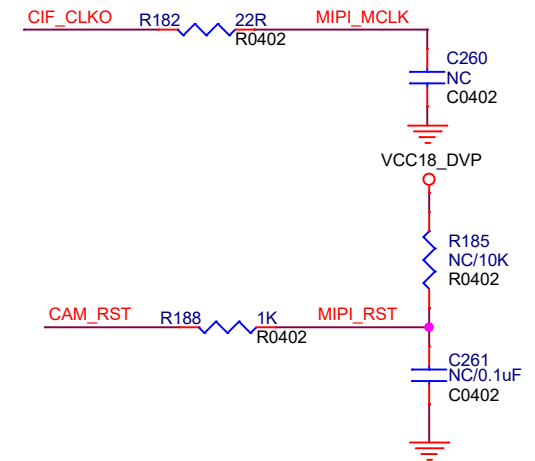
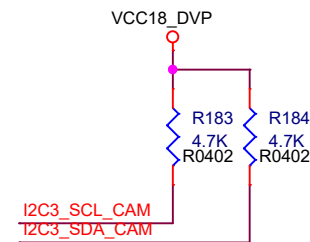
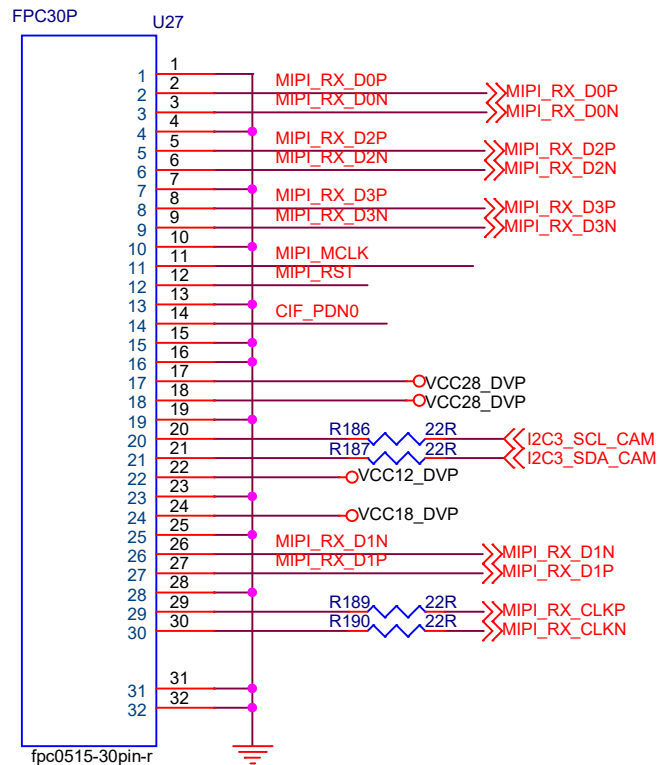
Note:  
Reserve PAD for Update.



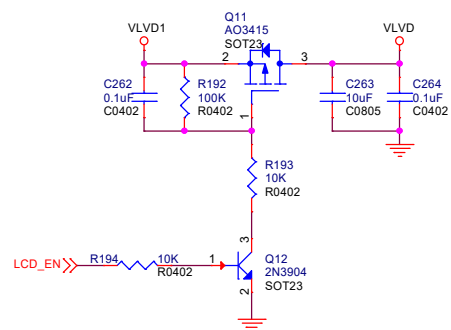
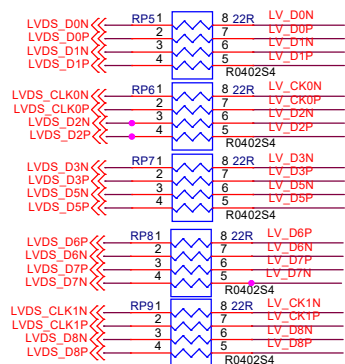
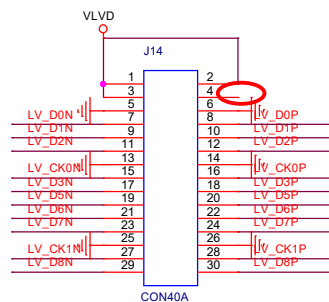
# Camera Power

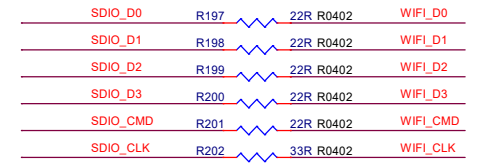
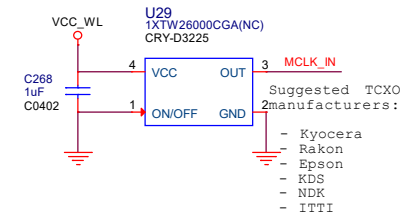
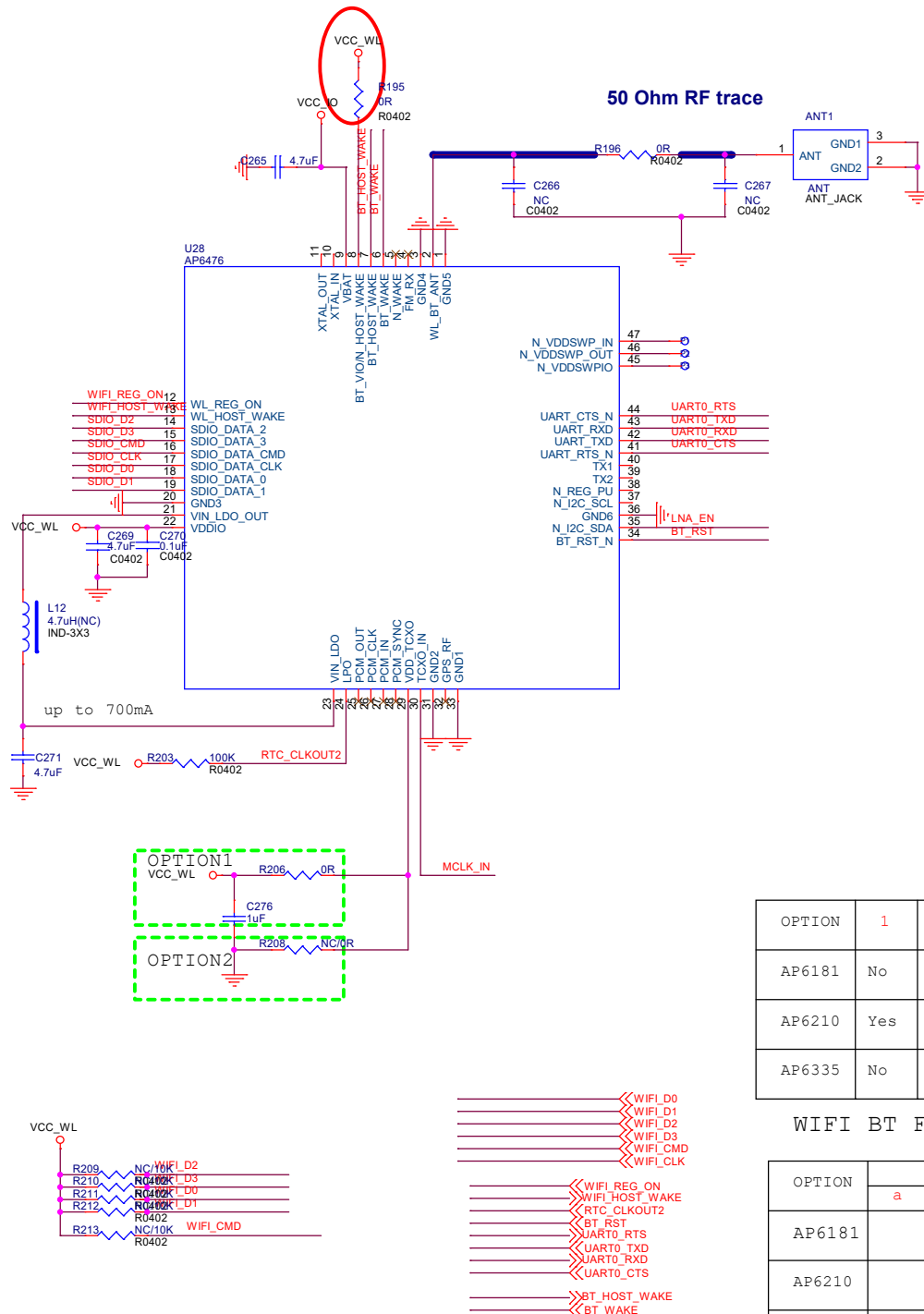


# MIPI Camera



 瑞芯微电子		福州瑞芯微电子	
Title: Camera-MIPI CSI			
File: RK3288_V10			REV:V0.1
Create Date: Friday, October 15, 2010		Page Num:21	
Modify Date: Tuesday, May 07, 2019		Page Total:34	



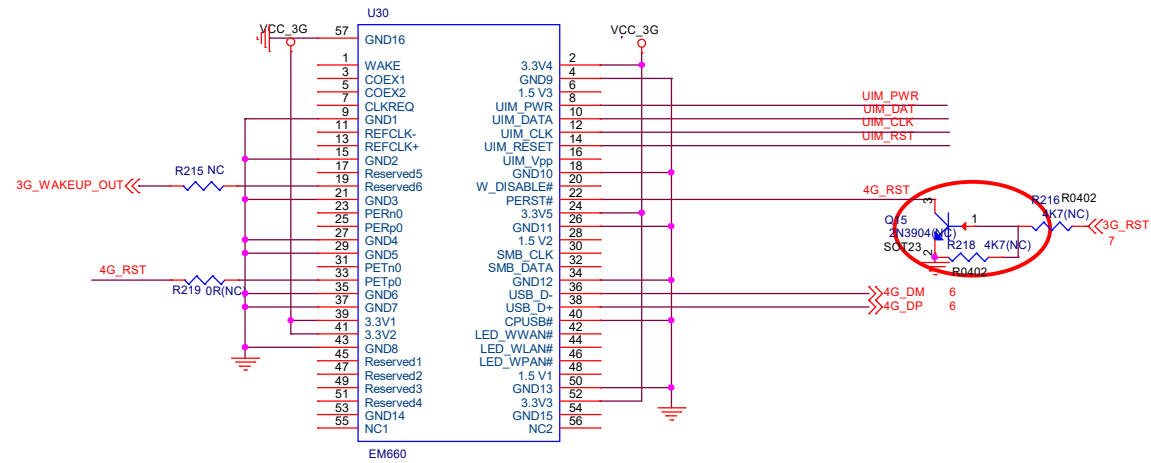


OPTION	1	2	6	7	8
AP6181	No	No	Yes	No	Yes
AP6210	Yes	No	No	Yes	Yes
AP6335	No	Yes	Yes	No	Yes

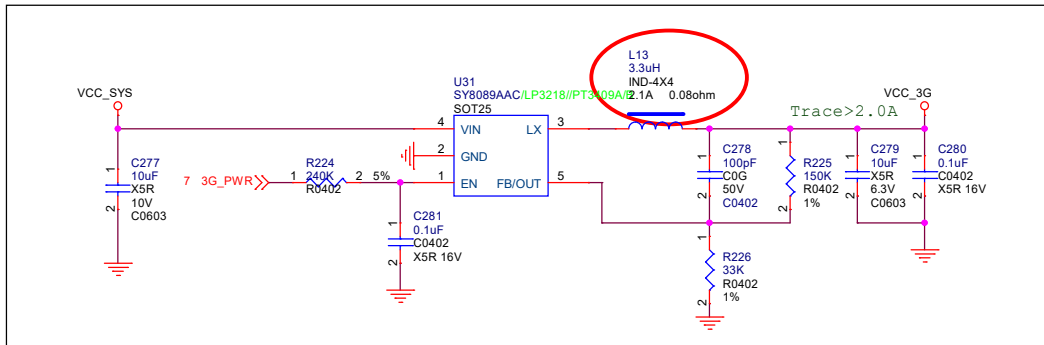
#### WIFI BT FM GPS MODULE

OPTION	WIFI				BT4.0	FM	VDD_IO
	a	b/g/n	ac	5GHz			
AP6181		Yes					1.71-3.6V
AP6210		Yes			Yes		1.71-3.6V
AP6335 default	Yes	Yes	Yes	Yes	Yes	Yes	1.71-3.63V

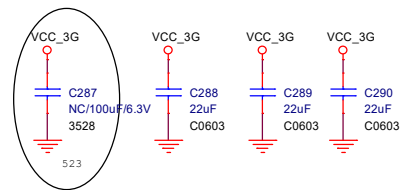
## 3G Power



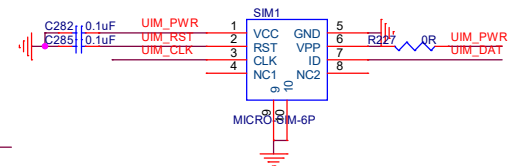
## EVDO MODUEL



## 3G Power



## 靠座子 摆数

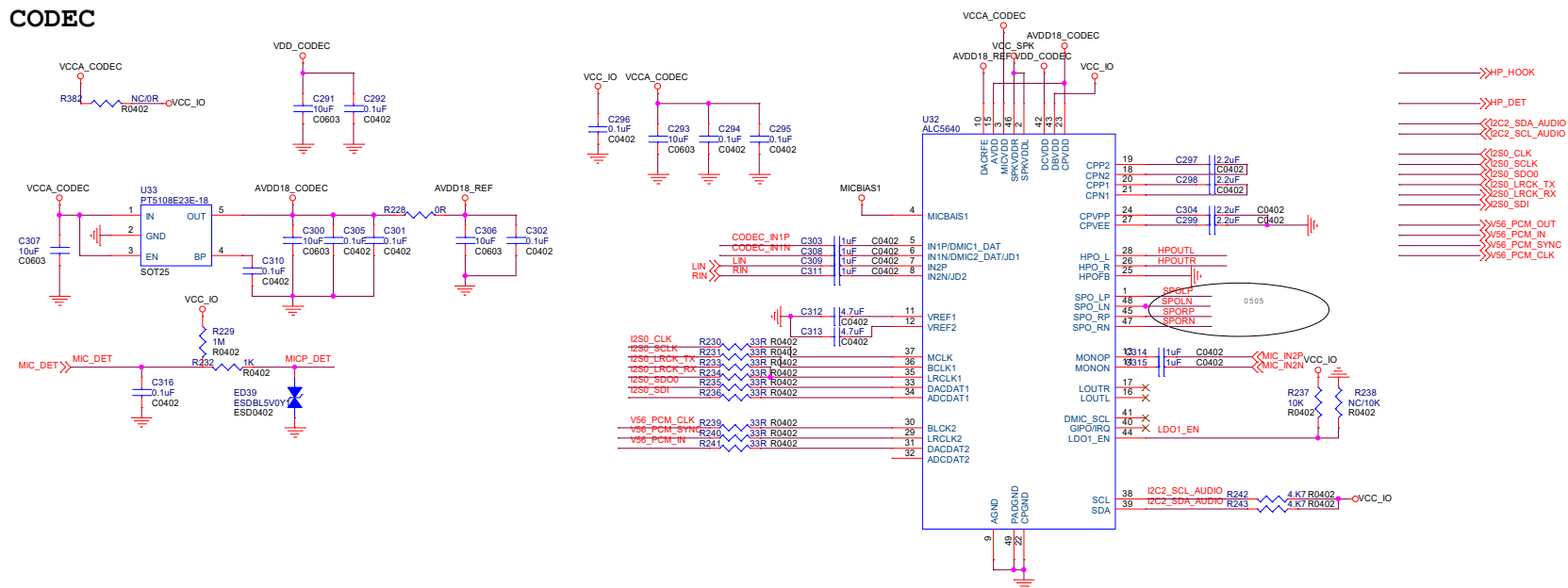


## SIM Card

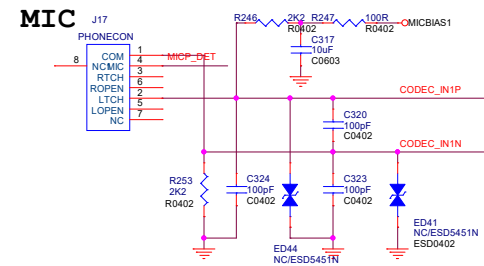
<b>Rackchip</b> 瑞芯微电子 福州瑞芯微电子公司	
Title: 3G	
File: RK3288_V10	
Create Date: Monday, April 29, 2019	Page Num: 24
Modify Date: Tuesday, May 07, 2019	Page Total: 34



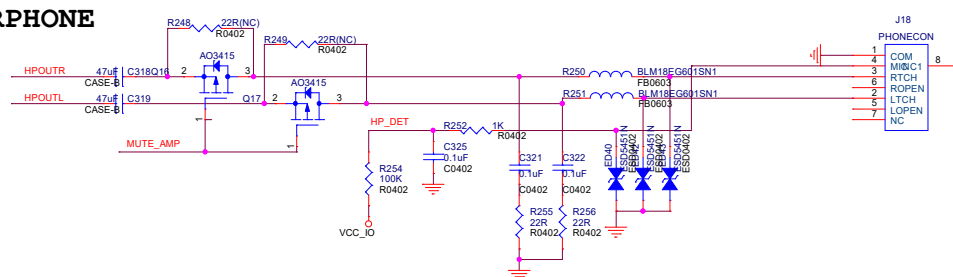
## CODEC



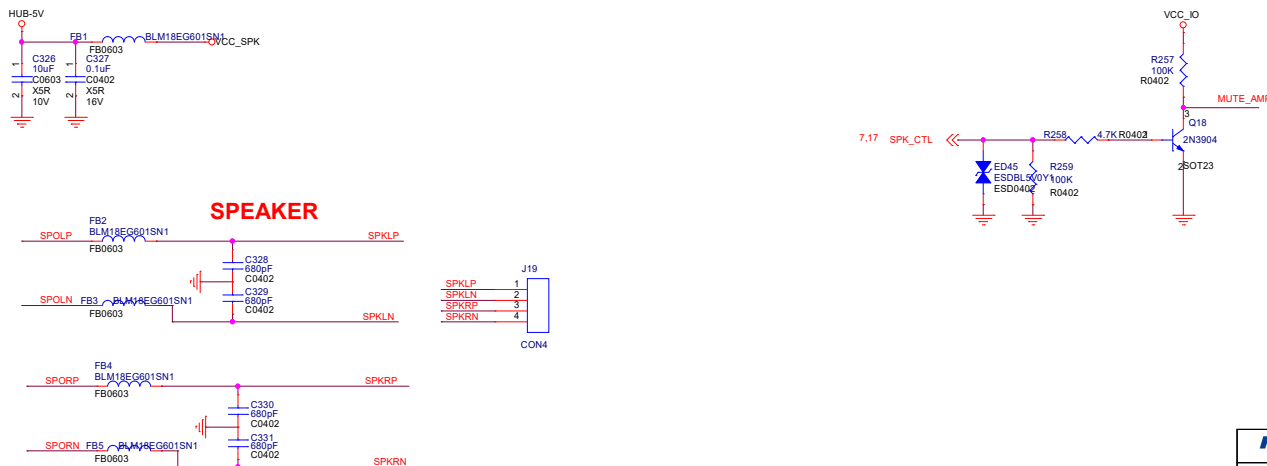
## MI



## EARPHONE

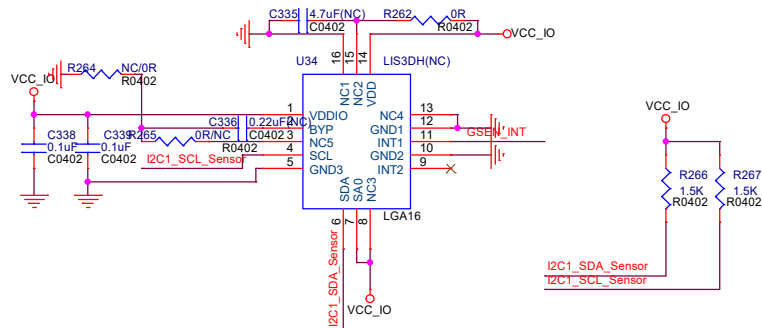


**SPEAKER**



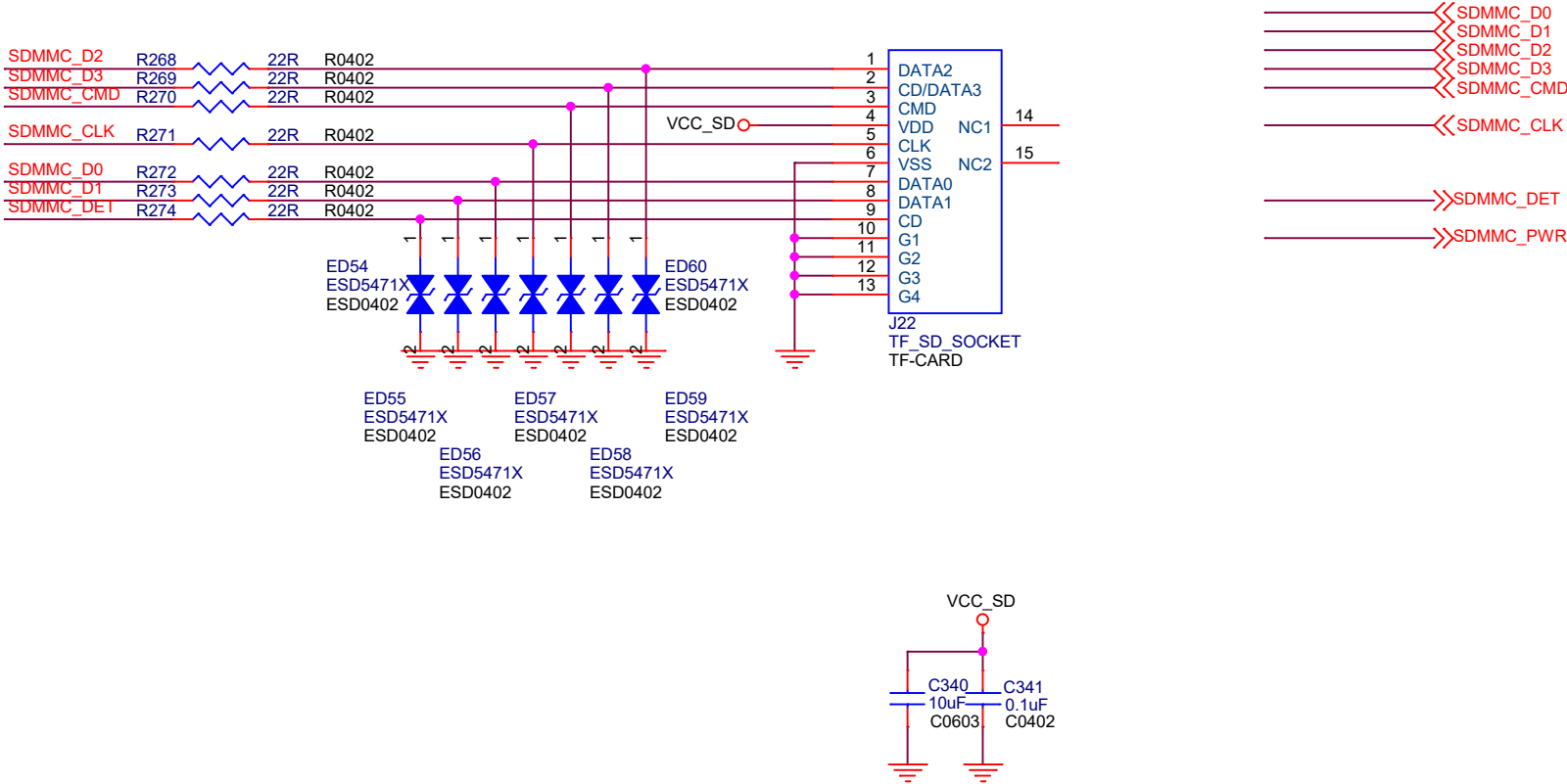
## LSM303D with 3D Gsensor and E-compass

	LIS3DH	MMA8452Q	LSM303D
C8001	NC	NC	4.7uF
R8001	0ohm	NC	NC
R8002	NC	0ohm	NC
C8004	NC	0.1uF	0.22uF
R8003	NC	NC	OR
R8004	NC	NC	OR

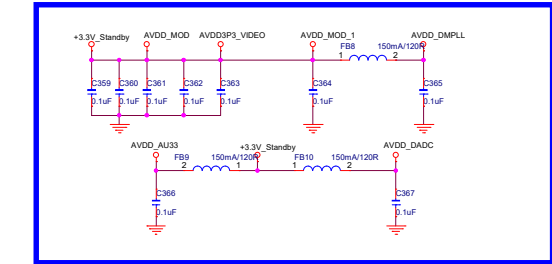
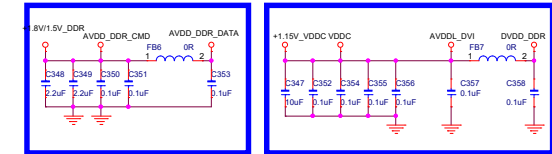
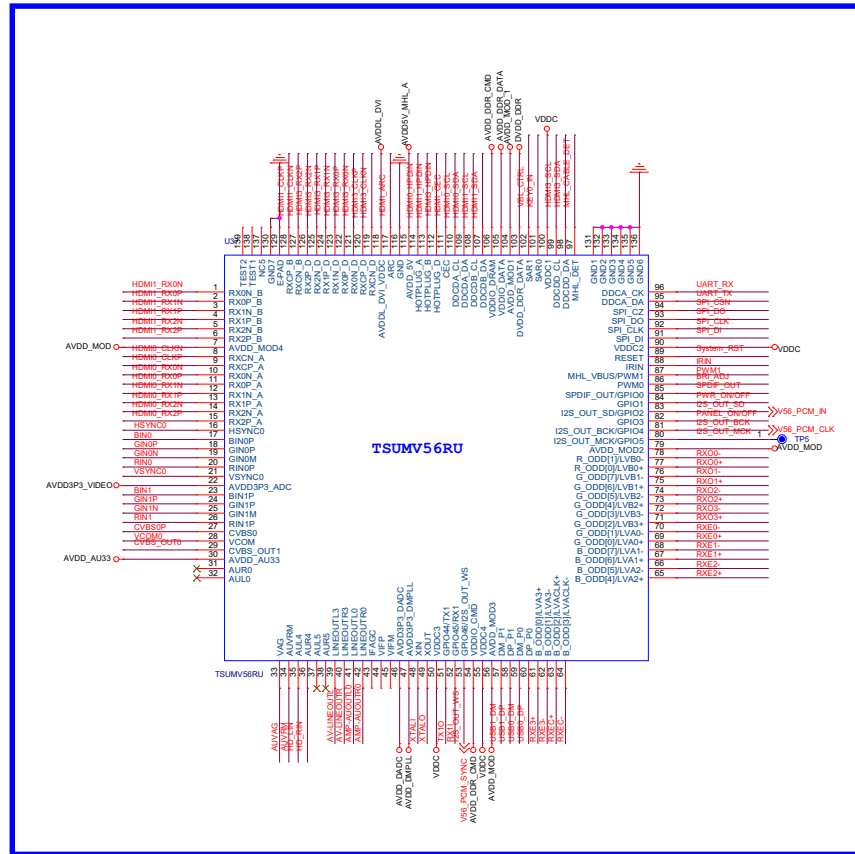
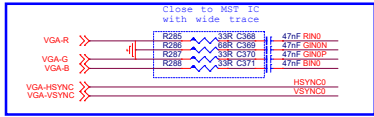
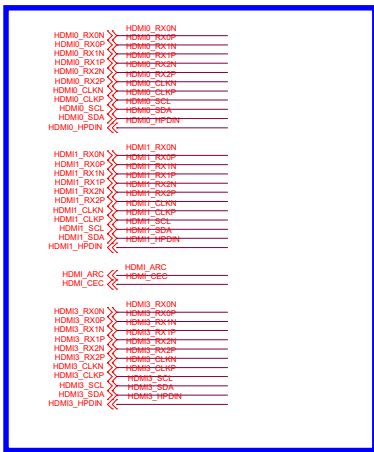


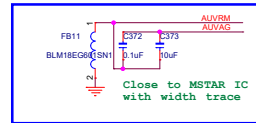

TF CARD



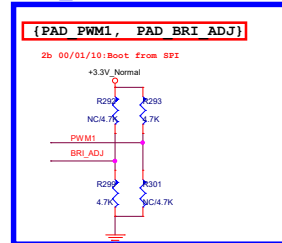




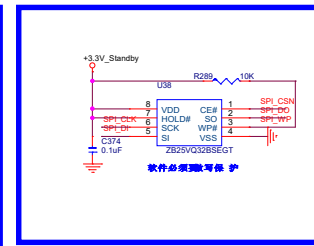
OTHER      LVDS



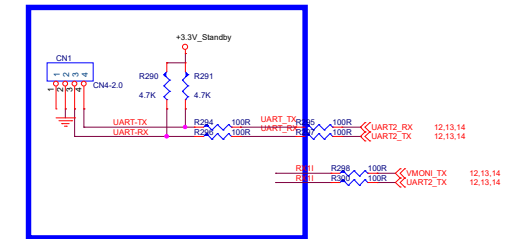
## CHIP\_CONFIG



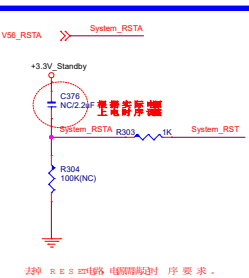
SPI NOR FLASH



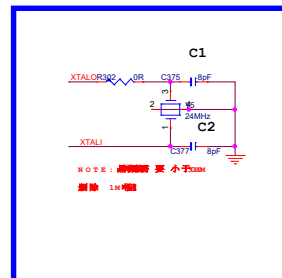
DEBUG



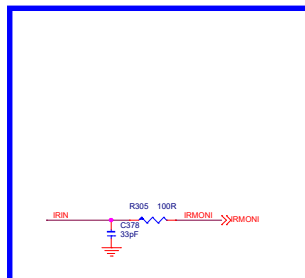
## Reset



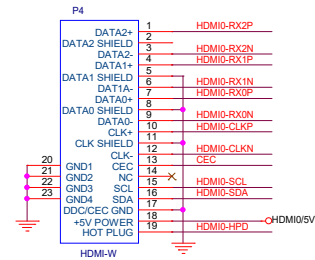
## Crystal



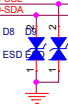
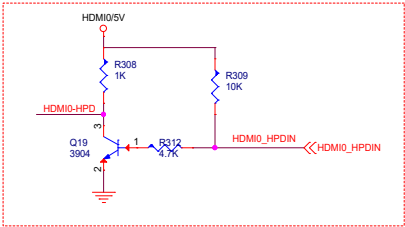
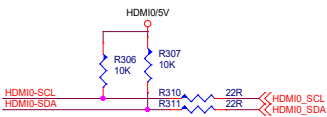
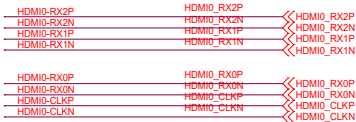
## IR&amp;KEYB



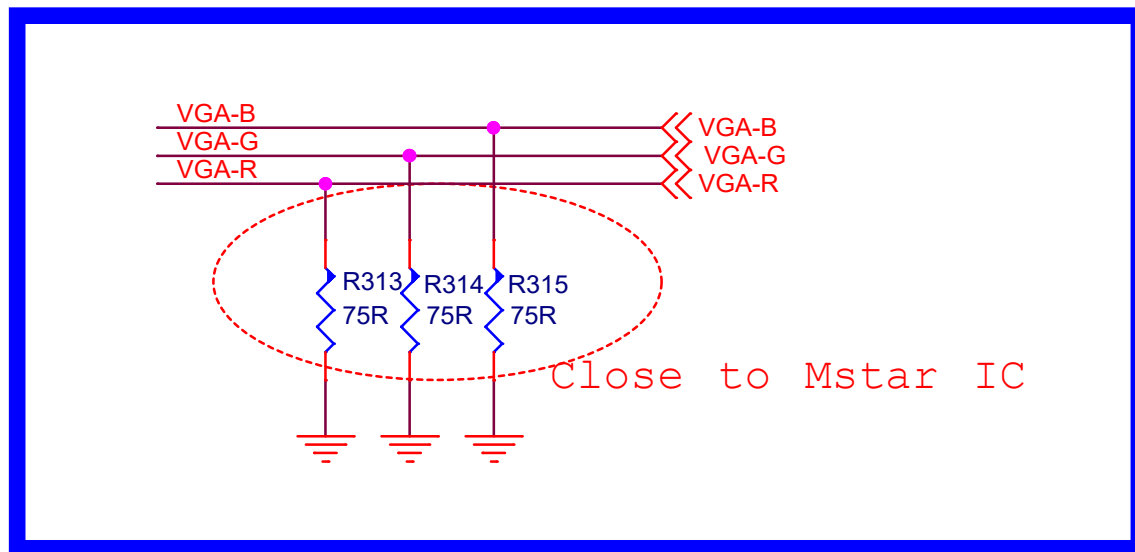
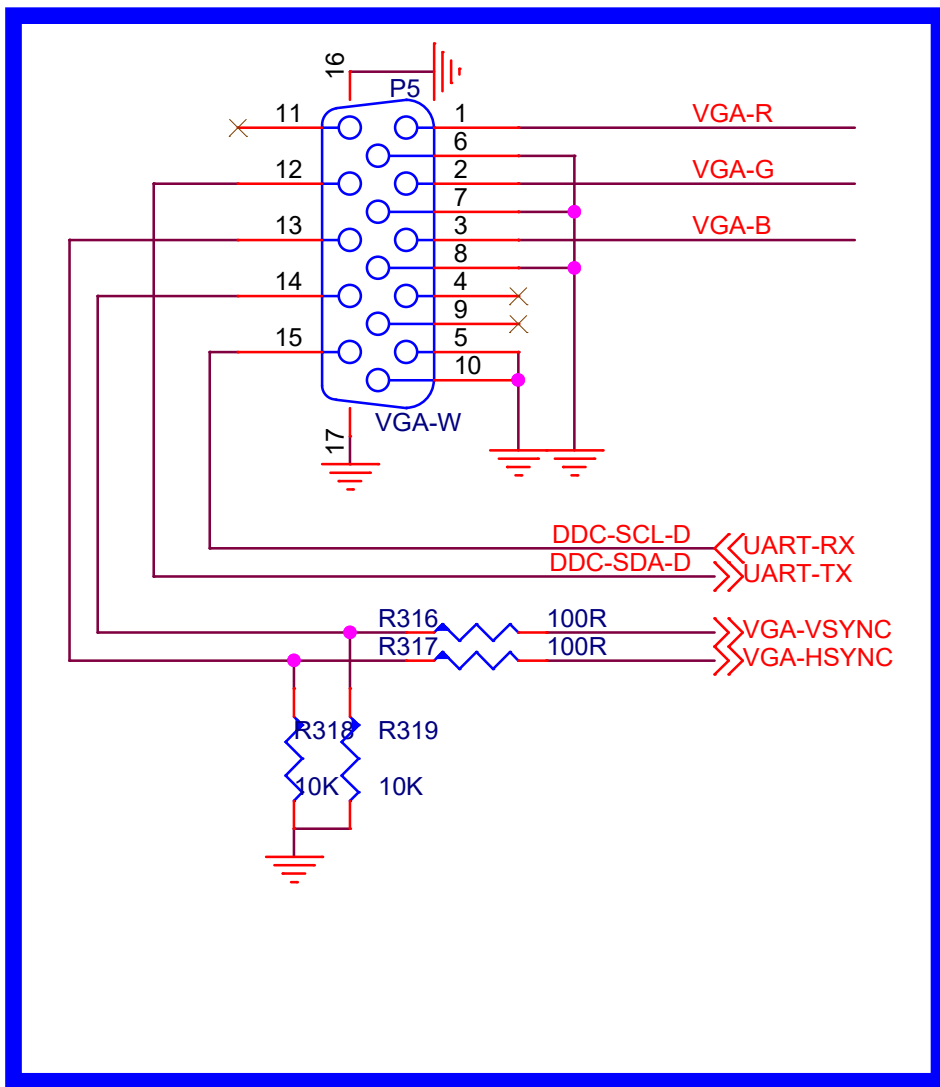
HDMI 1




靠近端子放置

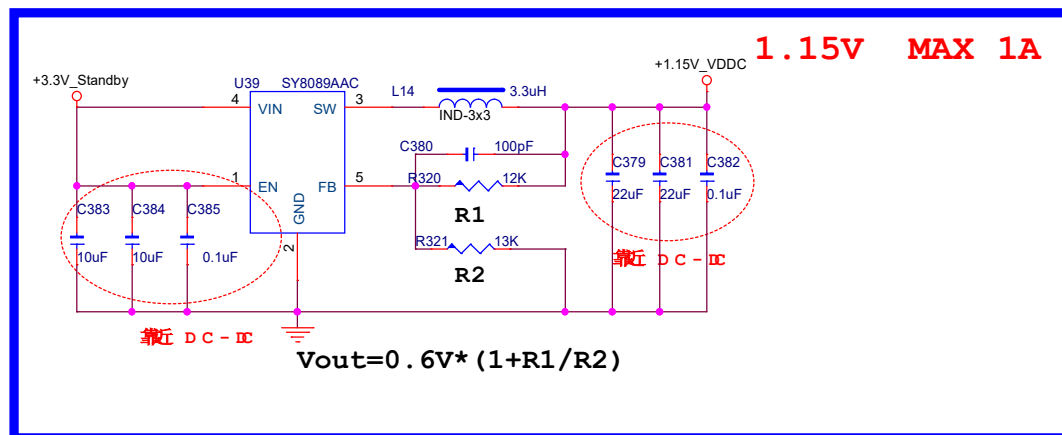


# VGA

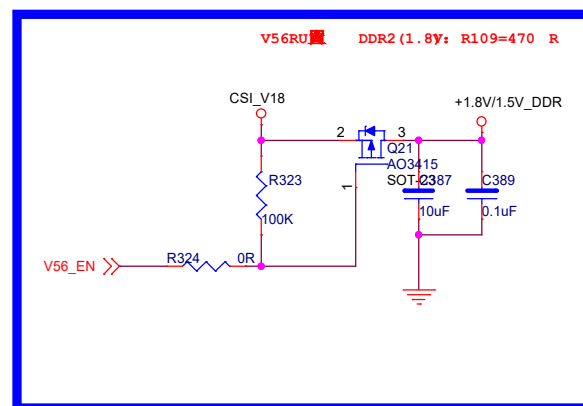
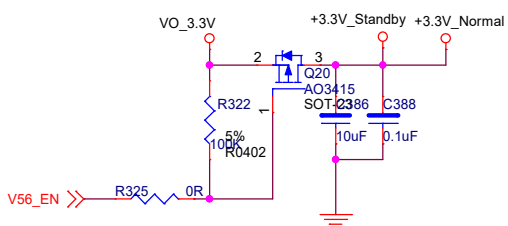


<div>&lt;OrgName&gt;<div>SMC 0755-26037701</div></div>		<div> SMART-CORE Technology</div>
<div>Title<div>TSUMV56RU</div></div>		
<div>Size<div>Custom</div></div>	<div>Document Number<div>VGA</div></div>	
<div>Date:<div>Tuesday, May 07, 2019</div></div>	<div>Sheet<div>32</div></div>	<div>of<div>34</div></div>

# 1.15V 1A Max for VDDC



## Power for DDR



<OrgName>

SMC 0755-26037700



Title

TSUMV56RU

Size

Document Number

Rev

Custom

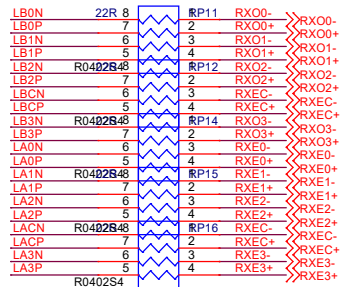
System power

V2.4

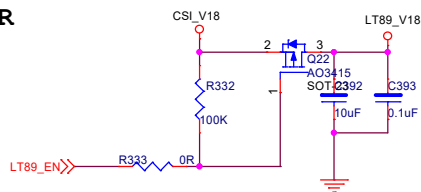
Date: Tuesday, May 07, 2019

Sheet 33 of 34

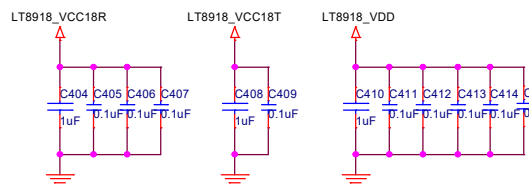
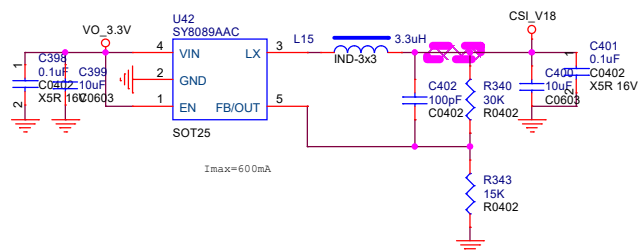




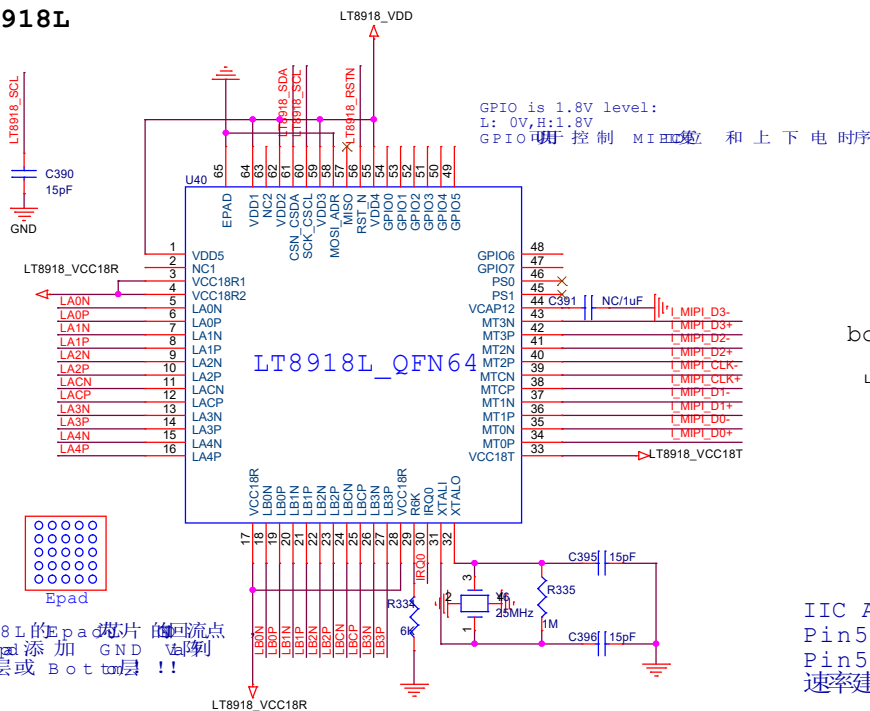
## POWER



功耗: 108 0P60 1.8V 2m A



## LT8918L



both 3.3V and 1.8V is OK.

IIC Addr:  
Pin58=L 0x40 Bit0 高  
Pin58=H 0x42 Bit0 高  
速率建议 10K

## MCU

