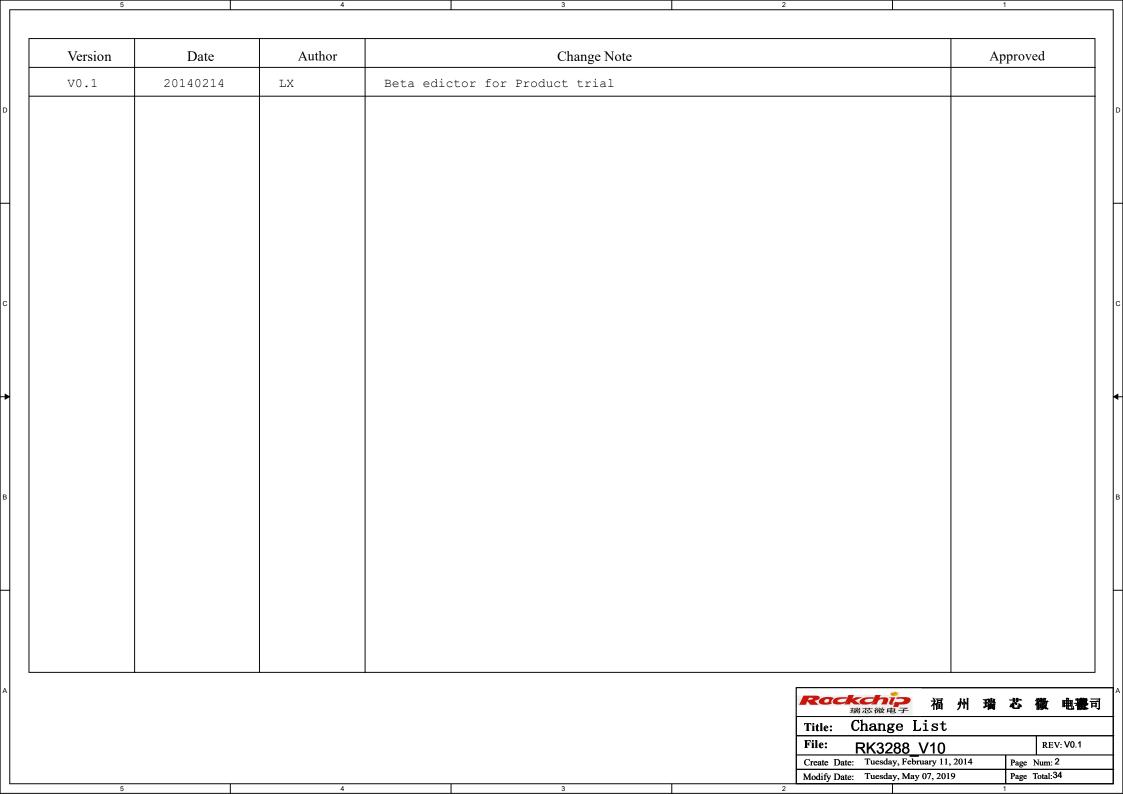
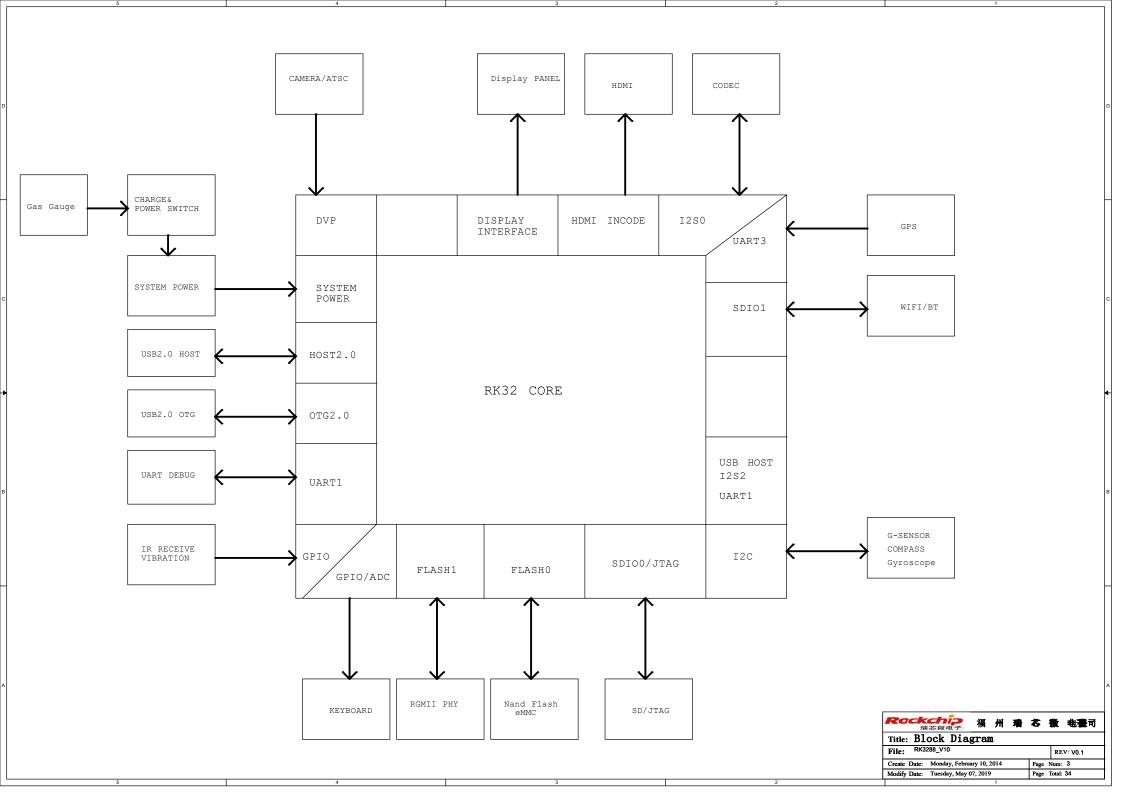
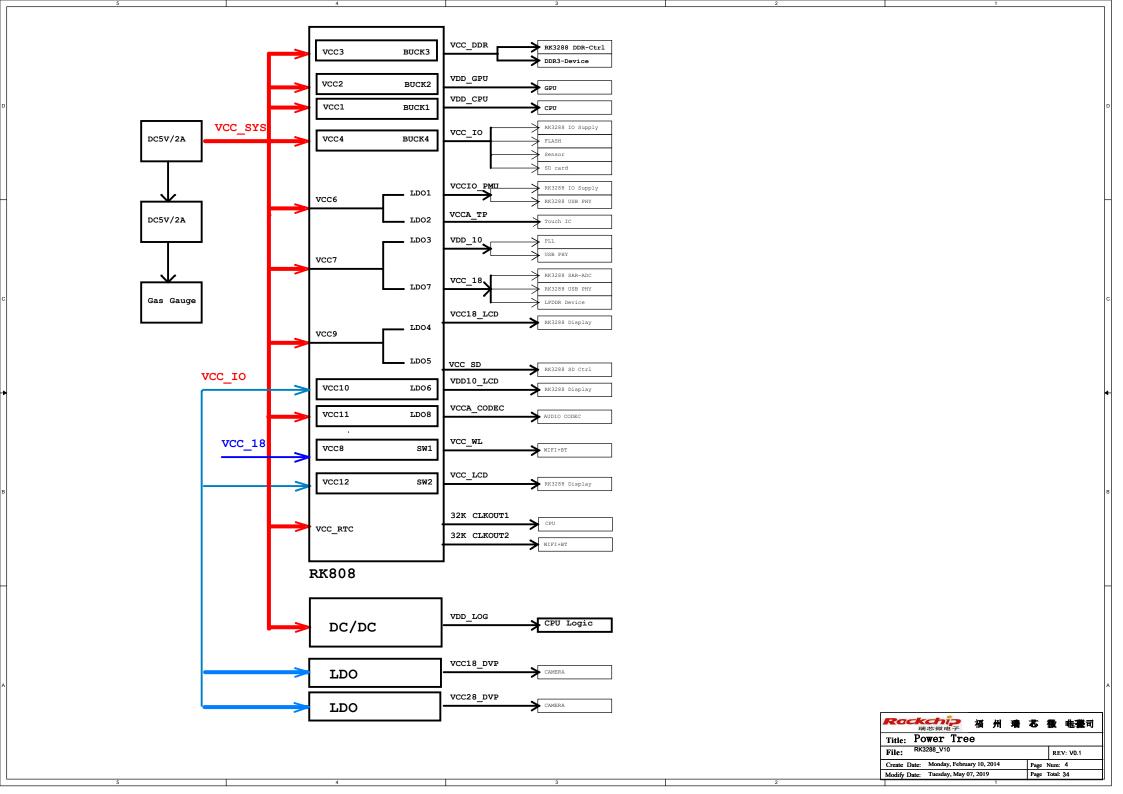
01.Index 02.Change List 03.Block Diagram 04.Power Tree 05.RK808-System Power 06.RK808-Chg/Gauge-DC 07.RK808-Chg/Gauge-USB (option) 08.RK3288 Power 09.USB Port 10.RK3288 PMU Controler 11.RK3288 DDR Controler 12.RK3288 Flash Controler 13.RK3288 USB/HSIC Controler 14.RK3288 SARADC/Key Board 15.RK3288 DVP Interface 16.RK3288 Display Interface 17.RK3288 GPIO 18.eFUSE (option) 19.Sensor/VIB 20.TF Card 21.Touch Panel 22.Audio Codec 23.HDMI Port 24.RAM-DDR3 4x16bit 26.RAM-LPDDR2(168P) (option) 27.RAM-LPDDR2(220P) (option) 29.Memory-Nand FLASH 30.Memory-eMMC (option) 31.Memory-tSD (option) 32.Camerá-MIPI CSI 33.Camera-CIF (option) 34.LCM-eDP Panel 35.LCM-MIPI Panel (option) 36.LCM-Dual MIPI Panel (option) 37.LCM-LVDS Panel (option) 38.WIFI/BT

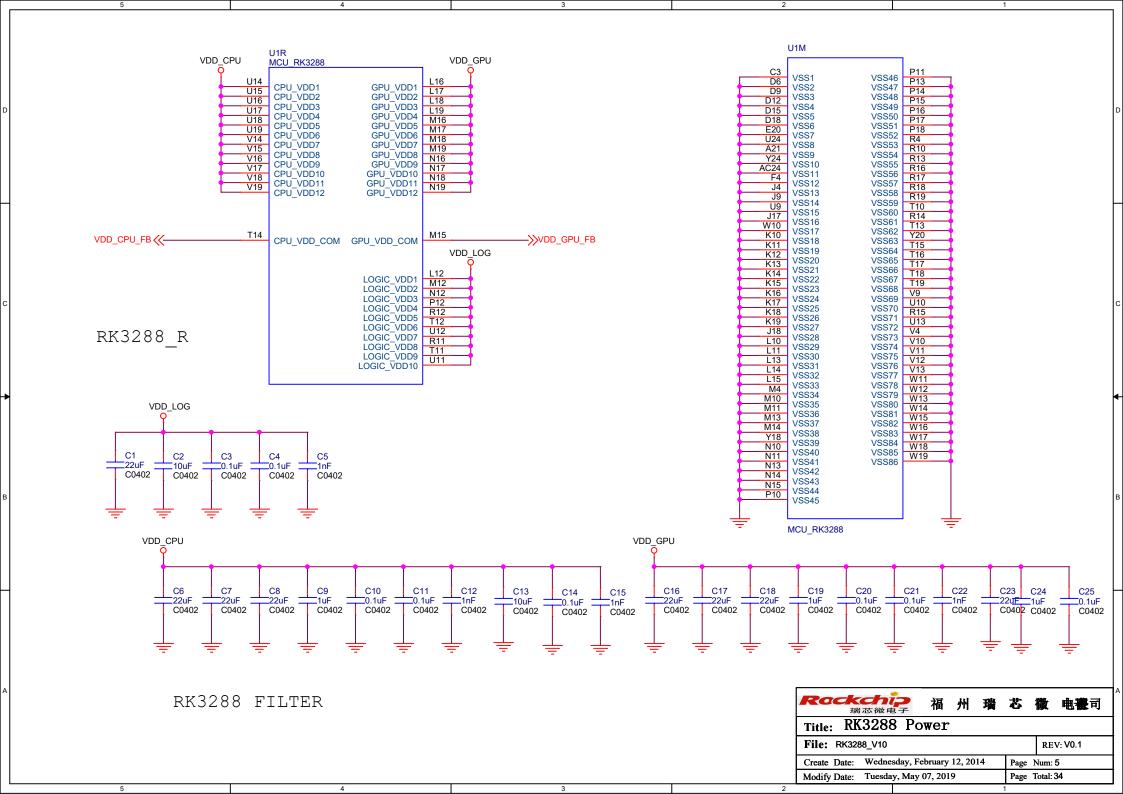
I2C address(7bit): 1.I2C0 POWER: **RK808** 0x1b CW2013 0x62 BQ24296 0x6b BQ27230 0x55 2.I2C1 Sensor: LSM330TR G:0x6a A:0x1e MPU6050C 0x68 CM3218 0x10.0x0c MMA8452Q 0X1d LIS3DH 0X19 LSM303D 0X1d 3.I2C2 Audio Codec: ES8323 0x10 4.I2C3 Camera: OV2659 0x30 OV8825 0x36 5.I2C4 Touch: CT363 0x1b

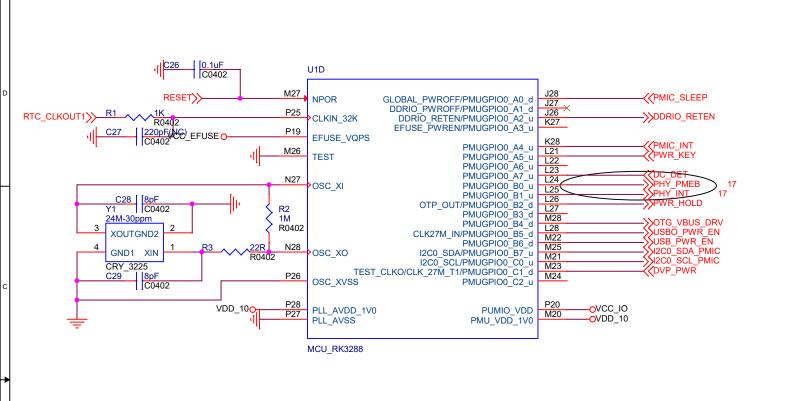
Rockchi, 瑞芯微电	₹ 福	州	瑞	芯	微	电视司
Title: Index						
File: RK328	8 V10				RE	V: V0.1
Create Date: Monday,	February 10,	2014		Page	Num:	1
Modify Date: Tuesday,	May 07, 201	9		Page	Total:3	34

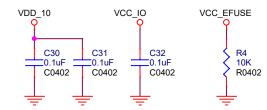








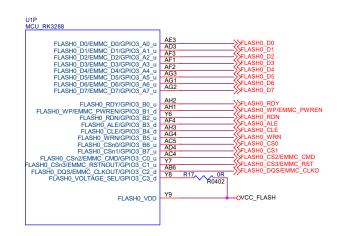


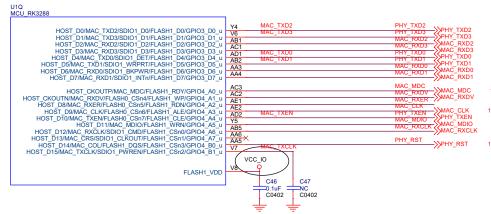




5 4 3 2

RK3288 DDR Channel-0 RK3288 DDR Channel-1 U1K U1L DDR0_A0 C11 DDR0_A1 A10 DDR0_A2 E12 R19 DDR0_DQ0 DDR1_DQ0 DDR1_A0 DDR1_A1 DDR1_A2 DDR1_A3 DDR0 D DDR0 A1 DDR1 DDR1_A1 DDR1_A2 DDR0_DQ1 DDR0_DQ2 DDR1_DQ1 DDR1_DQ2 DDR0_A2 DDR0_DQ3 DDR0_A3 DDR0_A4 W1 DDR1_DQ3 DDR1_A3 DDR1_A4 A19 E18 DDR1_A4 DDR1_A5 DDR1_A6 V2 DDR1_DQ4 V5 DDR1_DQ5 V3 DDR1_DQ6 DDR0_D5 DDR0_D6 DDR1_A5 DDR1_A6 DDR1_A7 DDR0_DQ5 DDR0_A5 SDDR0_A5 DDR1 DDR0 DQ6 DDR0_A6 SDDR1 A7 DDR0_DQ7 DDR0_A7 DDR0_A7 DDR1 E1 DDR1_DQ7 DDR1_DQ8 DDR1_DQ9 C1 DDR1_DQ9 DDR1_DQ7 DDR1_A8 P1 DDR1_A9 N2 DDR0_A8 A14 DDR0_DO8 DDR0_A9 A14 DDR1_A9 DDR1_A10 DDR0_D9 DDR0_D10 DDR0_DQ9 DDR0_A9 DDR1 DDR0_A10 DDR0_A11 DDR0_A11 DDR0_A12 DDR0_A13 DDR0_A13 DDR0_A14 DDR0_A13 F3 DDR1 DQ10 H5 DDR1 DQ11 H4 DDR1 DQ12 J5 DDR1 DQ14 DDR1 A10 DDR0_DQ10 C6 DDR0_D11 DDR0_D12 DDR0_D13 DDR0_D14 DDR1_A11 DDR1_A12 DDR1_A13 DDR1_A14 DDR0_A11 DDR0_DQ11 DDR1_A11 P6 DDR1_A12 P3 DDR1_A13 P2 DDR0_DQ12 DDR0_DQ13 DDR0_A13 DDR0_DQ14 DDR0_A14 DDR1 DO14 DDR1 A14 J6 DDR1_DQ15 F8 B15 A16 DDR0_D16 DDR0 DQ15 DDR1_A15 -SDDR1_A15 DDR0_A15 T2 DDR1_DQ15 DDR0_DQ16 DDR1 D1 DDR0_CLK A11 DDR0_CLK ->>DDR1_CLK DDR0 DQ17 T1 DDR1_DQ17 R5 DDR1_DQ18 U4 DDR1_DQ19 V1 DDR1_DQ20 DDR1 DQ17 DDR1 CLK F15 DDR0_D18 SDDR0_CLKN -SDDR1_CLKN DDR0_DQ18 DDR0 DO19 DDR0_BA0 DDR0_BA1 DDR0_BA2 DDR1_BA0 DDR1_BA1 DDR1_BA2 DDR0_D20 DDR0_D21 DDR0_DQ20 DDR0_BA0 V1 DDR1_DQ21 U3 DDR1_DQ21 V2 DDR1_DQ22 B1 DDR1_DQ23 B2 DDR1_DQ24 DDR1_DQ25 DDR1_BA1 DDR1_BA2 DDR0_BA1 DDR0_BA2 DDBU_DO31 C17 A18 DDR0_D22 DDR0_DQ22 DDR0_D23 < DDR0_D24 < DDR0_D25 < DDR0_DQ23 DDR0 DQ24 DDR0_ODT0 E15 ->>DDR0_ODT0 ->>DDR0_ODT1 DDR1_D24 -SSDDR1 ODT0 -SDDR1_ODT1 G6 DDR1 DQ26 DDR1 DQ26 DDR0 DQ25 DDR1 ODT1 D5 E6 C5 DDR0_D26 DDR0_D27 DDR0_D28 DDR0 DQ26 DDR0_CS0N DDR0_CS1N DDR1_CS0N DDR1_CS1N DDR0_DQ27 DDR0_DQ28 DDR0_CSn0 DDR0_CSn1 DDR1_D27 DDR1_DQ27 DDR1_DQ28 DDR1_CSn0 DDR1_CSn1 F5 DDR0_D29 DDR0_DQ29 C2 DDR1_DQ20 H6 DDR1_DQ30 DDR1_DQ31 C4 E7 DDR1_CKE0 ->>DDR0_CKE0 ->>DDR0_CKE1 DDR0 CKE0 DDR1 CKE0 DDR0_DO30 DDR0_D31 << DDR0_DQ31 DDR0_CKE1 DDR1 D31 2 AA2 DDR1_DQS0 DDR1_DQS0n A20 SODRO RASN ->>DDR1 RASN DDR0 DQS0P (DDR0_DQS0 DDR0_RASn DDR1 DOS0P DDR1_RASn DDR1_CASn DDR0_RASn A8 DDR0_CASn B9 DDR0_CASN DDR0 DOS0n -SODR1_WEN DDR0_DOS1P DDR0 DQS1 DDR0 WEn DDR1 DQS1P G2 DDR1_DQS1 DDR1_DQS1 DDR1_DQS1n DDR1 WEn A7 DDR0 DQS1 B17 DDR0 DQS1 DDR0 DQS2 DDR0 DQS2 DDR0_DQS1M DDR1_DQS1M DDR0 DOS1n U2 DDR1_DQS1n U1 DDR1_DQS2 D2 DDR1_DQS2n DDR1_DQS3n DDR1_DQS3n DDR1_DQS2P of DDR1_DQS2M of DDR1_DQS2M of DQS2M DDR0_RESET ⟨CDDR0_RST DDR1_RESET ->>DDR1_RST DDR0 DQS2M DDR0_DQS2n DDR0_DQS3 DDR0_VREF H12 DDR0_VREFAO -OVREFAO_DDR0 OVREFAO_DDR1 DDR1 VREF A4 L8 DDR0_DQS3n DDR1_VREFAO G14 G15 G11 U5 G5 DDR1_DM0 DDR1_DM1 DDR0 DM0 < NC1 NC2 NC4 NC5 DDR0_DM0 R7 × DDR0_DM1 DDR1_DM1 < DDR0 DM1 D3 DDR1_DM2 DDR0_DM2 NC6 В3 DDR0 DM3 DDR1 DM3 H9 OVCC DDR OVCC_DDR DDR0 RETLE DDR0_VDD1 DDR0_VDD2 DDR0_VDD3 DDR0_VDD4 DDR1 RETLE DDR0 RETLE DDR1 RETLE G12 DDR0_RETL DDR0_ATO M7 DDR1_RETL DDR1_ATO DDR1_DTO1 DDR1_DTO1 DDR0_DTO1 DDR0_DTO0 DDBU_VDD4 DDR1_DTO1 DDR1_DTO0 DDR1_VDD4 DDRU_VDD5 J10 DDR0_VDD5 H14 G17 P8 -OVCC DDR -OVCC DDR DDR0_PZQ DDR0_VDDAO DDR1_PZQ DDR1_VDDAO R0402 R0402 MCU RK3288 MCU RK3288 240R%1 R5 240R%1 VCC_DDR 0 R7 10K R0402 DDRIO_RETEN R10 NC R0402 DDRIO_RETEN >>-| R11 \\ R0402 | R12 NC R0402 VCC DDR VCC DDR C33 C34 C35 T1nF C36 C37 C38 C0402 C0402 C0402 C0402 C0402 C0402 VCC_DDR VCC DDR C41 | 0.1uF | C0402 | R14 | 10K | R0402 VREFAO_DDR0 VREFAO_DDR1 R15 C42 C43 R16 C44 Rockchip 瑞芯微电子 C0402 R0402 C0402 福州瑞芯徽电量新司 Title: RK3288 DDR Controler RK3288 K RK3288_V10 RK3288 L REV: V0.1 Create Date: Saturday, January 11, 2014 Page Num: 7 Modify Date: Tuesday, May 07, 2019 Page Total:34







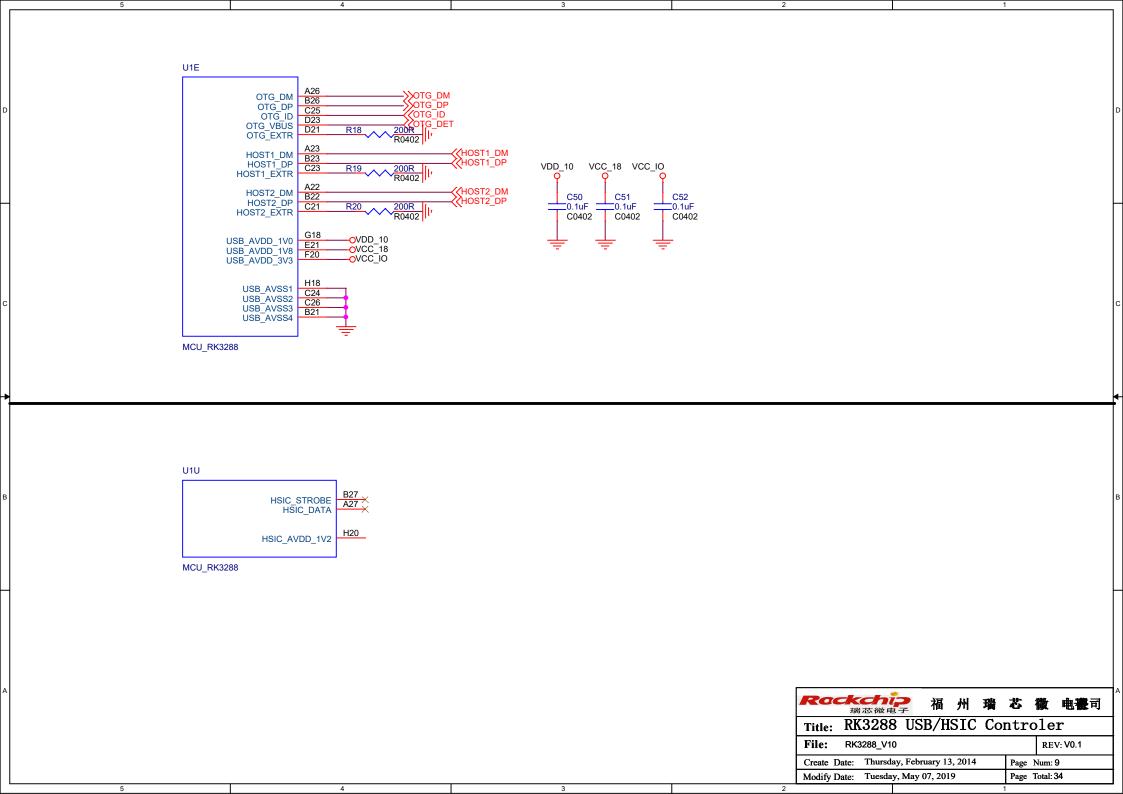
Note:VCC_IO 3.0V(**楓**定为3.0V); VCC FLASH (**攤**7的1.8V 或 3**) 缬**控的FLA**y**D管脚相连).

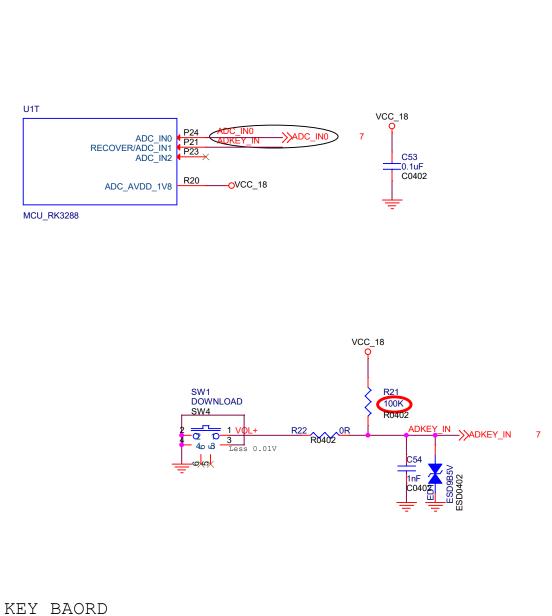
Title: RK3288_V10

Create Date: Tuesday, November 29, 2011

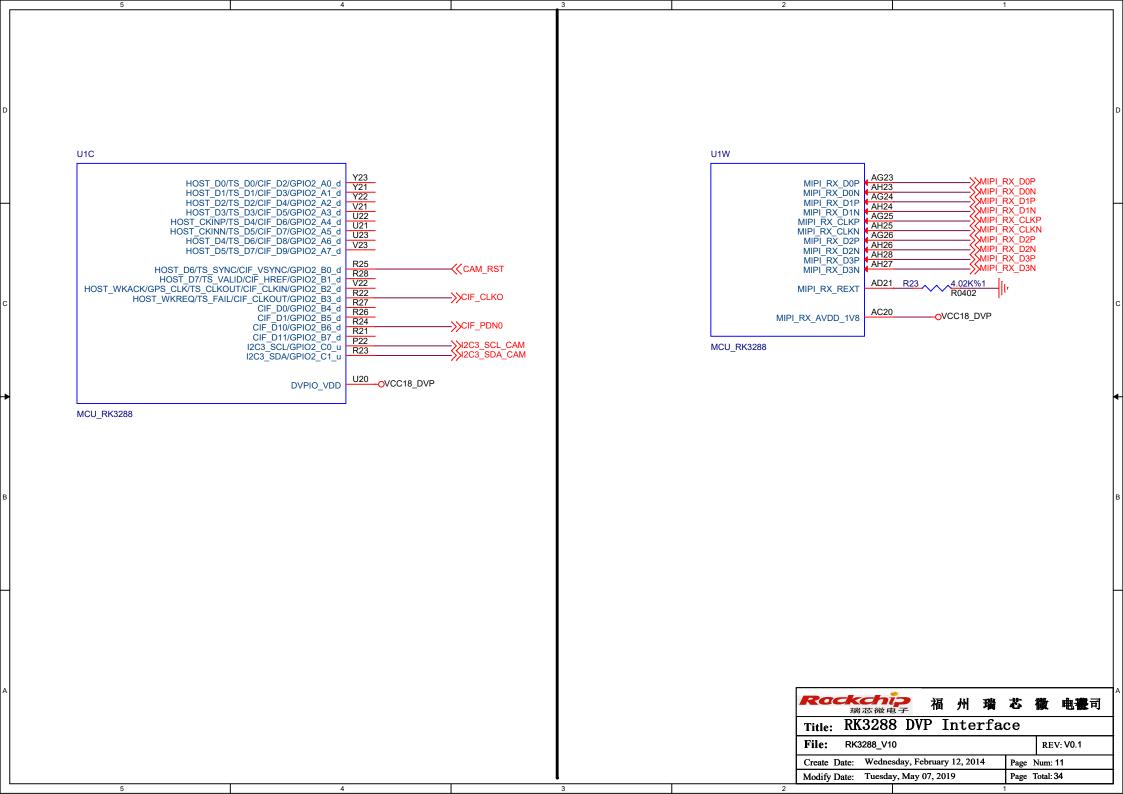
Modify Date: Tuesday, May 07, 2019

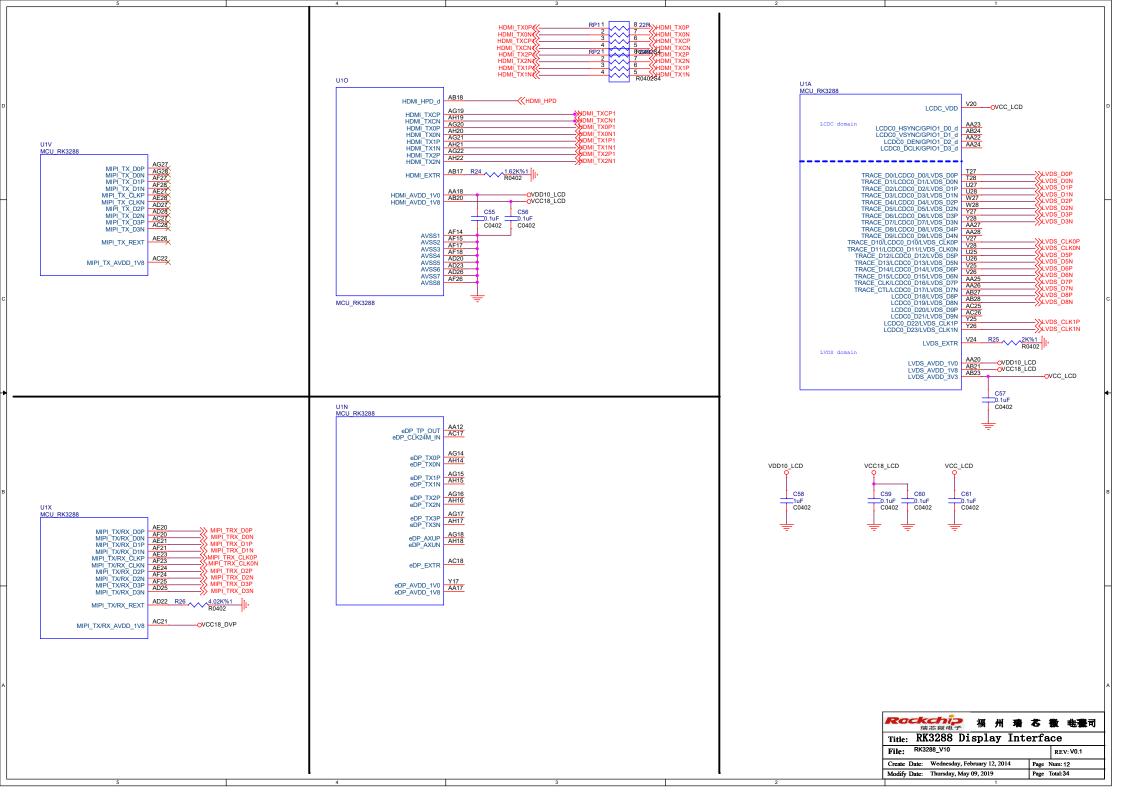
Page Tout: 34

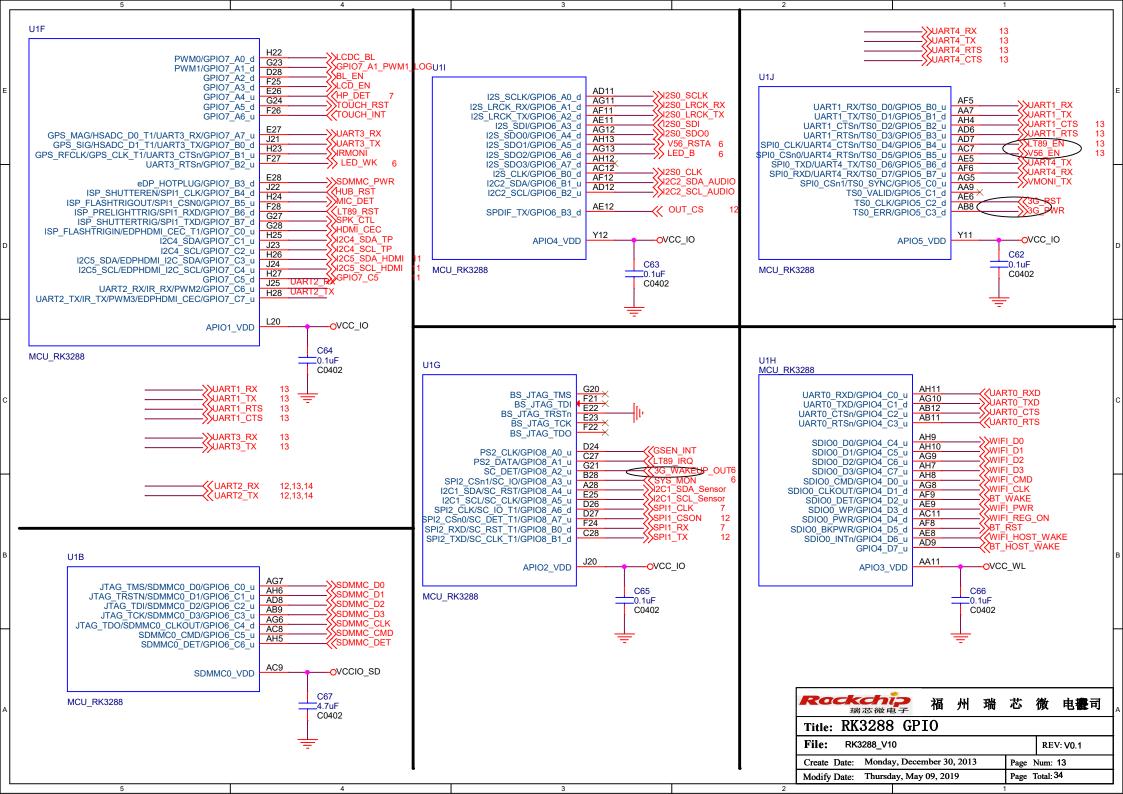


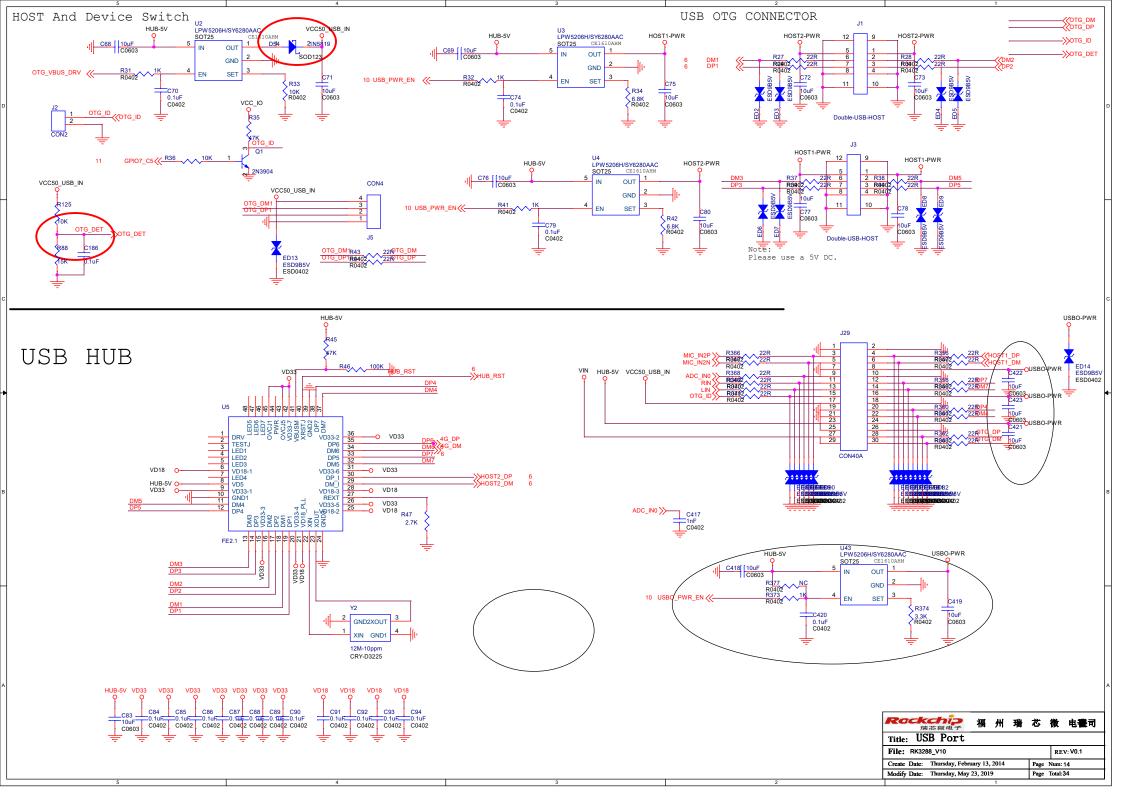


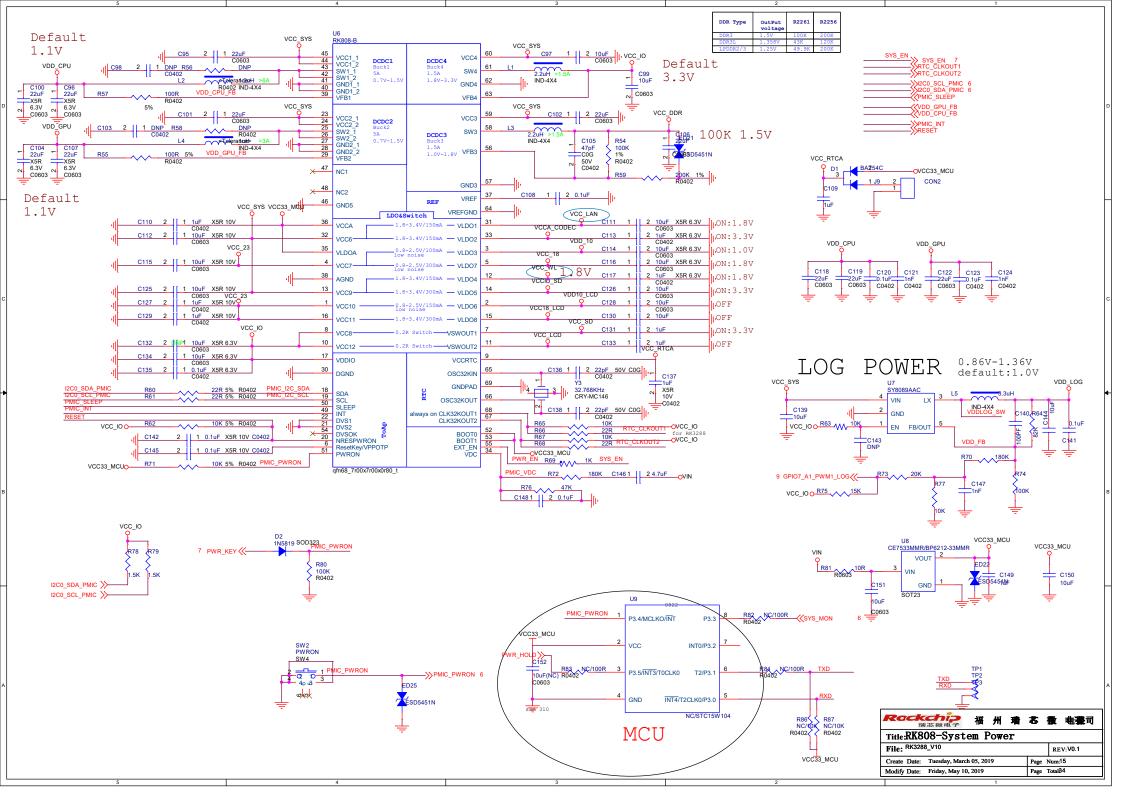
ROCKChip 福州瑞	芯	微电极带司
Title: RK3288 SARADC/Key	Boaı	rd
File: RK3288_V10		REV: V0.1
Create Date: Wednesday, February 12, 2014	Page	Num: 10
Modify Date: Tuesday, May 07, 2019	Page	Total: 34

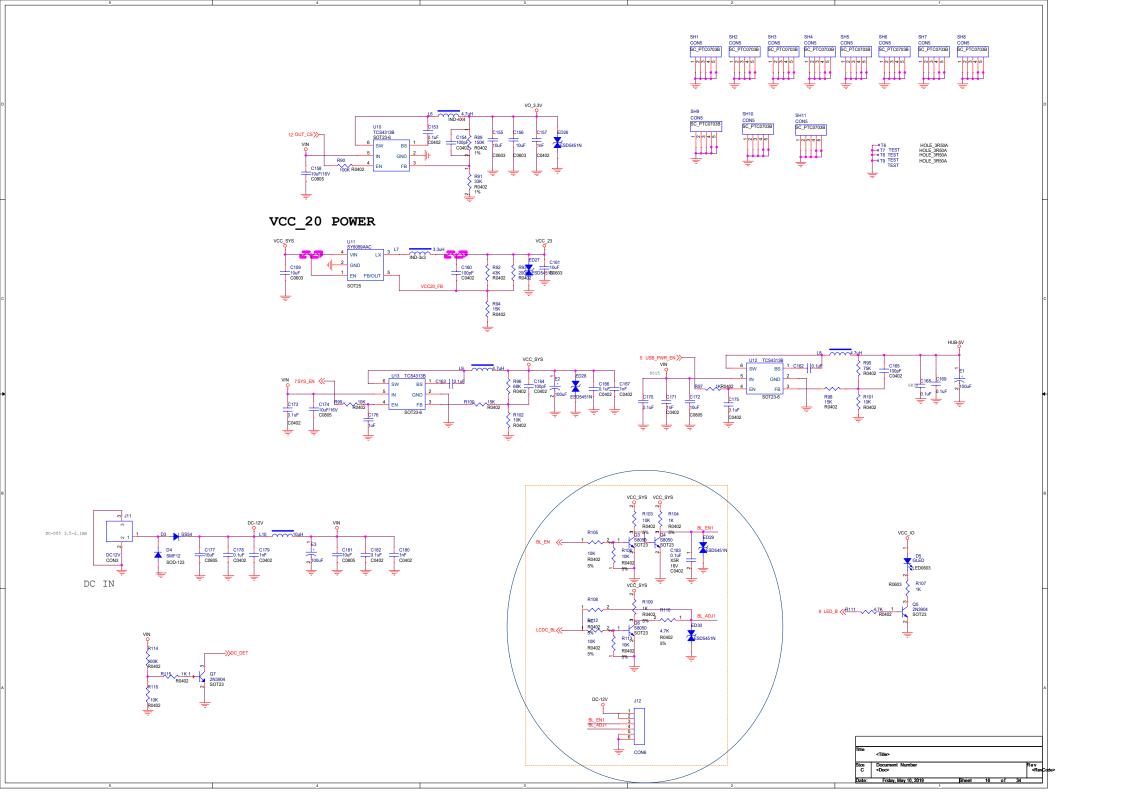


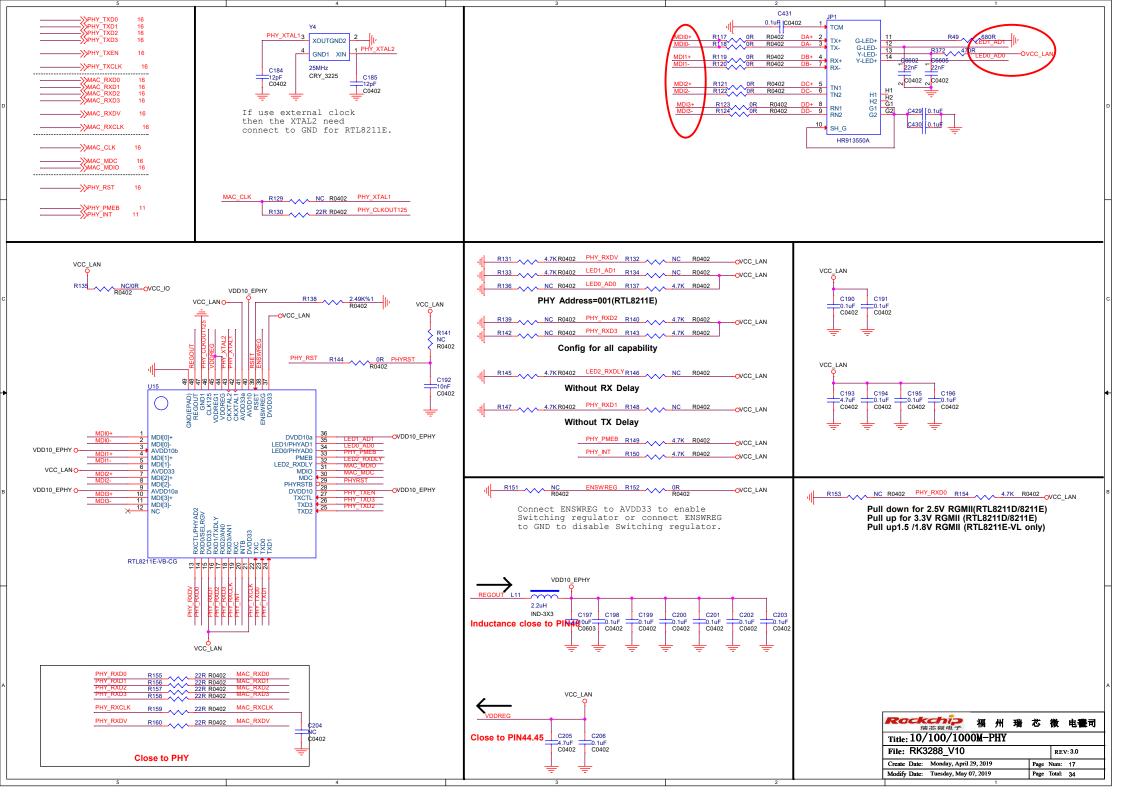


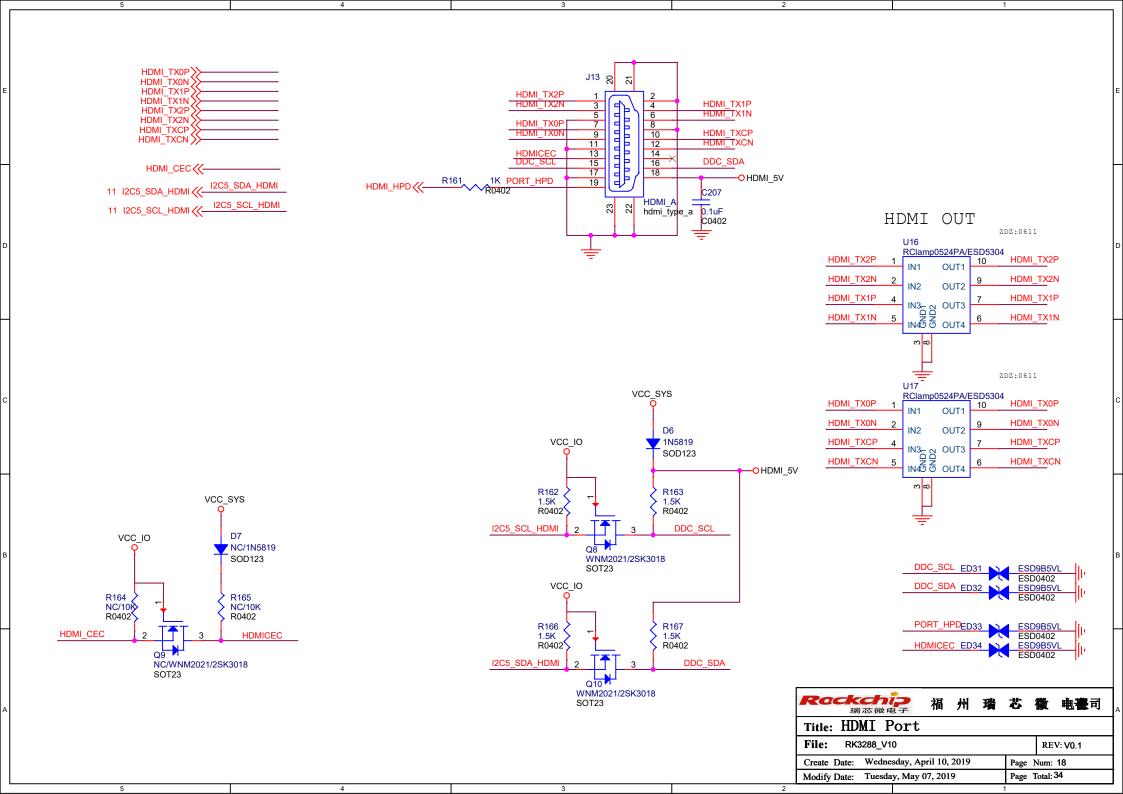


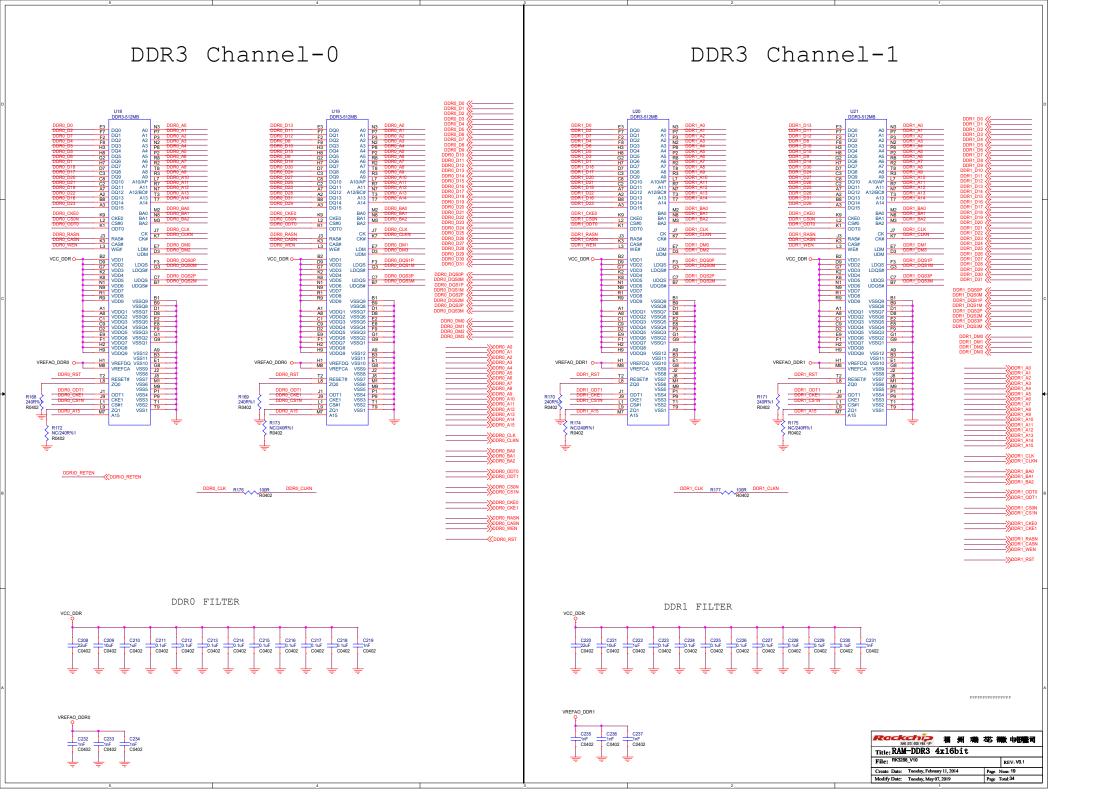


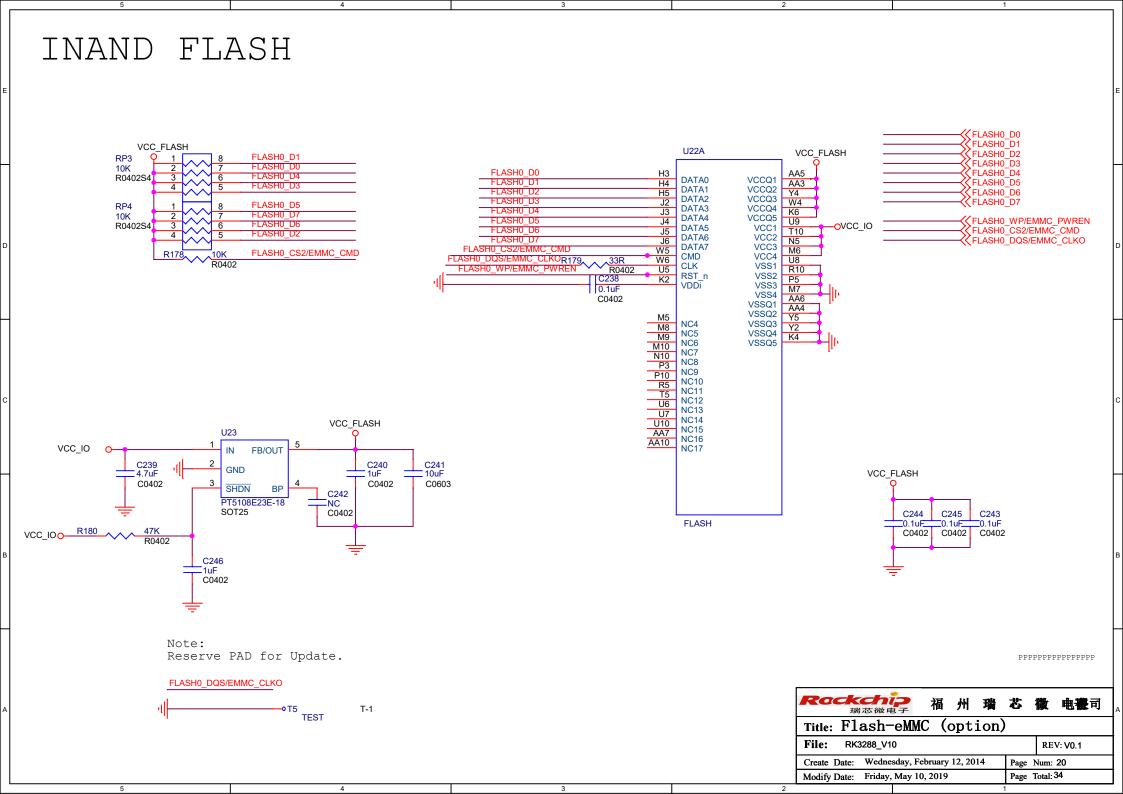


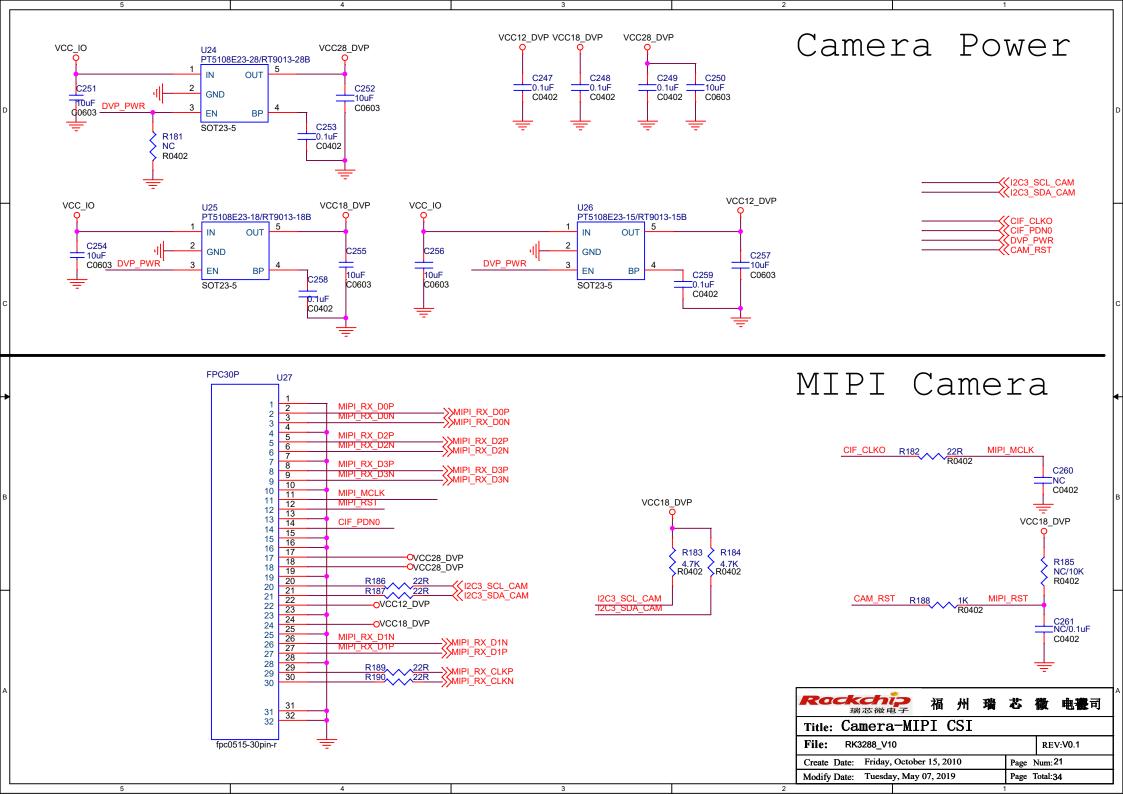


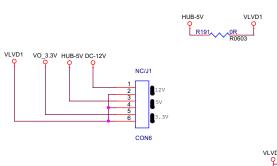


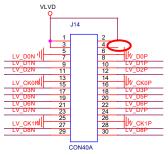


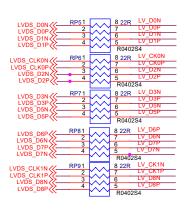


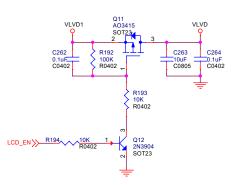




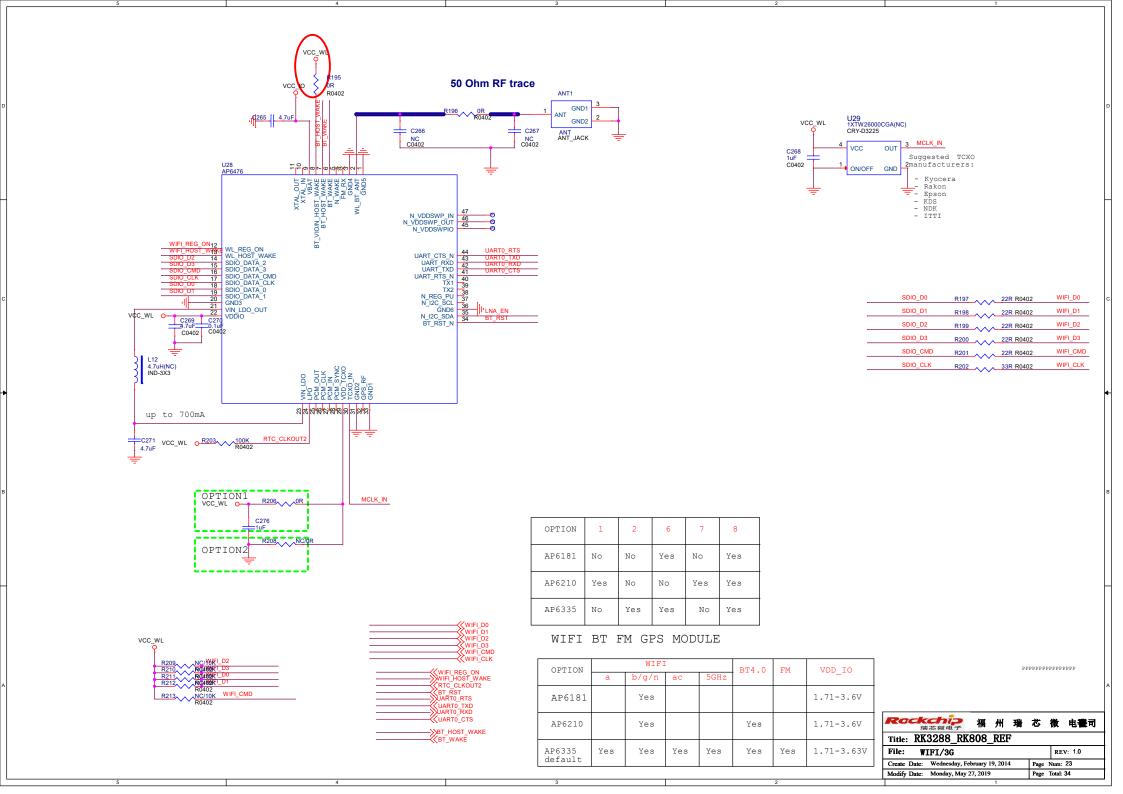


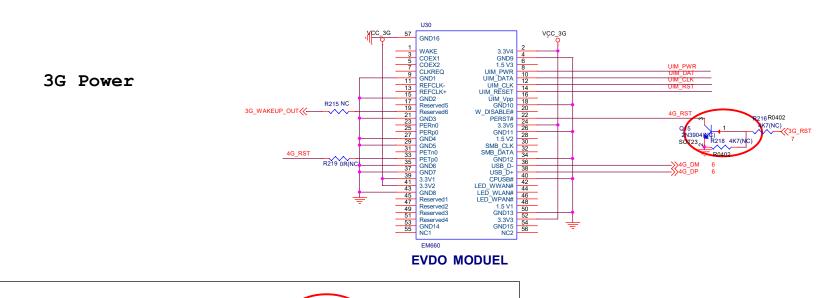


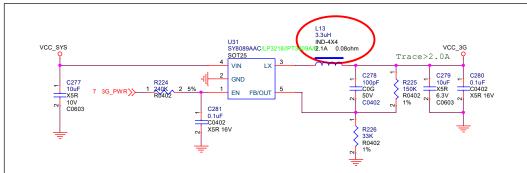


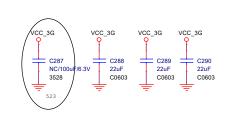


Rockchip ^{瑞芯微电子} 福 州	瑞	芯	徹	电栅司	
Title: LCM-eDP Panel					
File: RK3288_V10			RE	V: V0.1	
Create Date: Tuesday, April 30, 2019		Page	Num:	22	
Modific Data: Thursday May 00, 2010		Page Total-34			

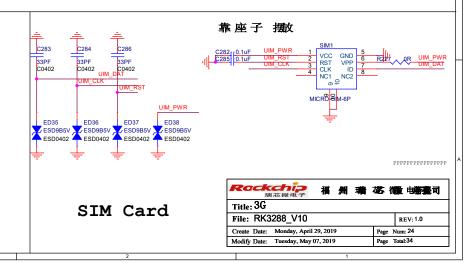


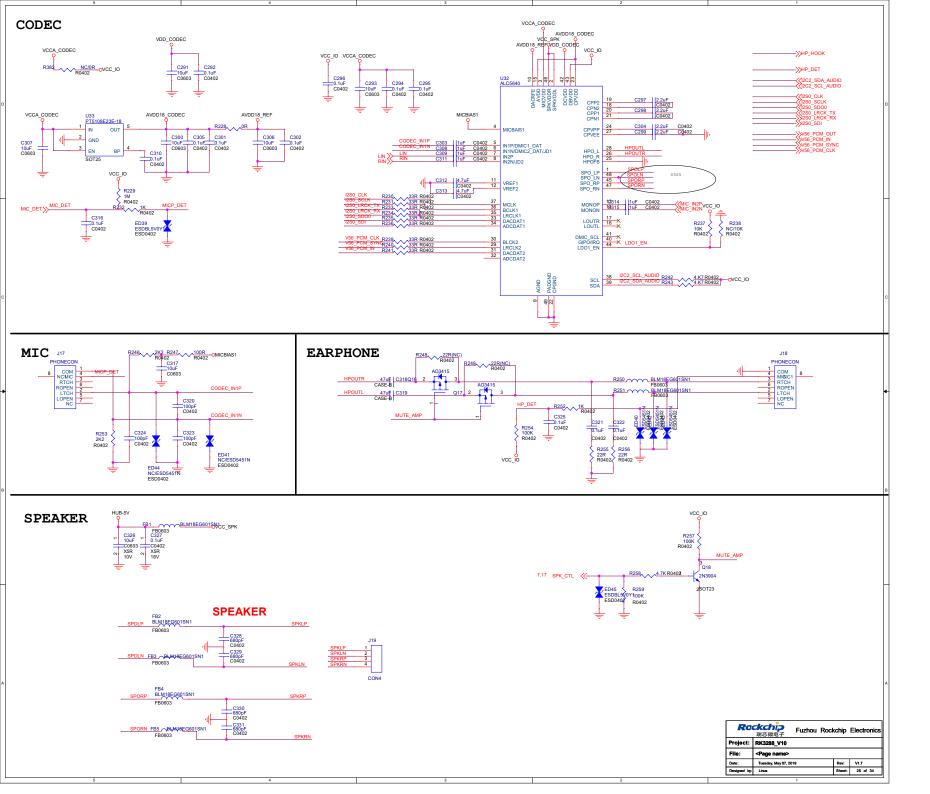








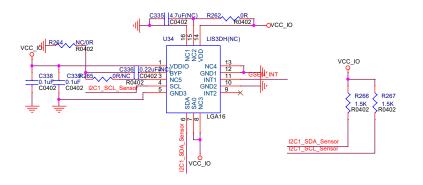




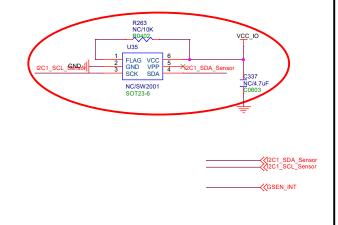
G-sensor

LSM303D with 3D Gsensor and E-compass

	LIS3DH	MMA8452Q	LSM303D
C8001	NC	NC	4.7uF
R8001	Oohm	NC	NC
R8002	NC	0ohm	NC
C8004	NC	0.luF	0.22uF
R8003	NC	NC	0R
R8004	NC	NC	0R



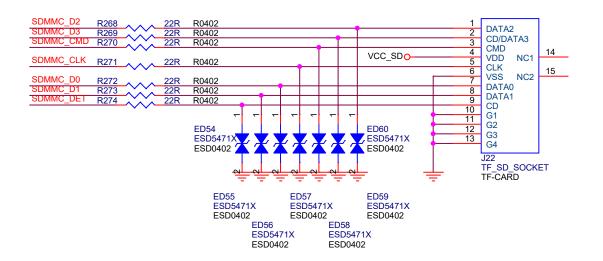
Compass

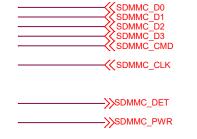


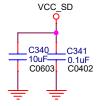
Title: Sensor/VIB
File: RK3288_V10
Create Date: Wednesday, February 12, 2014
Modify Date: Tuesday, May 07, 2019
Page Total: 34

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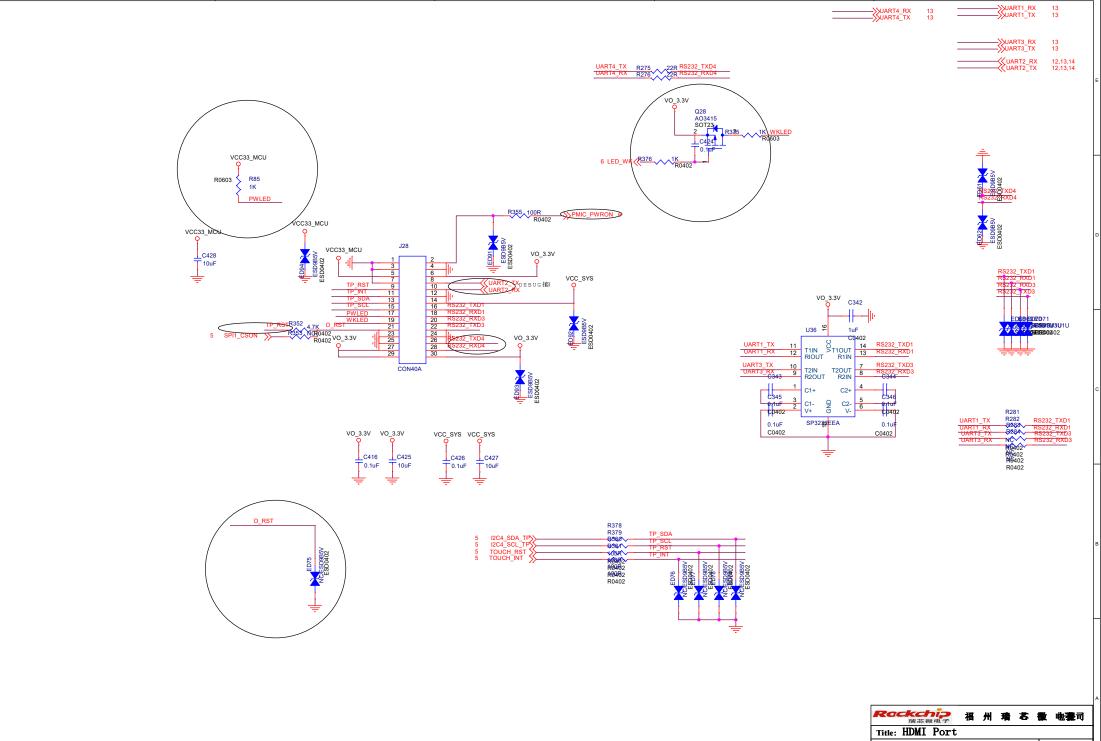




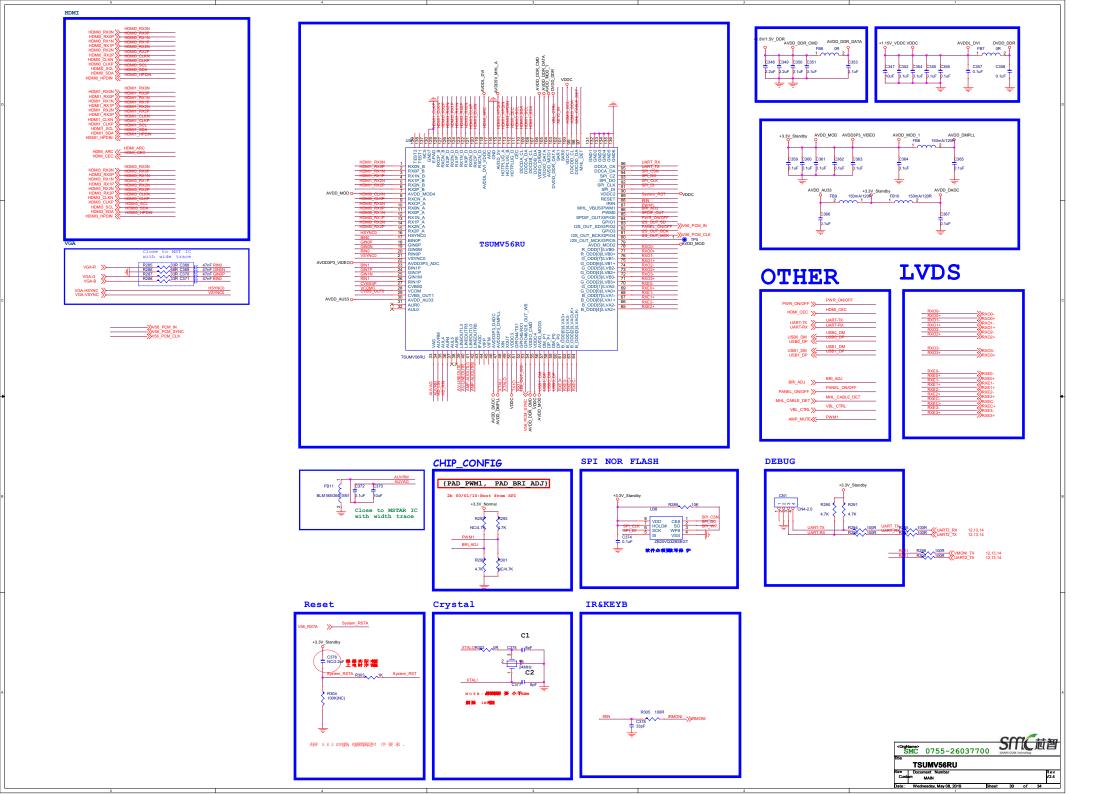


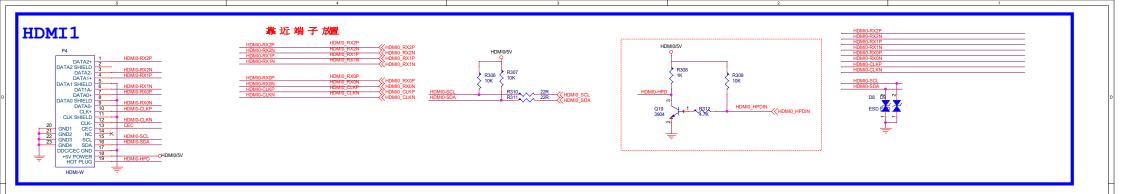


Rock	Chip 芯微电子	福	州	瑞	芯	黴	电震司	
Title: TF	` Card							
File: RK	3288_V10					RE	V: V0.1	
Create Date:	Wednesday, F	ebruary	12, 20)14	Page	Num:	28	
Modify Date:	Tuesday, May	7 07, 201	9		Page	Total:	34	

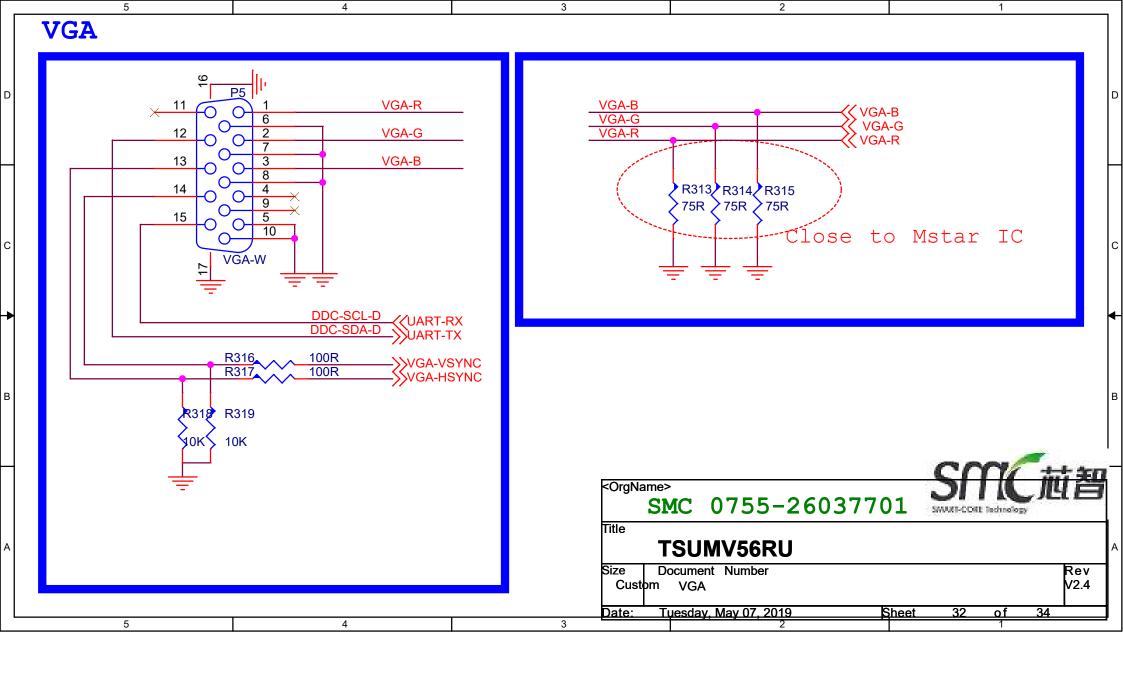


File: RK3288_V10 REV: V0.1 Create Date: Thursday, January 02, 2014 Page Num: 29 Modify Date: Tuesday, May 07, 2019 Page Total: 34



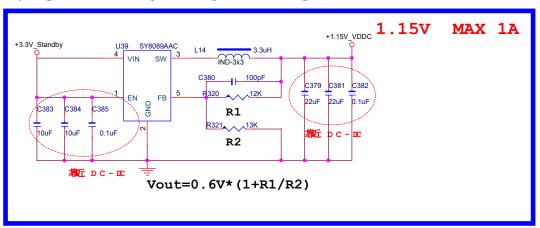






5 4 3 2 1

1.15V 1A Max for VDDC



Power for DDR

