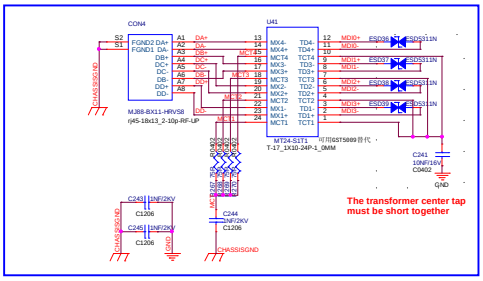
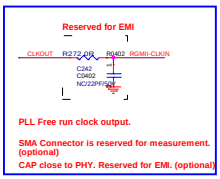
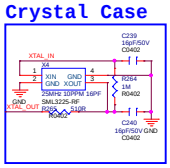
RJ45 & Transformer

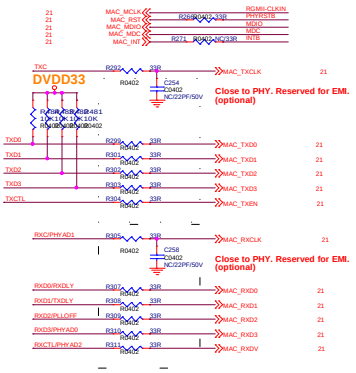


RSET

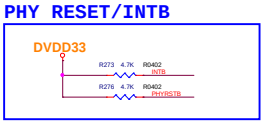


Crystal Case

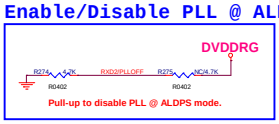




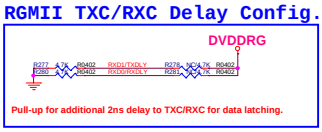
PHY RESET/INTB



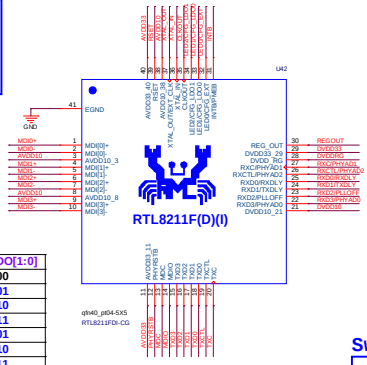
Enable/Disable PLL @ ALDPS



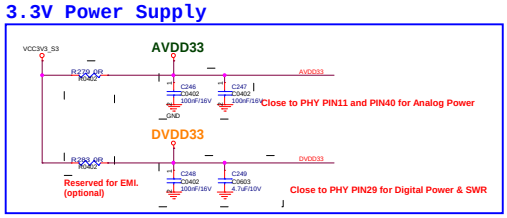
RGMII TXC/RXC Delay Config.



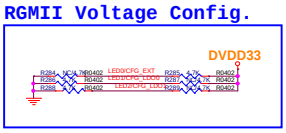
MDIO



3.3V Power Supply

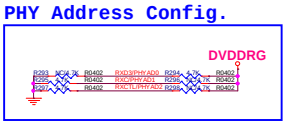


RGMII Voltage Config.



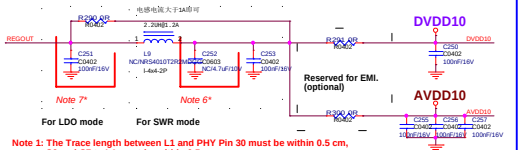
RGMII Power Source	CFG_EXT	CFG_LDO[1:0]
External 3.3V (default)	1'b1	2'b00
External 2.5V	1'b1	2'b01
External 1.8V	1'b1	2'b10
External 1.5V	1'b1	2'b11
Internal 2.5V	1'b0	2'b01
Internal 1.8V	1'b0	2'b10
Internal 1.5V	1'b0	2'b11

PHY Address Config.



PHY Address	PHYAD[2:0]
0	3'b000
1 (default)	3'b001
2	3'b010
3	3'b011
4	3'b100
5	3'b101
6	3'b110
7	3'b111

Switching Regulator



Note 1: The Trace length between L1 and PHY Pin 30 must be within 0.5 cm, C6 and C7 to L1 must be within 0.5cm.

Note 2: Bypass CAPs close to PHY DVDD10/AVDD10 power pins.

Note 3: Any inductance or bead except L1 is not allowed on the path from REGOUT to DVDD10/AVDD10.

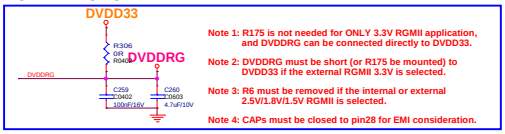
Note 4: R3 is reserved to change the DVDD10/AVDD10 supply source to LDO mode (RTL8211FD).

Note 5: No design change of PCB model is needed if R3 is reserved. If only RTL8211FD used for particular PCB model, directly short REGOUT to DVDD10/AVDD10.

Note 6: If RTL8211FI is selected, the C6 should be replaced as 10uF X7R capacitor for industrial grade application. Please refer to the datasheet for other industrial grade information.

Note 7: If RTL8211FDI is selected, the C5 should be replaced for industrial grade application and the value is still under testing.

RGMII Power



Jawest Electronic Technology Co.,Ltd			
Design Name: JWS3398-MAIN			
Size: C	Page Name: 10/100/1000 PHY (NET)		Rev: V1.1
Date: Wednesday, April 17, 2019	Sheet: 17	of: 21	