

1. Description

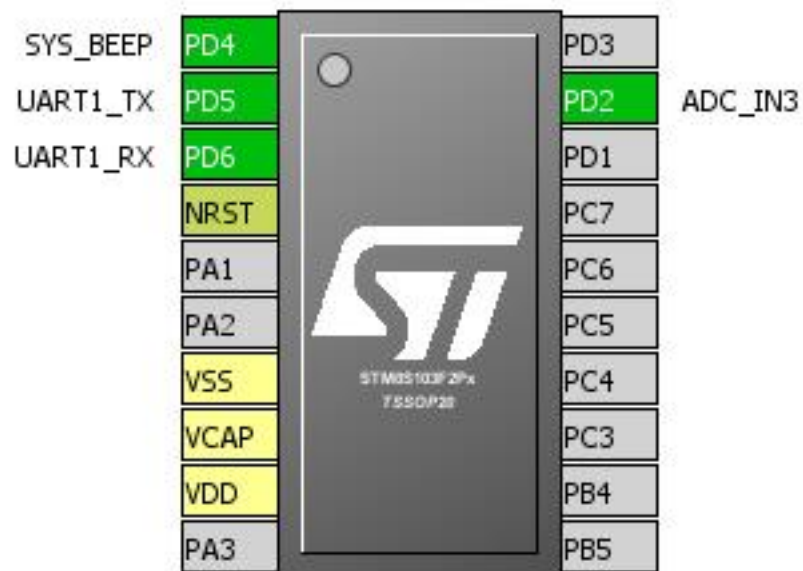
1.1. Project

Project Name	STM-Test
Board Name	No information
Generated with:	STM8CubeMX 1.2.0
Date	02/26/2018

1.2. MCU

MCU Series	STM8S
MCU Line	STM8S103/105
MCU name	STM8S103F2Px
MCU Package	TSSOP20
MCU Pin number	20

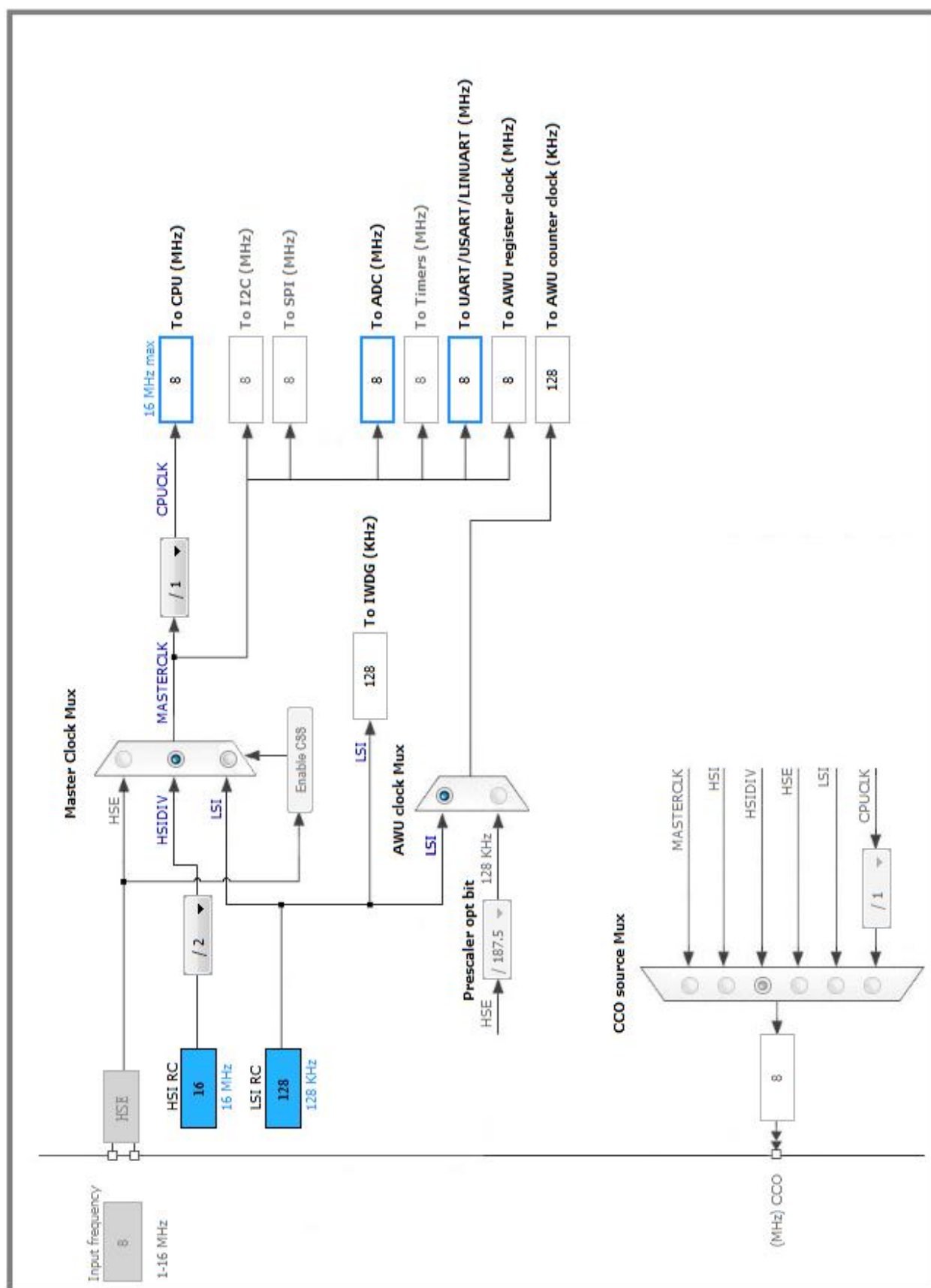
2. Pinout Configuration



3. Pins Configuration

Pin Number TSSOP20	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	PD4	I/O	SYS_BEEP	
2	PD5	I/O	UART1_TX	
3	PD6	I/O	UART1_RX	
4	NRST	Reset		
7	VSS	Power		
8	VCAP	Power		
9	VDD	Power		
19	PD2	I/O	ADC_IN3	

4. Clock Tree Configuration



5. Power Consumption Calculator report

5.1. Microcontroller Selection

Series	STM8S
Line	STM8S103/105
MCU	STM8S103F2Px
Datasheet	15441_Rev13

5.2. Parameter Selection

Temperature	25
Vdd	3.3

5.3. Sequence

Step	Step1
Mode	RUN
Vdd	3.3
Voltage Source	Battery
Range	No Scale
Fetch Type	FLASH
Clock Configuration	HSE
Clock Source Frequency	16.0 MHz
CPU Frequency	16.0 MHz
Peripherals	ADC UART1
Additional Cons.	0 mA
Average Current	5.12 mA
Duration	1 ms
DMIPS	16.0
Ta Max	103.58
Category	In DS Table

5.4. RESULTS

Sequence Time	1 ms	Average Current	5.12 mA
Battery Life	0	Average DMIPS	16.0 DMIPS

5.5. Chart

