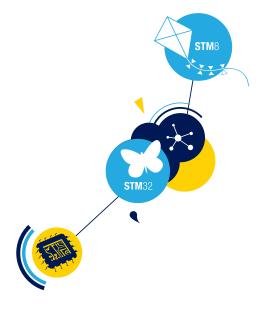


# 8- and 32-bit microcontrollers



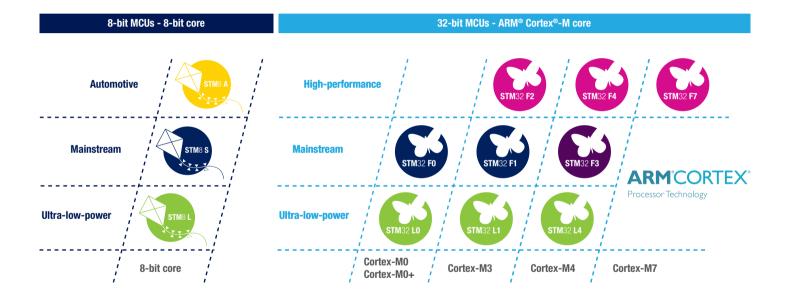


**Product selection guide** 



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# STM32 - 32-bit microcontroller families

				Timer 1	unctions						Ser	ial interf	ace				Supply (lo		
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Package	16-/32-bit timers	Others	ADC external channels	DAC channels	I/Os	SPI	I <sup>2</sup> S	I <sup>2</sup> C	USART	CEC	USB FS	CAN 2.0B	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Maximum operating temperature range (°C)
						STM	32F0x0 Valu	e line -	48 MH	z CPU									
STM32F030F4	16	4	TSS0P20	4x16-bit		9x12-bit		15	1		1	1				2.4 to 3.6	3.4	250	-40 to +85
STM32F030C6	32	4	LQFP48	4x16-bit		10x12-bit		39	1		1	1				2.4 to 3.6	3.4	250	-40 to +85
STM32F030K6	32	4	LQFP32	4x16-bit		10x12-bit		26	1		1	1				2.4 to 3.6	3.4	250	-40 to +85
STM32F070F6	32	6	TSS0P20	5x16-bit		9x12-bit		15	1	0	1	2		1		2.4 to 3.6	1.7	264	-40 to +85
STM32F070C6	32	6	LQFP48	5x16-bit	2 x WDG.	10x12-bit		37	1	0	1	2		1		2.4 to 3.6	1.7	264	-40 to +85
STM32F030C8	64	8	LQFP48	6x16-bit	RTC, 24-bit	10x12-bit		39	2		2	2				2.4 to 3.6	3.4	250	-40 to +85
STM32F030R8	64	8	LQFP64	6x16-bit	downcounter	16x12-bit		55	2		2	2				2.4 to 3.6	3.4	250	-40 to +85
STM32F070CB	128	16	LQFP48	8x16-bit		10x12-bit		37	2	0	2	4		1		2.4 to 3.6	1.8	273	-40 to +85
STM32F070RB	128	16	LQFP64	8x16-bit		16x12-bit		51	2	0	2	4		1		2.4 to 3.6	1.8	273	-40 to +85
STM32F030CC	256	32	LQFP48	8x16-bit		10x12-bit		38	2	0	2	4				2.4 to 3.6	1.8	306	-40 to +85
STM32F030RC	256	32	LQFP64	8x16-bit		16x12-bit		52	2	0	2	6				2.4 to 3.6	1.8	306	-40 to +85

				Timer 1	functions						Sei	ial interl	face				Supply (Ic		
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Package	16-/32-bit timers	Others	ADC external channels	DAC channels	I/Os	SPI	I <sup>2</sup> S	I <sup>2</sup> C	USART	CEC	USB FS	CAN 2.0B	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Maximum operating temperature range (°C)
				,	'	Si	M32F0x1 li	ine - 48	MHz C	PU	'								
STM32F031C4	16	4	LQFP48	5x16-bit / 1x32-bit		10x12-bit		39	1	1	1	1				2.0 to 3.6	1.7	250	-40 to +105
STM32F031F4	16	4	TSS0P20	5x16-bit / 1x32-bit		9x12-bit		15	1	1	1	1				2.0 to 3.6	1.7	250	-40 to +105
STM32F031G4	16	4	UFQFPN28	5x16-bit / 1x32-bit		10x12-bit		23	1	1	1	1				2.0 to 3.6	1.7	250	-40 to +105
STM32F031K4	16	4	LQFP32 UFQFPN32	5x16-bit / 1x32-bit		10x12-bit		25/27	1	1	1	1				2.0 to 3.6	1.7	250	-40 to +105
STM32F051C4	16	8	LQFP48 UFQFPN48	7x16-bit / 1x32-bit		10x12-bit	1x12-bit	39	1	1	1	1	1			2.0 to 3.6	1.7	250	-40 to +105
STM32F051K4	16	8	LQFP32 UFQFPN32	7x16-bit / 1x32-bit		10x12-bit	1x12-bit	27	1	1	1	1	1			2.0 to 3.6	1.7	250	-40 to +105
STM32F051R4	16	8	LQFP64 UFBGA64	7x16-bit / 1x32-bit		16x12-bit	1x12-bit	55	1	1	1	1	1			2.0 to 3.6	1.7	250	-40 to +105
STM32F031C6	32	4	LQFP48	5x16-bit / 1x32-bit	2 x WDG, RTC. 24-bit	10x12-bit		39	1	1	1	1				2.0 to 3.6	1.7	250	-40 to +105
STM32F031E6	32	4	WLCSP25	5x16-bit / 1x32-bit	downcounter	10x12-bit		20	1	1	1	1				2.0 to 3.6	1.7	250	-40 to +105
STM32F031F6	32	4	TSS0P20	5x16-bit / 1x32-bit		9x12-bit		15	1	1	1	1				2.0 to 3.6	1.7	250	-40 to +105
STM32F031G6	32	4	UFQFPN28	5x16-bit / 1x32-bit		10x12-bit		23	1	1	1	1				2.0 to 3.6	1.7	250	-40 to +105
STM32F031K6	32	4	LQFP32 UFQFPN32	5x16-bit / 1x32-bit		10x12-bit		25/27	1	1	1	1				2.0 to 3.6	1.7	250	-40 to +105
STM32F051C6	32	8	LQFP48 UFQFPN48	7x16-bit / 1x32-bit		10x12-bit	1x12-bit	39	1	1	1	2	1			2.0 to 3.6	1.7	250	-40 to +105
STM32F051K6	32	8	LQFP32 UFQFPN32	7x16-bit / 1x32-bit		10x12-bit	1x12-bit	27	1	1	1	2	1			2.0 to 3.6	1.7	250	-40 to +105
STM32F051R6	32	8	LQFP64 UFBGA64	7x16-bit / 1x32-bit		16x12-bit	1x12-bit	55	1	1	1	2	1			2.0 to 3.6	1.7	250	-40 to +105
STM32F051C8	64	8	LQFP48 UFQFPN48	7x16-bit / 1x32-bit		10x12-bit	1x12-bit	39	2	1	2	2	1			2.0 to 3.6	1.7	250	-40 to +105

				Timer f	unctions						Sei	ial interl	ace				Supply (Ic		
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Package	16-/32-bit timers	Others	ADC external channels	DAC channels	I/Os	SPI	I <sup>2</sup> S	I <sup>2</sup> C	USART	CEC	USB FS	CAN 2.0B	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Maximum operating temperature range (°C)
STM32F051K8	64	8	LQFP32 UFQFPN32	7x16-bit / 1x32-bit		10x12-bit	1x12-bit	27	1	1	1	2	1			2.0 to 3.6	1.7	250	-40 to +105
STM32F051R8	64	8	LQFP64 UFBGA64	7x16-bit / 1x32-bit		16x12-bit	1x12-bit	55	2	1	2	2	1			2.0 to 3.6	1.7	250	-40 to +105
STM32F051T8	64	8	WLCSP36	7x16-bit / 1x32-bit		10x12-bit	2x12-bit	29	2	1	1	2	1			2.0 to 3.6	1.7	250	-40 to +105
STM32F071V8	64	16	LQFP100 UFBGA100	8x16-bit / 1x32-bit		16x12-bit	2x12-bit	87	2	2	2	4	1			2.0 to 3.6	1.8	260	-40 to +105
STM32F071CB	128	16	LQFP48 UFQFPN48 WLCSP49	8x16-bit / 1x32-bit		10x12-bit	2x12-bit	37	2	2	2	4	1			2.0 to 3.6	1.8	260	-40 to +105
STM32F071RB	128	16	LQFP64	8x16-bit / 1x32-bit	2 x WDG.	16x12-bit	2x12-bit	51	2	2	2	4	1			2.0 to 3.6	1.8	260	-40 to +105
STM32F071VB	128	16	LQFP100 UFBGA100	8x16-bit / 1x32-bit	RTC, 24-bit downcounter	16x12-bit	2x12-bit	87	2	2	2	4	1			2.0 to 3.6	1.8	260	-40 to +105
STM32F091VB	128	32	LQFP100 UFBGA100	8x16-bit / 1x32-bit		16x12-bit	2x12-bit	88	2	2	2	8	1		1	2.0 to 3.6	1.8	306	-40 to +105
STM32F091RB	128	32	LQFP64 UFBGA64 WLCSP64	8x16-bit / 1x32-bit		16x12-bit	2x12-bit	52	2	2	2	8	1		1	2.0 to 3.6	1.8	306	-40 to +105
STM32F091CB	128	32	LQFP48 UFQFPN48	8x16-bit / 1x32-bit		10x12-bit	2x12-bit	38	2	2	2	6	1		1	2.0 to 3.6	1.8	306	-40 to +105
STM32F091VC	256	32	LQFP100 UFBGA100	8x16-bit / 1x32-bit		16x12-bit	2x12-bit	88	2	2	2	8	1		1	2.0 to 3.6	1.8	306	-40 to +105
STM32F091RC	256	32	LQFP64 UFBGA64 WLCSP64	8x16-bit / 1x32-bit		16x12-bit	2x12-bit	52	2	2	2	8	1		1	2.0 to 3.6	1.8	306	-40 to +105
STM32F091CC	256	32	LQFP48 UFQFPN48	8x16-bit / 1x32-bit		10x12-bit	2x12-bit	38	2	2	2	6	1		1	2.0 to 3.6	1.8	306	-40 to +105
						STM32F0x2	line - 48 M	Hz CPU	with U	SB and	CAN								
STM32F042F4	16	6	TSS0P20	5x16-bit / 1x32-bit	2 x WDG, RTC. 24-bit	9x12-bit		16	1	1	1	2	1	1	1	2.0 to 3.6	1.7	250	-40 to +105
STM32F042G4	16	6	UFQFPN28	5x16-bit / 1x32-bit	downcounter	10x12-bit		24	1	1	1	2	1	1	1	2.0 to 3.6	1.7	250	-40 to +105

				Timer 1	functions						Ser	ial interf	ace				Supply (lc		
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Package	16-/32-bit timers	Others	ADC external channels	DAC channels	I/Os	SPI	I <sup>2</sup> S	I <sup>2</sup> C	USART	CEC	USB FS	CAN 2.0B	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Maximum operating temperature range (°C)
STM32F042K4	16	6	LQFP32 UFQFPN32	5x16-bit / 1x32-bit		10x12-bit		28	1	1	1	2	1	1	1	2.0 to 3.6	1.7	250	-40 to +105
STM32F042C4	16	6	LQFP48 UFQFPN48	5x16-bit / 1x32-bit		10x12-bit		38	2	1	1	2	1	1	1	2.0 to 3.6	1.7	250	-40 to +105
STM32F042F6	32	6	TSS0P20	5x16-bit / 1x32-bit		9x12-bit		16	1	1	1	2	1	1	1	2.0 to 3.6	1.7	250	-40 to +105
STM32F042G6	32	6	UFQFPN28	5x16-bit / 1x32-bit		10x12-bit		24	1	1	1	2	1	1	1	2.0 to 3.6	1.7	250	-40 to +105
STM32F042K6	32	6	LQFP32 UFQFPN32	5x16-bit / 1x32-bit		10x12-bit		28	1	1	1	2	1	1	1	2.0 to 3.6	1.7	250	-40 to +105
STM32F042T6	32	6	WLCSP36	5x16-bit / 1x32-bit		10x12-bit		30	1	1	1	2	1	1	1	2.0 to 3.6	1.7	250	-40 to +105
STM32F042C6	32	6	LQFP48 UFQFPN48	5x16-bit / 1x32-bit	2 x WDG, RTC, 24-bit	10x12-bit		38	2	1	1	2	1	1	1	2.0 to 3.6	1.7	250	-40 to +105
STM32F072C8	64	16	LQFP48 UFQFPN48 WLCSP49	8x16-bit / 1x32-bit	downcounter	10x12-bit	2x12-bit	37	2	1	2	4	1	1	1	2.0 to 3.6	1.8	260	-40 to +105
STM32F072R8	64	16	LQFP64	8x16-bit / 1x32-bit		16x12-bit	2x12-bit	51	2	1	2	4	1	1	1	2.0 to 3.6	1.8	260	-40 to +105
STM32F072V8	64	16	LQFP100	8x16-bit / 1x32-bit		16x12-bit	2x12-bit	87	2	1	2	4	1	1	1	2.0 to 3.6	1.8	260	-40 to +105
STM32F072CB	128	16	LQFP48 UFQFPN48 WLCSP49	8x16-bit / 1x32-bit		10x12-bit	2x12-bit	37	2	1	2	4	1	1	1	2.0 to 3.6	1.8	260	-40 to +105
STM32F072RB	128	16	LQFP64 BGA64	8x16-bit / 1x32-bit		16x12-bit	2x12-bit	51	2	1	2	4	1	1	1	2.0 to 3.6	1.8	260	-40 to +105
STM32F072VB	128	16	LQFP100 UFBGA100	8x16-bit / 1x32-bit		16x12-bit	2x12-bit	87	2	1	2	4	1	1	1	2.0 to 3.6	1.8	260	-40 to +105
						STM32	F0x8 line - 4	48 MHz	CPU w	ith USB									
STM32F038C6	32	4	LQFP48 UFQFPN48	5x16-bit / 1x32-bit	2 x WDG, RTC. 24-bit	9x12-bit		38	1	1	1	1				1.65 to 1.95	1.7	250	-40 to +105
STM32F038E6	32	4	WLCSP25	5x16-bit / 1x32-bit	downcounter	10x12-bit		20	1	1	1	1				1.65 to 1.95	1.7	250	-40 to +105

				Timer f	unctions						Ser	ial interf	ace				Supply (lo		
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Package	16-/32-bit timers	Others	ADC external channels	DAC channels	I/Os	SPI	I <sup>2</sup> S	I <sup>2</sup> C	USART	CEC	USB FS	CAN 2.0B	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Maximum operating temperature range (°C)
STM32F038F6	32	4	TSS0P20	5x16-bit / 1x32-bit		9x12-bit		14	1	1	1	1				1.65 to 1.95	1.7	250	-40 to +105
STM32F038G6	32	4	UFQFPN28	5x16-bit / 1x32-bit		10x12-bit		22	1	1	1	1				1.65 to 1.95	1.7	250	-40 to +105
STM32F038K6	32	4	UFQFPN32	5x16-bit / 1x32-bit		10x12-bit		26	1	1	1	1				1.65 to 1.95	1.7	250	-40 to +105
STM32F048C6	32	6	LQFP48 UFQFPN48	5x16-bit / 1x32-bit		10x12-bit		38	2	1	1	2	1	1	1	1.65 to 1.95	1.7	250	-40 to +105
STM32F048G6	32	6	UFQFPN28	5x16-bit / 1x32-bit		10x12-bit		24	1	1	1	2	1	1	1	1.65 to 1.95	1.7	250	-40 to +105
STM32F048T6	32	6	WLCSP36	5x16-bit / 1x32-bit		10x12-bit		30	1	1	1	2	1	1	1	1.65 to 1.95	1.7	250	-40 to +105
STM32F058C8	64	8	UFQFPN48	7x16-bit / 1x32-bit	2 x WDG.	10x12-bit	2x12-bit	39	2	1	2	2	1			1.65 to 1.95	1.7	250	-40 to +105
STM32F058R8	64	8	LQFP64	7x16-bit / 1x32-bit	RTC, 24-bit downcounter	16x12-bit	2x12-bit	55	2	1	2	2	1			1.65 to 1.95	1.7	250	-40 to +105
STM32F058T8	64	8	WLCSP36	7x16-bit / 1x32-bit	downloaditor	10x12-bit	2x12-bit	29	2	1	1	2	1			1.65 to 1.95	1.7	250	-40 to +105
STM32F078CB	128	16	LQFP48 WLCSP49	9x16-bit / 1x32-bit		10x12-bit	2x12-bit	36	2	2	2	4	1	1	1	1.65 to 1.95	1.7	250	-40 to +105
STM32F078RB	128	16	BGA64 LQFP64	9x16-bit / 1x32-bit		16x12-bit	2x12-bit	50	2	2	2	4	1	1	1	1.65 to 1.95	1.8	260	-40 to +105
STM32F078VB	128	16	BGA100 LQFP100	9x16-bit / 1x32-bit		16x12-bit	2x12-bit	86	2	2	2	4	1	1	1	1.65 to 1.95	1.7	260	-40 to +105
STM32F098CC	256	32	LQFP48 UQFN48	8x16-bit / 1x32-bit		10x12-bit	2x12-bit	37	2	2	2	6	1		1	1.65 to 1.95	1.8	306	-40 to +105
STM32F098RC	256	32	BGA64 LQFP64 WLCSP64	8x16-bit / 1x32-bit		16x12-bit	2x12-bit	51	2	2	2	8	1		1	1.65 to 1.95	1.8	306	-40 to +105
STM32F098VC	256	32	BGA100 LQFP100	8x16-bit / 1x32-bit		16x12-bit	2x12-bit	87	2	2	2	8	1		1	1.65 to 1.95	1.8	306	-40 to +105

				Timer 1	functions							Se	rial ii	nterfac	е				Supply (Ic		Manianana
Part nmber	Flash size (Kbytes)	Internal RAM size (Kbytes)	Package	16-bit timers	Others	ADC	DAC	I/Os	SPI	I <sup>2</sup> S	I <sup>2</sup> C	USART + UART¹	CEC	USB FS	CAN 2.0B	SDIO	Ethernet MAC10 /100	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Maximum operating temperature range (°C)
						STN	32F100 Va	lue line	- 24	MHz	CPU										
STM32F100C4	16	4	LQFP48	6x16-bit		10x12-bit	2x12-bit	37	1		1	2	1					2 to 3.6	1.7	358	-40 to +105
STM32F100R4	16	4	LQFP64 TFBGA64	6x16-bit		16x12-bit	2x12-bit	51	1		1	2	1					2 to 3.6	1.7	358	-40 to +105
STM32F100C6	32	4	LQFP48	6x16-bit		10x12-bit	2x12-bit	37	1		1	2	1					2 to 3.6	1.7	358	-40 to +105
STM32F100R6	32	4	LQFP64 TFBGA64	6x16-bit		16x12-bit	2x12-bit	51	1		1	2	1					2 to 3.6	1.7	358	-40 to +105
STM32F100C8	64	8	LQFP48	7x16-bit		10x12-bit	2x12-bit	37	2		2	3	1					2 to 3.6	1.7	358	-40 to +105
STM32F100R8	64	8	LQFP64 TFBGA64	7x16-bit		16x12-bit	2x12-bit	51	2		2	3	1					2 to 3.6	1.7	358	-40 to +105
STM32F100V8	64	8	LQFP100	7x16-bit		16x12-bit	2x12-bit	80	2		2	3	1					2 to 3.6	1.7	358	-40 to +105
STM32F100CB	128	8	LQFP48	7x16-bit	2 x WDG,	10x12-bit	2x12-bit	37	2		2	3	1					2 to 3.6	1.7	358	-40 to +105
STM32F100RB	128	8	LQFP64 TFBGA64	7x16-bit	RTC, 24-bit downcounter	16x12-bit	2x12-bit	51	2		2	3	1					2 to 3.6	1.7	358	-40 to +105
STM32F100VB	128	8	LQFP100	7x16-bit	downcounter	16x12-bit	2x12-bit	80	2		2	3	1					2 to 3.6	1.7	358	-40 to +105
STM32F100RC	256	24	LQFP64	11x16-bit		16x12-bit	2x12-bit	51	3		2	3+2	1					2 to 3.6	2.2	396	-40 to +105
STM32F100VC	256	24	LQFP100	11x16-bit		16x12-bit	2x12-bit	80	3		2	3+2	1					2 to 3.6	2.2	396	-40 to +105
STM32F100ZC	256	24	LQFP144	11x16-bit		16x12-bit	2x12-bit	112	3		2	3+2	1					2 to 3.6	2.2	396	-40 to +105
STM32F100RD	384	32	LQFP64	11x16-bit		16x12-bit	2x12-bit	51	3		2	3+2	1					2 to 3.6	2.2	396	-40 to +105
STM32F100VD	384	32	LQFP100	11x16-bit		16x12-bit	2x12-bit	80	3		2	3+2	1					2 to 3.6	2.2	396	-40 to +105
STM32F100ZD	384	32	LQFP144	11x16-bit		16x12-bit	2x12-bit	112	3		2	3+2	1					2 to 3.6	2.2	396	-40 to +105
STM32F100RE	512	32	LQFP64	11x16-bit		16x12-bit	2x12-bit	51	3		2	3+2	1					2 to 3.6	2.2	396	-40 to +105
STM32F100VE	512	32	LQFP100	11x16-bit		16x12-bit	2x12-bit	80	3		2	3+2	1					2 to 3.6	2.2	396	-40 to +105
STM32F100ZE	512	32	LQFP144	11x16-bit		16x12-bit	2x12-bit	112	3		2	3+2	1					2 to 3.6	2.2	396	-40 to +105

				Timer 1	unctions							Se	erial ir	iterfac	e				Supply (		Maximum
Part nmber	Flash size (Kbytes)	Internal RAM size (Kbytes)	Package	16-bit timers	Others	ADC	DAC	I/Os	SPI	I²S	I <sup>2</sup> C	USART + UART¹	CEC	USB FS	CAN 2.0B	SDIO	Ethernet MAC10 /100	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	operating temperature range (°C)
						STM3	2F101 Acc	ess lin	e - 36	МН	CPU										
STM32F101C4	16	4	LQFP48	2x16-bit		10x12-bit		36	1		1	2						2 to 3.6	1.7	363	-40 to +105
STM32F101R4	16	4	LQFP64	2x16-bit		16x12-bit		51	1		1	2						2 to 3.6	1.7	363	-40 to +105
STM32F101T4	16	4	VFQFPN36	2x16-bit		10x12-bit		26	1		1	2						2 to 3.6	1.7	363	-40 to +105
STM32F101C6	32	6	LQFP48	2x16-bit		10x12-bit		36	1		1	2						2 to 3.6	1.7	363	-40 to +105
STM32F101R6	32	6	LQFP64	2x16-bit		16x12-bit		51	1		1	2						2 to 3.6	1.7	363	-40 to +105
STM32F101T6	32	6	VFQFPN36	2x16-bit		10x12-bit		26	1		1	2						2 to 3.6	1.7	363	-40 to +105
STM32F101C8	64	10	LQFP48	3x16-bit		10x12-bit		36	2		2	3						2 to 3.6	1.7	363	-40 to +105
STM32F101R8	64	10	LQFP64	3x16-bit		16x12-bit		51	2		2	3						2 to 3.6	1.7	391	-40 to +105
STM32F101T8	64	10	VFQFPN36	3x16-bit		10x12-bit		26	1		1	2						2 to 3.6	1.7	391	-40 to +105
STM32F101V8	64	10	LQFP100	3x16-bit		16x12-bit		80	2		2	3						2 to 3.6	1.7	391	-40 to +105
STM32F101CB	128	16	LQFP48 VFQFPN48	3x16-bit		10x12-bit		36	2		2	3						2 to 3.6	1.7	363	-40 to +105
STM32F101RB	128	16	LQFP64 TFBGA64	3x16-bit	2 x WDG,	16x12-bit		51	2		2	3						2 to 3.6	1.7	391	-40 to +105
STM32F101TB	128	16	VFQFPN36	3x16-bit	RTC, 24-bit	10x12-bit		26	1		1	2						2 to 3.6	1.7	391	-40 to +105
STM32F101VB	128	16	LQFP100	3x16-bit	downcounter	16x12-bit		80	2		2	3						2 to 3.6	1.7	391	-40 to +105
STM32F101RC	256	32	LQFP64	6x16-bit			2x12-bit	51	3		2	3+2						2 to 3.6	1.9	433	-40 to +105
STM32F101VC	256	32	LQFP100	6x16-bit		16x12-bit	2x12-bit	80	3		2	3+2						2 to 3.6	1.9	433	-40 to +105
STM32F101ZC	256	32	LQFP144	6x16-bit		16x12-bit	2x12-bit	112	3		2	3+2						2 to 3.6	1.9	433	-40 to +105
STM32F101RD	384	48	LQFP64	6x16-bit		16x12-bit	2x12-bit	51	3		2	3+2						2 to 3.6	1.9	433	-40 to +105
STM32F101VD	384	48	LQFP100	6x16-bit		16x12-bit	2x12-bit	80	3		2	3+2						2 to 3.6	1.9	433	-40 to +105
STM32F101ZD	384	48	LQFP144	6x16-bit		16x12-bit	2x12-bit	112	3		2	3+2						2 to 3.6	1.9	433	-40 to +105
STM32F101RE	512	48	LQFP64	6x16-bit		16x12-bit	2x12-bit	51	3		2	3+2						2 to 3.6	1.9	433	-40 to +105
STM32F101VE	512	48	LQFP100	6x16-bit			2x12-bit	80	3		2	3+2						2 to 3.6	1.9	433	-40 to +105
STM32F101ZE	512	48	LQFP144	6x16-bit		16x12-bit	2x12-bit	112	3		2	3+2						2 to 3.6	1.9	433	-40 to +105
STM32F101RF	768	80	LQFP64	12x16-bit		16x12-bit	2x12-bit	51	3		2	3+2						2 to 3.6	1.9	433	-40 to +105
STM32F101VF	768	80	LQFP100	12x16-bit		16x12-bit	2x12-bit	80	3		2	3+2						2 to 3.6	1.9	433	-40 to +105

				Timer f	unctions							Se	rial in	iterfac	e				Supply (		Mavimum
Part nmber	Flash size (Kbytes)	Internal RAM size (Kbytes)	Package	16-bit timers	Others	ADC	DAC	I/Os	SPI	I <sup>2</sup> S	I <sup>2</sup> C	USART + UART¹	CEC	USB FS	CAN 2.0B	SDI0	Ethernet MAC10 /100	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Maximum operating temperature range (°C)
STM32F101ZF	768	80	LQFP144	12x16-bit	0 11/00	16x12-bit	2x12-bit	112	3		2	3+2						2 to 3.6	1.9	433	-40 to +105
STM32F101RG	1024	80	LQFP64	12x16-bit	2 x WDG, RTC, 24-bit	16x12-bit	2x12-bit	51	3		2	3+2						2 to 3.6	1.9	433	-40 to +105
STM32F101VG	1024	80	LQFP100	12x16-bit	downcounter	16x12-bit	2x12-bit	80	3		2	3+2						2 to 3.6	1.9	433	-40 to +105
STM32F101ZG	1024	80	LQFP144	12x16-bit		16x12-bit	2x12-bit	112	3		2	3+2						2 to 3.6	1.9	433	-40 to +105
						STM3	2F102 USE	Acces	s lin	e - 48	MHz										
STM32F102C4	16	4	LQFP48	2x16-bit		10x12-bit		36	1		1	2		1				2 to 3.6	1.55	348	-40 to +105
STM32F102R4	16	4	LQFP64	2x16-bit		16x12-bit		51	1		1	2		1				2 to 3.6	1.55	348	-40 to +105
STM32F102C6	32	6	LQFP48	2x16-bit	0 14/00	10x12-bit		36	1		1	2		1				2 to 3.6	1.55	348	-40 to +105
STM32F102R6	32	6	LQFP64	2x16-bit	2 x WDG, RTC, 24-bit	16x12-bit		51	1		1	2		1				2 to 3.6	1.55	348	-40 to +105
STM32F102C8	64	10	LQFP48	3x16-bit	downcounter	10x12-bit		36	2		2	3		1				2 to 3.6	1.7	373	-40 to +105
STM32F102R8	64	10	LQFP64	3x16-bit	downloaditter	16x12-bit		51	2		2	3		1				2 to 3.6	1.7	373	-40 to +105
STM32F102CB	128	16	LQFP48	3x16-bit		10x12-bit		36	2		2	3		1				2 to 3.6	1.7	373	-40 to +105
STM32F102RB	128	16	LQFP64	3x16-bit		16x12-bit		51	2		2	3		1				2 to 3.6	1.7	373	-40 to +105
						STM32F	103 Perfor	mance	line -	- 72 N	/IHz (	CPU									
STM32F103C4	16	6	LQFP48	3x16-bit		10x12-bit		36	1		1	2		1	1			2 to 3.6	1.55	337	-40 to +105
STM32F103R4	16	6	LQFP64 TFBGA64	3x16-bit		16x12-bit		51	1		1	2		1	1			2 to 3.6	1.55	337	-40 to +105
STM32F103T4	16	6	VFQFPN36	3x16-bit		10x12-bit		26	1		1	2		1	1			2 to 3.6	1.55	337	-40 to +105
STM32F103C6	32	10	LQFP48 VFQFPN48	3x16-bit		10x12-bit		36	1		1	2		1	1			2 to 3.6	1.55	337	-40 to +105
STM32F103R6	32	10	LQFP64 TFBGA64	3x16-bit	2 x WDG.	16x12-bit		51	1		1	2		1	1			2 to 3.6	1.55	337	-40 to +105
STM32F103T6	32	10	VFQFPN36	3x16-bit	RTC, 24-bit	10x12-bit		26	1		1	2		1	1			2 to 3.6	1.55	373	-40 to +105
STM32F103C8	64	20	LQFP48	4x16-bit	downcounter	10x12-bit		36	2		2	3		1	1			2 to 3.6	1.7	373	-40 to +105
STM32F103R8	64	20	LOFP64 TFBGA64	4x16-bit		16x12-bit		51	2		2	3		1	1			2 to 3.6	1.7	373	-40 to +105
STM32F103T8	64	20	VFQFPN36	4x16-bit		10x12-bit		26	1		1	2		1	1			2 to 3.6	1.7	373	-40 to +105
STM32F103V8	64	20	LFBGA100 LQFP100	4x16-bit		16x12-bit		80	2		2	3		1	1			2 to 3.6	1.7	373	-40 to +105
STM32F103CB	128	20	LQFP48 VFQFPN48	4x16-bit		10x12-bit		36	2		2	3		1	1			2 to 3.6	1.7	373	-40 to +105

				Timer 1	unctions							Se	rial ir	iterfac	e				Supply (Ic		Maximum
Part nmber	Flash size (Kbytes)	Internal RAM size (Kbytes)	Package	16-bit timers	Others	ADC	DAC	I/Os	SPI	I <sup>2</sup> S	I <sup>2</sup> C	USART + UART¹	CEC	USB FS	CAN 2.0B	SDIO	Ethernet MAC10 /100	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	operating temperature range (°C)
STM32F103RB	128	20	LQFP64 TFBGA64	4x16-bit		16x12-bit		51	2		2	3		1	1			2 to 3.6	1.7	373	-40 to +105
STM32F103TB	128	20	VFQFPN36	4x16-bit		10x12-bit		26	1		1	2		1	1			2 to 3.6	1.7	373	-40 to +105
STM32F103VB	128	20	LFBGA100 LQFP100	4x16-bit		16x12-bit		80	2		2	3		1	1			2 to 3.6	1.7	373	-40 to +105
STM32F103RC	256	48	LQFP64 WLCSP64	8x16-bit		16x12-bit	2x12-bit	51	3	2	2	3+2		1	1	1		2 to 3.6	1.9	421	-40 to +105
STM32F103VC	256	48	LFBGA100 LQFP100	8x16-bit		16x12-bit	2x12-bit	80	3	2	2	3+2		1	1	1		2 to 3.6	1.9	421	-40 to +105
STM32F103ZC	256	48	LFBGA144 LQFP144	8x16-bit		21x12-bit	2x12-bit	112	3	2	2	3+2		1	1	1		2 to 3.6	1.9	421	-40 to +105
STM32F103RD	384	64	LQFP64 WLCSP64	8x16-bit		16x12-bit	2x12-bit	51	3	2	2	3+2		1	1	1		2 to 3.6	1.9	421	-40 to +105
STM32F103VD	384	64	LFBGA100 LQFP100	8x16-bit	2 x WDG, RTC, 24-bit	16x12-bit	2x12-bit	80	3	2	2	3+2		1	1	1		2 to 3.6	1.9	421	-40 to +105
STM32F103ZD	384	64	LFBGA144 LQFP144	8x16-bit	downcounter	21x12-bit	2x12-bit	112	3	2	2	3+2		1	1	1		2 to 3.6	1.9	421	-40 to +105
STM32F103RE	512	64	LQFP64 WLCSP64	8x16-bit		16x12-bit	2x12-bit	51	3	2	2	3+2		1	1	1		2 to 3.6	1.9	421	-40 to +105
STM32F103VE	512	64	LFBGA100 LQFP100	8x16-bit		16x12-bit	2x12-bit	80	3	2	2	3+2		1	1	1		2 to 3.6	1.9	421	-40 to +105
STM32F103ZE	512	64	LFBGA144 LQFP144	8x16-bit		21x12-bit	2x12-bit	112	3	2	2	3+2		1	1	1		2 to 3.6	1.9	421	-40 to +105
STM32F103RF	768	96	LQFP64	12x16-bit		16x12-bit		51	3	2	2	3+2		1	1	1		2 to 3.6	1.9	421	-40 to +105
STM32F103VF	768	96	LQFP100	14x16-bit		16x12-bit	2x12-bit	80	3	2	2	3+2		1	1	1		2 to 3.6	1.9	421	-40 to +105
STM32F103ZF	768	96	LFBGA144 LQFP144	14x16-bit		21x12-bit	2x12-bit	112	3	2	2	3+2		1	1	1		2 to 3.6	1.9	421	-40 to +105
STM32F103RG	1024	96	LQFP64	12x16-bit		16x12-bit		51	3	2	2	3+2		1	1	1		2 to 3.6	1.9	421	-40 to +105
STM32F103VG	1024	96	LQFP100	14x16-bit		16x12-bit	2x12-bit	80	3	2	2	3+2		1	1	1		2 to 3.6	1.9	421	-40 to +105
STM32F103ZG	1024	96	LFBGA144 LQFP144	14x16-bit		21x12-bit	2x12-bit	112	3	2	2	3+2		1	1	1		2 to 3.6	1.9	421	-40 to +105

				Timer 1	unctions							Se	rial in	iterfac	e				Supply (Ic		Maximum
Part nmber	Flash size (Kbytes)	Internal RAM size (Kbytes)	Package	16-bit timers	Others	ADC	DAC	I/Os	SPI	I <sup>2</sup> S	I <sup>2</sup> C	USART + UART¹	CEC	USB FS	CAN 2.0B	SDIO	Ethernet MAC10 /100	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	operating temperature range (°C)
						STM32F10	5/107 Con	nectivi	ty line	- 72	MHz	<b>CPU</b>									
STM32F105R8	64	64	LQFP64	7x16-bit		16x12-bit	2x12-bit	51	3	2	2	3+2		OTG	2			2 to 3.6	1.9	393	-40 to +105
STM32F105V8	64	64	LQFP100	7x16-bit		16x12-bit	2x12-bit	80	3	2	2	3+2		OTG	2			2 to 3.6	1.9	393	-40 to +105
STM32F105RB	128	64	LQFP64	7x16-bit		16x12-bit	2x12-bit	51	3	2	2	3+2		OTG	2			2 to 3.6	1.9	393	-40 to +105
STM32F105VB	128	64	LFBGA100 LQFP100	7x16-bit	0 14/00	16x12-bit	2x12-bit	80	3	2	2	3+2		OTG	2			2 to 3.6	1.9	393	-40 to +105
STM32F107RB	128	64	LQFP64	7x16-bit	2 x WDG, RTC, 24-bit	16x12-bit		51	3	2	2	3+2		OTG	2		Yes	2 to 3.6	1.9	393	-40 to +105
STM32F107VB	128	64	LQFP100	7x16-bit	downcounter	16x12-bit	2x12-bit	80	3	2	2	3+2		OTG	2		Yes	2 to 3.6	1.9	393	-40 to +105
STM32F105RC	256	64	LQFP64	7x16-bit	downloounter	16x12-bit	2x12-bit	51	3	2	2	3+2		OTG	2			2 to 3.6	1.9	393	-40 to +105
STM32F105VC	256	64	LQFP100	7x16-bit		16x12-bit	2x12-bit	80	3	2	2	3+2		OTG	2			2 to 3.6	1.9	393	-40 to +105
STM32F107RC	256	64	LQFP64	7x16-bit		16x12-bit	2x12-bit	51	3	2	2	3+2		OTG	2		Yes	2 to 3.6	1.9	393	-40 to +105
STM32F107VC	256	64	LFBGA100 LQFP100	7x16-bit		16x12-bit	2x12-bit	80	3	2	2	3+2		OTG	2		Yes	2 to 3.6	1.9	393	-40 to +105

### Note

<sup>1.</sup> Marked in the table (3+2) means 3 USART and 2 UART. All UARTs have LIN master/slave function. All USARTs have IrDA, ISO 7816, modem control and LIN master/slave functions.

				Timer fur	nctions		Analog	J					Ser	ial interfa	ice			Supply cu	rrent (lcc)	
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Package	16-/32-bit timers	Others	ADC 16-bit / 12-bit	DAC	Comp.	Op- amp	I/Os	SPI	I <sup>2</sup> S	I <sup>2</sup> C	USART / UART		CAN 2.0B	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Operating temperature range (°C)
					STM32F	301 line –	72 MHz wi	th advanc	ced time	r and 1	2-bit	ADC (5	MSP	S)						
STM32F301C6	32	16	LQFP48 WLCSP49	5x16-bit / 1x32-bit		1x12-bit (11 Ch.)	1x12-bit	3	1	37	2		3	3			2.0 to 3.6	1.4	353	-40 to +105
STM32F301K6	32	16	UFQFPN32	5x16-bit / 1x32-bit		1x12-bit (8 Ch.)	1x12-bit	2	1	24	2		3	2			2.0 to 3.6	1.4	353	-40 to +105
STM32F301R6	32	16	LQFP64	5x16-bit / 1x32-bit	SysTick, 2 x WDG.	1x12-bit (15 Ch.)	1x12-bit	3	1	51	2	up to 2x full	3	3			2.0 to 3.6	1.4	353	-40 to +105
STM32F301C8	64	16	LQFP48 WLCSP49	5x16-bit / 1x32-bit	RTC	1x12-bit (11 Ch.)	1x12-bit	3	1	37	2	duplex	3	3			2.0 to 3.6	1.4	353	-40 to +105
STM32F301K8	64	16	UFQFPN32	5x16-bit / 1x32-bit		1x12-bit (8 Ch.)	1x12-bit	2	1	24	2		3	2			2.0 to 3.6	1.4	353	-40 to +105
STM32F301R8	64	16	LQFP64	5x16-bit / 1x32-bit		1x12-bit (15 Ch.)	1x12-bit	3	1	51	2		3	3			2.0 to 3.6	1.4	353	-40 to +105
					STM32F	302 line –	72 MHz wit	th advanc	ced time	r and 1	2-bit	ADC (5	MSP	S)						
STM32F302C6	32	16	LQFP48 WLCSP49	5x16-bit / 1x32-bit		1x12-bit (11 Ch.)	1x12-bit	3	1	37	2		3	3	1	1	2.0 to 3.6	1.4	353	-40 to +105
STM32F302K6	32	16	UFQFPN32	5x16-bit / 1x32-bit		1x12-bit (8 Ch.)	1x12-bit	2	1	24	2		3	2	1	1	2.0 to 3.6	1.4	353	-40 to +105
STM32F302R6	32	16	LQFP64	5x16-bit / 1x32-bit		1x12-bit (15 Ch.)	1x12-bit	3	1	51	2		3	3	1	1	2.0 to 3.6	1.4	353	-40 to +105
STM32F302C8	32	16	LQFP48 WLCSP49	5x16-bit / 1x32-bit		1x12-bit (11 Ch.)	1x12-bit	3	1	37	2		3	3	1	1	2.0 to 3.6	1.4	353	-40 to +105
STM32F302K8	64	16	UFQFPN32	5x16-bit / 1x32-bit	SysTick, 2 x WDG.	1x12-bit (8 Ch.)	1x12-bit	2	1	24	2	up to 2x full	3	2	1	1	2.0 to 3.6	1.4	353	-40 to +105
STM32F302R8	64	16	LQFP64	5x16-bit / 1x32-bit	RTC	1x12-bit (15 Ch.)	1x12-bit	3	1	51	2	duplex	3	3	1	1	2.0 to 3.6	1.4	353	-40 to +105
STM32F302CB	128	32	LQFP48	7x16-bit / 1x32-bit		2x12-bit (9 Ch.)	1x12-bit	4	2	37	3		2	3	1	1	2.0 to 3.6	1.5	392	-40 to +105
STM32F302RB	128	32	LQFP64	7x16-bit / 1x32-bit		2x12-bit (16 Ch.)	1x12-bit	4	2	52	3		2	5	1	1	2.0 to 3.6	1.5	392	-40 to +105
STM32F302VB	128	32	LQFP100	7x16-bit / 1x32-bit		2x12-bit (17 Ch.)	1x12-bit	4	2	87	3		2	5	1	1	2.0 to 3.6	1.5	392	-40 to +105
STM32F302CC	256	40	LQFP48	7x16-bit / 1x32-bit		2x12-bit (9 Ch.)	1x12-bit	4	2	37	3		2	3	1	1	2.0 to 3.6	1.5	392	-40 to +105

				Timer fu	nctions		Analog						Ser	ial interfa	ice			Supply cu	rrent (lcc)	
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Package	16-/32-bit timers	Others	ADC 16-bit / 12-bit	DAC	Comp.	Op- amp	I/Os	SPI	I <sup>2</sup> S	I <sup>2</sup> C	USART / UART	USB FS	CAN 2.0B	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Operating temperature range (°C)
STM32F302RC	256	40	LQFP64	7x16-bit / 1x32-bit		2x12-bit (16 Ch.)	1x12-bit	4	2	52	3		2	5	1	1	2.0 to 3.6	1.5	392	-40 to +105
STM32F302VC	256	40	LQFP100	7x16-bit / 1x32-bit		2x12-bit (17 Ch.)	1x12-bit	4	2	87	3		2	5	1	1	2.0 to 3.6	1.5	392	-40 to +105
STM32F302RD	384	64	LQFP64	7x16-bit / 1x32-bit		2x12-bit (16 Ch.)	1x12-bit	4	2	51	4		3	5	1	1	2.0 to 3.6	1.5	392	-40 to +105
STM32F302VD	384	64	LQFP100	7x16-bit / 1x32-bit	SysTick, 2 x WDG.	2x12-bit (17 Ch.)	1x12-bit	4	2	86	4	up to 2x full	3	5	1	1	2.0 to 3.6	1.5	392	-40 to +105
STM32F302ZD	384	64	LQFP144	7x16-bit / 1x32-bit	RTC	2x12-bit (18 Ch.)	1x12-bit	4	2	115	4	duplex	3	5	1	1	2.0 to 3.6	1.5	392	-40 to +105
STM32F302RE	512	64	LQFP64	7x16-bit / 1x32-bit		2x12-bit (16 Ch.)	1x12-bit	4	2	51	4		3	5	1	1	2.0 to 3.6	1.5	392	-40 to +105
STM32F302VE	512	64	LQFP100	7x16-bit / 1x32-bit		2x12-bit (17 Ch.)	1x12-bit	4	2	86	4		3	5	1	1	2.0 to 3.6	1.5	392	-40 to +105
STM32F302ZE	512	64	LQFP144	7x16-bit / 1x32-bit		2x12-bit (18 Ch.)	1x12-bit	4	2	115	4		3	5	1	1	2.0 to 3.6	1.5	392	-40 to +105
				STM32F303	line – 72	MHz with u	p to 16-Kb	yte CCM	-SRAM,	advanc	ed tir	ner and	12-b	it ADC (5	MSF	PS)				
STM32F303C6	32	16	LQFP48	7x16-bit / 1x32-bit		2x12-bit (15 Ch.)	3x12-bit	3	1	37	1		1	3		1	2.0 to 3.6	1.4	353	-40 to +105
STM32F303K6	32	16	LQFP32	7x16-bit / 1x32-bit		2x12-bit (9 Ch.)	3x12-bit	2	1	25	1		1	2	1	1	2.0 to 3.6	1.4	353	-40 to +105
STM32F303R6	32	16	LQFP64	7x16-bit / 1x32-bit		2x12-bit (21 Ch.)	3x12-bit	3	1	51	1	No	1	3	1	1	2.0 to 3.6	1.4	353	-40 to +105
STM32F303C8	64	16	LQFP48	7x16-bit / 1x32-bit	0 . T	2x12-bit (15 Ch.)	3x12-bit	3	1	37	1	INO	1	3	1	1	2.0 to 3.6	1.4	353	-40 to +105
STM32F303K8	64	16	LQFP32	7x16-bit / 1x32-bit	SysTick, 2 x WDG, RTC	2x12-bit (9 Ch.)	3x12-bit	2	1	25	1		1	2	1	1	2.0 to 3.6	1.4	353	-40 to +105
STM32F303R8	64	16	LQFP64	7x16-bit / 1x32-bit	RIG	2x12-bit (21 Ch.)	3x12-bit	3	1	51	1		1	3	1	1	2.0 to 3.6	1.4	353	-40 to +105
STM32F303CB	128	40	LQFP48	9x16-bit / 1x32-bit		4x12-bit (15 Ch.)	2x12-bit	7	4	37	3	up to	2	3	1	1	2.0 to 3.6	1.5	392	-40 to +105
STM32F303RB	128	40	LQFP64	9x16-bit / 1x32-bit		4x12-bit (22 Ch.)	2x12-bit	7	4	52	3	2x full duplex	2	5	1	1	2.0 to 3.6	1.5	392	-40 to +105
STM32F303VB	128	40	LQFP100	9x16-bit / 1x32-bit		4x12-bit (39 Ch.)	2x12-bit	7	4	87	3	aupiox	2	5	1	1	2.0 to 3.6	1.5	392	-40 to +105

				Timer fu	nctions		Analog	J					Ser	ial interfa	ace			Supply cu	rrent (Icc)	
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Package	16-/32-bit timers	Others	ADC 16-bit / 12-bit	DAC	Comp.	Op- amp	I/Os	SPI	I <sup>2</sup> S	I <sup>2</sup> C	USART / UART	USB FS	CAN 2.0B	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Operating temperature range (°C)
STM32F303CC	256	48	LQFP48	9x16-bit / 1x32-bit		4x12-bit (15 Ch.)	2x12-bit	7	4	37	3		2	3	1	1	2.0 to 3.6	1.5	392	-40 to +105
STM32F303RC	256	48	LQFP64	9x16-bit / 1x32-bit		4x12-bit (22 Ch.)	2x12-bit	7	4	52	3		2	5	1	1	2.0 to 3.6	1.5	392	-40 to +105
STM32F303VC	256	48	LQFP100	9x16-bit / 1x32-bit		4x12-bit (39 Ch.)	2x12-bit	7	4	87	3		2	5	1	1	2.0 to 3.6	1.5	392	-40 to +105
STM32F303RD	384	80	LQFP64	10x16-bit / 1x32-bit	0	4x12-bit (22 Ch.)	2x12-bit	7	4	51	4		3	5	1	1	2.0 to 3.6	1.5	392	-40 to +105
STM32F303ZD	384	80	LQFP144	10x16-bit / 1x32-bit	SysTick, 2 x WDG, RTC	4x12-bit (40 Ch.)	2x12-bit	7	4	115	4	up to 2x full	3	5	1	1	2.0 to 3.6	1.5	392	-40 to +105
STM32F303VD	384	80	LQFP100	10x16-bit / 1x32-bit	RIC	4x12-bit (39 Ch.)	2x12-bit	7	4	86	4	duplex	3	5	1	1	2.0 to 3.6	1.5	392	-40 to +105
STM32F303RE	512	80	LQFP64	10x16-bit / 1x32-bit		4x12-bit (22 Ch.)	2x12-bit	7	4	51	4		3	5	1	1	2.0 to 3.6	1.5	392	-40 to +105
STM32F303VE	512	80	LQFP100	10x16-bit / 1x32-bit		4x12-bit (39 Ch.)	2x12-bit	7	4	86	4		3	5	1	1	2.0 to 3.6	1.5	392	-40 to +105
STM32F303ZE	512	80	LQFP144	10x16-bit / 1x32-bit		4x12-bit (40 Ch.)	2x12-bit	7	4	115	4		3	5	1	1	2.0 to 3.6	1.5	392	-40 to +105
							2F373 line	– 72 MH	z with 10	6-bit Σ	Δ ADC	;								
STM32F373C8	64	16	LQFP48	9x16-bit / 2x32-bit		3x16-bit (8ch.) / 1x12-bit (9ch.)	3x12-bit	2		36	3		2	3	1	1	2.0 to 3.6	1.6	417	-40 to +105
STM32F373R8	64	16	LQFP64	9x16-bit / 2x32-bit	SysTick, 2 x WDG,	3x16-bit (8ch.) / 1x12-bit (16ch.)	3x12-bit	2		52	3	up to 3x half	2	3	1	1	2.0 to 3.6	1.6	417	-40 to +105
STM32F373V8	64	16	LQFP100 UFBGA100	9x16-bit / 2x32-bit	RTC	3x16-bit (21ch.) / 1x12-bit (16ch.)	3x12-bit	2		84	3	duplex	2	3	1	1	2.0 to 3.6	1.6	417	-40 to +105
STM32F373CB	128	24	LQFP48	9x16-bit / 2x32-bit		3x16-bit (8ch.) / 1x12-bit (9ch.)	3x12-bit	2		36	3		2	3	1	1	2.0 to 3.6	1.6	417	-40 to +105

				Timer fu	nctions		Analog						Ser	ial interf	ice			Supply cu	rrent (lcc)	
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Package	16-/32-bit timers	Others	ADC 16-bit / 12-bit	DAC	Comp.	Op- amp	I/Os	SPI	I <sup>2</sup> S	I <sup>2</sup> C	USART / UART	USB FS	CAN 2.0B	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Operating temperature range (°C)
STM32F373RB	128	24	LQFP64	9x16-bit / 2x32-bit		3x16-bit (8ch.) / 1x12-bit (16ch.)	3x12-bit	2		52	3		2	3	1	1	2.0 to 3.6	1.6	417	-40 to +105
STM32F373VB	128	24	LQFP100 UFBGA100	9x16-bit / 2x32-bit		3x16-bit (21ch.) / 1x12-bit (16ch.)	3x12-bit	2		84	3		2	3	1	1	2.0 to 3.6	1.6	417	-40 to +105
STM32F373CC	256	32	LQFP48	9x16-bit / 2x32-bit	SysTick, 2 x WDG, RTC	3x16-bit (8ch.) / 1x12-bit (9ch.)	3x12-bit	2		36	3	up to 3x half duplex	2	3	1	1	2.0 to 3.6	1.6	417	-40 to +105
STM32F373RC	256	32	LQFP64	9x16-bit / 2x32-bit		3x16-bit (8ch.) / 1x12-bit (16ch.)	3x12-bit	2		52	3		2	3	1	1	2.0 to 3.6	1.6	417	-40 to +105
STM32F373VC	256	32	LQFP100 UFBGA100	9x16-bit / 2x32-bit		3x16-bit (21ch.) / 1x12-bit (16ch.)	3x12-bit	2		84	3		2	3	1	1	2.0 to 3.6	1.6	417	-40 to +105
							STM32F3x	4 line – 7	2 MHz v	vith HR	-time	r								
STM32F334C4	16	16	LQFP48	7x16-bit / 1x32-bit		2x12-bit (15 Ch.)	3x12-bit	3	1	37	1		1	3		1	2.0 to 3.6	1	420	-40 to +105
STM32F334K4	16	16	LQFP32	7x16-bit / 1x32-bit	SysTick, 2 x WDG,	2x12-bit (9 Ch.)	3x12-bit	2	1	25	1		1	2		1	2.0 to 3.6	1	420	-40 to +105
STM32F334C6	32	16	LQFP48	7x16-bit / 1x32-bit	RTC, HR-timer	2x12-bit (15 Ch.)	3x12-bit	3	1	37	1	No	1	3		1	2.0 to 3.6	1	420	-40 to +105
STM32F334K6	32	16	LQFP32	7x16-bit / 1x32-bit	(1x217ps)	2x12-bit (9 Ch.)	3x12-bit	2	1	25	1		1	2		1	2.0 to 3.6	1	420	-40 to +105
STM32F334R6	32	16	LQFP64	7x16-bit / 1x32-bit		4x12-bit (21 Ch.)	3x12-bit	3	1	51	1		1	3		1	2.0 to 3.6	1	420	-40 to +105

				Timer fu	nctions		Analog	j					Ser	ial interfa	ice			Supply cu	rrent (lcc)	
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Package	16-/32-bit timers	Others	ADC 16-bit / 12-bit	DAC	Comp.	Op- amp	I/Os	SPI	I <sup>2</sup> S	I <sup>2</sup> C	USART / UART	USB FS	CAN 2.0B	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Operating temperature range (°C)
STM32F334C8	64	16	LQFP48	7x16-bit / 1x32-bit	SysTick,	2x12-bit (15 Ch.)	3x12-bit	3	1	37	1		1	3		1	2.0 to 3.6	1	420	-40 to +105
STM32F334K8	64	16	LQFP32	7x16-bit / 1x32-bit	2 x WDG, RTC, HR-timer	2x12-bit (9 Ch.)	3x12-bit	2	1	25	1	No	1	2		1	2.0 to 3.6	1	420	-40 to +105
STM32F334R8	64	16	LQFP64	7x16-bit / 1x32-bit	(1x217ps)	4x12-bit (21 Ch.)	3x12-bit	3	1	51	1		1	3		1	2.0 to 3.6	1	420	-40 to +105
						ST	M32F3x8 I	ine - 72 l	/IHz reg	off 1.8	٧									
STM32F318C8	64	16	LQFP48 WLCSP49	5x16-bit / 1x32-bit		1x12-bit (11 Ch.)	1x12-bit	3	1	36	2	up to 2x full	3	3		1	1.65 to 1.95	4.2	350	-40 to +105
STM32F318K8	64	16	UFQFPN32	5x16-bit / 1x32-bit		1x12-bit (8 Ch.)	1x12-bit	2	1	23	2	duplex	3	2		1	1.65 to 1.95	4.2	350	-40 to +105
STM32F328C8	64	16	LQFP48	7x16-bit / 1x32-bit		2x12-bit (14 Ch.)	3x12-bit	3	1	36	1		1	3		1	1.65 to 1.95	6.8	420	-40 to +105
STM32F358CC	256	48	LQFP48	9x16-bit / 1x32-bit		4x12-bit (14 Ch.)	2x12-bit	7	4	36	3	+0	2	5		1	1.65 to 1.95	7.4	368	-40 to +105
STM32F358RC	256	48	LQFP64	9x16-bit / 1x32-bit		4x12-bit (21 Ch.)	2x12-bit	7	4	51	3	up to 2x full duplex	2	5		1	1.65 to 1.95	7.4	368	-40 to +105
STM32F358VC	256	48	LQFP100	9x16-bit / 1x32-bit	SysTick, 2 x WDG,	4x12-bit (38 Ch.)	2x12-bit	7	4	86	3	uupiex	2	5		1	1.65 to 1.95	7.4	368	-40 to +105
STM32F378CC	256	32	LQFP48	9x16-bit / 2x32-bit	RTC	3x16-bit (8ch.) / 1x12-bit (9ch.)	3x12-bit	2		36	3		2	3		1	1.65 to 1.95	5.9	430	-40 to +105
STM32F378RC	256	32	LQFP64	9x16-bit / 2x32-bit		3x16-bit (8ch.) / 1x12-bit (16ch.)	3x12-bit	2		52	3	up to 3x half duplex	2	3		1	1.65 to 1.95	5.9	430	-40 to +105
STM32F378VC	256	32	LQFP100	9x16-bit / 2x32-bit		3x16-bit (21ch.) / 1x12-bit (16ch.)	3x12-bit	2		84	3		2	3		1	1.65 to 1.95	5.9	430	-40 to +105
STM32F398VE	512	80	LQFP100	10x16-bit / 1x32-bit		4x12-bit (39 Ch.)	2x12-bit	7	4	86	4	up to 2x full duplex	3	5		1	1.65 to 1.95	9.8	392	-40 to +105

Notes: Supply voltage 2.0 to 3.6V for all devices or 1.8V +/-8% dedicated sales type WLCSP66 package available in 1.8V +/-8% dedicated sales type only

# STM32 F2 SERIES - ARM® CORTEX®-M3 HIGH-PERFORMANCE MCUs

					Timer f	unctions							Seria	l interfa	ice				Supply (Ic		
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	AES	Package	16-/32-bit timers	Others	ADC	DAC	I/Os	SPI	I <sup>2</sup> S	I <sup>2</sup> C	USART + UART3	USB OTG FS +FS/ HS	CAN 2.0B	SDIO	Ethernet MAC10 /100	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Maximum operating temperature range (°C)
					STIV	132F2x5 line:	USB OTG (I	S/HS¹), c	rypto	/hash	proc	essor	<sup>2</sup> - 120 M	IHz CPU							
STM32F205RB	128	64		LQFP64	12x16-bit / 2x32-bit		16x12-bit	2x12-bit	51	3	2	3	4+2	2	2	1		1.8 to 3.6	2.5	188	-40 to +105
STM32F205VB	128	64		LQFP100	12x16-bit / 2x32-bit		16x12-bit	2x12-bit	82	3	2	3	4+2	2	2	1		1.8 to 3.6	2.5	188	-40 to +105
STM32F205RC	256	96		LQFP64	12x16-bit / 2x32-bit		16x12-bit	2x12-bit	51	3	2	3	4+2	2	2	1		1.8 to 3.6	2.5	188	-40 to +105
STM32F205VC	256	96		LQFP100	12x16-bit / 2x32-bit		16x12-bit	2x12-bit	82	3	2	3	4+2	2	2	1		1.8 to 3.6	2.5	188	-40 to +105
STM32F205ZC	256	96		LQFP144	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	114	3	2	3	4+2	2	2	1		1.8 to 3.6	2.5	188	-40 to +105
STM32F205RE	512	128		LQFP64 WLCSP64	12x16-bit / 2x32-bit		16x12-bit	2x12-bit	51	3	2	3	4+2	2	2	1		1.7/1.8 to 3.6	2.5	188	-40 to +105
STM32F215RE <sup>2</sup>	512	128	Yes	LQFP64	12x16-bit / 2x32-bit	2 x WDG, RTC. 24-bit	16x12-bit	2x12-bit	51	3	2	3	4+2	2	2	1		1.8 to 3.6	2.5	188	-40 to +105
STM32F215VE <sup>2</sup>	512	128	Yes	LQFP100	12x16-bit / 2x32-bit		16x12-bit	2x12-bit	82	3	2	3	4+2	2	2	1		1.8 to 3.6	2.5	188	-40 to +105
STM32F205VE	512	128		LQFP100	12x16-bit / 2x32-bit		16x12-bit	2x12-bit	82	3	2	3	4+2	2	2	1		1.8 to 3.6	2.5	188	-40 to +105
STM32F205ZE	512	128		LQFP144	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	114	3	2	3	4+2	2	2	1		1.8 to 3.6	2.5	188	-40 to +105
STM32F215ZE <sup>2</sup>	512	128	Yes	LQFP144	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	114	3	2	3	4+2	2	2	1		1.8 to 3.6	2.5	188	-40 to +105
STM32F205RF	768	128		LQFP64	12x16-bit / 2x32-bit		16x12-bit	2x12-bit	51	3	2	3	4+2	2	2	1		1.8 to 3.6	2.5	188	-40 to +105
STM32F205VF	768	128		LQFP100	12x16-bit / 2x32-bit		16x12-bit	2x12-bit	82	3	2	3	4+2	2	2	1		1.8 to 3.6	2.5	188	-40 to +105
STM32F205ZF	768	128		LQFP144	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	114	3	2	3	4+2	2	2	1		1.8 to 3.6	2.5	188	-40 to +105

# STM32 F2 SERIES - ARM® CORTEX®-M3 HIGH-PERFORMANCE MCUs

					Timer fo	unctions							Seria	l interfa	ice				(lc	c)	
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	AES	Package	16-/32-bit timers	Others	ADC	DAC	I/Os	SPI	l <sup>2</sup> S	I <sup>2</sup> C	USART + UART <sup>3</sup>	USB OTG FS +FS/ HS	CAN 2.0B	SDI0	Ethernet MAC10 /100	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Maximum operating temperature range (°C)
STM32F205RG	1024	128		LQFP64 WLCSP64	12x16-bit / 2x32-bit		16x12-bit	2x12-bit	51	3	2	3	4+2	2	2	1		1.7/1.8 to 3.6	2.5	188	-40 to +105
STM32F215RG <sup>2</sup>	1024	128	Yes	LQFP64	12x16-bit / 2x32-bit		16x12-bit	2x12-bit	51	3	2	3	4+2	2	2	1		1.8 to 3.6	2.5	188	-40 to +105
STM32F205VG	1024	128		LQFP100	12x16-bit / 2x32-bit	2 x WDG,	16x12-bit	2x12-bit	82	3	2	3	4+2	2	2	1		1.8 to 3.6	2.5	188	-40 to +105
STM32F215VG <sup>2</sup>	1024	128	Yes	LQFP100	12x16-bit / 2x32-bit	RTC, 24-bit downcounter	16x12-bit	2x12-bit	82	3	2	3	4+2	2	2	1		1.8 to 3.6	2.5	188	-40 to +105
STM32F205ZG	1024	128		LQFP144	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	114	3	2	3	4+2	2	2	1		1.8 to 3.6	2.5	188	-40 to +105
STM32F215ZG <sup>2</sup>	1024	128	Yes	LQFP144	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	114	3	2	3	4+2	2	2	1		1.8 to 3.6	2.5	188	-40 to +105
						line: 2x USB	OTG (FS/H	S¹), came	ra IF,	crypt	o/has	sh pro	ocessor <sup>2</sup>	- 120 M	Hz CP	U					
STM32F207VC	256	128		LQFP100	12x16-bit / 2x32-bit		16x12-bit	2x12-bit	82	3	2	2	4+2	2	2	1	Yes	1.8 to 3.6	2.5	188	-40 to +105
STM32F207ZC	256	128		LQFP144	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	114	3	2	2	4+2	2	2	1	Yes	1.8 to 3.6	2.5	188	-40 to +105
STM32F207IC	256	128		UFBGA176	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	140	3	2	2	4+2	2	2	1	Yes	1.8 to 3.6	2.5	188	-40 to +105
STM32F207VE	512	128		LQFP100	12x16-bit / 2x32-bit		16x12-bit	2x12-bit	82	3	2	2	4+2	2	2	1	Yes	1.8 to 3.6	2.5	188	-40 to +105
STM32F217VE <sup>2</sup>	512	128	Yes	LQFP100	12x16-bit / 2x32-bit	2 x WDG, RTC, 24-bit	16x12-bit	2x12-bit	82	3	2	2	4+2	2	2	1	Yes	1.8 to 3.6	2.5	188	-40 to +105
STM32F207ZE	512	128		LQFP144	12x16-bit / 2x32-bit	downcounter	24x12-bit	2x12-bit	114	3	2	2	4+2	2	2	1	Yes	1.8 to 3.6	2.5	188	-40 to +105
STM32F217ZE <sup>2</sup>	512	128	Yes	LQFP144	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	114	3	2	2	4+2	2	2	1	Yes	1.8 to 3.6	2.5	188	-40 to +105
STM32F207IE	512	128		UFBGA176	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	140	3	2	2	4+2	2	2	1	Yes	1.8 to 3.6	2.5	188	-40 to +105
STM32F217IE <sup>2</sup>	512	128	Yes	UFBGA176	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	140	3	2	2	4+2	2	2	1	Yes	1.8 to 3.6	2.5	188	-40 to +105
STM32F207VF	768	128		LQFP100	12x16-bit / 2x32-bit		16x12-bit	2x12-bit	82	3	2	2	4+2	2	2	1	Yes	1.8 to 3.6	2.5	188	-40 to +105

# STM32 F2 SERIES - ARM® CORTEX®-M3 HIGH-PERFORMANCE MCUs

		l-11			Timer f	unctions							Seria	l interfa	ace				(lc	<b>c</b> )	
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	AES	Package	16-/32-bit timers	Others	ADC	DAC	I/Os	SPI	I <sup>2</sup> S	I <sup>2</sup> C	USART + UART3	USB OTG FS +FS/ HS	CAN 2.0B	SDIO	Ethernet MAC10 /100	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Maximum operating temperature range (°C)
STM32F207ZF	768	128		LQFP144	12x16-bit / 2x32-bit	ĺ	24x12-bit	2x12-bit	114	3	2	2	4+2	2	2	1	Yes	1.8 to 3.6	2.5	188	-40 to +105
STM32F207IF	768	128		UFBGA176	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	140	3	2	2	4+2	2	2	1	Yes	1.8 to 3.6	2.5	188	-40 to +105
STM32F207VG	1024	128		LQFP100	12x16-bit / 2x32-bit		16x12-bit	2x12-bit	82	3	2	2	4+2	2	2	1	Yes	1.8 to 3.6	2.5	188	-40 to +105
STM32F217VG <sup>2</sup>	1024	128	Yes	LQFP100	12x16-bit / 2x32-bit	2 x WDG, RTC. 24-bit	16x12-bit	2x12-bit	82	3	2	2	4+2	2	2	1	Yes	1.8 to 3.6	2.5	188	-40 to +105
STM32F207ZG	1024	128		LQFP144	12x16-bit / 2x32-bit	downoountor	24x12-bit	2x12-bit	114	3	2	2	4+2	2	2	1	Yes	1.8 to 3.6	2.5	188	-40 to +105
STM32F217ZG <sup>2</sup>	1024	128	Yes	LQFP144	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	114	3	2	2	4+2	2	2	1	Yes	1.8 to 3.6	2.5	188	-40 to +105
STM32F207IG	1024	128		LQFP176 UFBGA176	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	140	3	2	2	4+2	2	2	1	Yes	1.8 to 3.6	2.5	188	-40 to +105
STM32F217IG <sup>2</sup>	1024	128	Yes	LQFP176 UFBGA176	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	140	3	2	2	4+2	2	2	1	Yes	1.8 to 3.6	2.5	188	-40 to +105

### Notes:

- 1. HS requires an external PHY connected to ULPI interface
- 2. Crypto/hash processor on STM32F215 and STM32F217
  3. Marked in the table (3+2) means 3 USART and 2 UART. All UARTs have LIN master/slave function. All USARTs have IrDA, ISO 7816, modem control and LIN master/slave functions.

				Timer f	unctions								Serial in	iterfac	е				Supply (Ic		Maximum
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Package	16-/32-bit timers	Others	ADC	DAC	I/Os	SPI	SAI	I <sup>2</sup> S	I <sup>2</sup> C	USART + UART <sup>3</sup>	USB OTG	CAN 2.0B	SDI0	Ethernet MAC10 /100	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Maximum operating temperature range (°C)
					132F401 line:	USB OTG (I	S), low p	ower (1	10 μΑ	typ. i	in Sto	op m	ode) - 84	4 MHz	CPU						
STM32F401CB	128	64	WLCSP49 UFQFPN48	6x16-bit / 2x32-bit		10x12-bit		36	3		2	3	3	1				1.7 <sup>3</sup> to 3.6	1.8	128	-40 to +105
STM32F401RB	128	64	LQFP64	6x16-bit / 2x32-bit		16x12-bit		50	3		2	3	3	1		1		1.7 <sup>3</sup> to 3.6	1.8	128	-40 to +105
STM32F401VB	128	64	LQFP100 UFBGA100	6x16-bit / 2x32-bit		16x12-bit		81	4		2	3	3	1		1		1.7 <sup>3</sup> to 3.6	1.8	128	-40 to +105
STM32F401CC	256	64	WLCSP49 UFQFPN48	6x16-bit / 2x32-bit		10x12-bit		36	3		2	3	3	1				1.7 <sup>3</sup> to 3.6	1.8	128	-40 to +105
STM32F401RC	256	64	LQFP64	6x16-bit / 2x32-bit		16x12-bit		50	3		2	3	3	1		1		1.7 <sup>3</sup> to 3.6	1.8	128	-40 to +105
STM32F401VC	256	64	LQFP100 UFBGA100	6x16-bit / 2x32-bit	2x WDG, RTC, 24-bit	16x12-bit		81	4		2	3	3	1		1		1.7 <sup>3</sup> to 3.6	1.8	128	-40 to +105
STM32F401CD	384	96	WLCSP49 UFQFPN48	6x16-bit / 2x32-bit	downcounter	10x12-bit		36	3		2	3	3	1				1.7 <sup>3</sup> to 3.6	1.8	137	-40 to +105
STM32F401RD	384	96	LQFP64	6x16-bit / 2x32-bit		16x12-bit		50	3		2	3	3	1		1		1.7³ to 3.6	1.8	137	-40 to +105
STM32F401VD	384	96	LQFP100 UFBGA100	6x16-bit / 2x32-bit		16x12-bit		81	4		2	3	3	1		1		1.7 <sup>3</sup> to 3.6	1.8	137	-40 to +105
STM32F401CE	512	96	WLCSP49 UFQFPN48	6x16-bit / 2x32-bit		10x12-bit		36	3		2	3	3	1				1.7 <sup>3</sup> to 3.6	1.8	137	-40 to +105
STM32F401RE	512	96	LQFP64	6x16-bit / 2x32-bit		16x12-bit		50	3		2	3	3	1		1		1.7 <sup>3</sup> to 3.6	1.8	137	-40 to +105
STM32F401VE	512	96	LQFP100 UFBGA100	6x16-bit / 2x32-bit		16x12-bit		81	4		2	3	3	1		1		1.7 <sup>3</sup> to 3.6	1.8	137	-40 to +105
			STIV	132F411 line	: Batch Acqui	sition Mod	e (BAM), l	JSB OT	G (FS	), low	pow	er (9	μA typ.	in Stop	mod	e) - 10	OO MHz CF	PU			
STM32F411CC	256	128	WLCSP49 UFQFPN48	6x16-bit / 2x32-bit	2x WDG.	12x12-bit		36	5		5	3	3	1		1		1.73 to 3.6	1.8	100	-40 to +105
STM32F411RC	256	128	LQFP64	6x16-bit / 2x32-bit	RTC, 24-bit downcounter	12x12-bit		50	5		5	3	3	1		1		1.73 to 3.6	1.8	100	-40 to +105
STM32F411VC	256	128	LQFP100 UFBGA100	6x16-bit / 2x32-bit	downloanter	12x12-bit		81	5		5	3	3	1		1		1.7 <sup>3</sup> to 3.6	1.8	100	-40 to +105

				Timer f	unctions								Serial in	iterfac	е				Supply (Ic		
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Package	16-/32-bit timers	Others	ADC	DAC	I/Os	SPI	SAI	I <sup>2</sup> S	I <sup>2</sup> C	USART + UART <sup>3</sup>	USB OTG	CAN 2.0B	SDI0	Ethernet MAC10 /100	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Maximum operating temperature range (°C)
STM32F411CE	512	128	WLCSP49 UFQFPN48	6x16-bit / 2x32-bit	0. WD0	12x12-bit		36	5		5	3	3	1		1		1.73 to 3.6	1.8	100	-40 to +105
STM32F411RE	512	128	LQFP64	6x16-bit / 2x32-bit	2x WDG, RTC, 24-bit	12x12-bit		50	5		5	3	3	1		1		1.73 to 3.6	1.8	100	-40 to +105
STM32F411VE	512	128	LQFP100 UFBGA100	6x16-bit / 2x32-bit	downcounter	12x12-bit		81	5		5	3	3	1		1		1.73 to 3.6	1.8	100	-40 to +105
					TM32F405/41	5 line: USB	OTG (FS/	HS¹), cr	ypto	/hash	proc	esso	r² - 168	MHz C	PU						
STM32F4050E	512	192	WLCSP90	12x16-bit / 2x32-bit		13x12-bit	2x12-bit	72	3		2	3	4+2	2	2	1		1.73 to 3.6	1.7	215	-40 to +105
STM32F4050G	1024	192	WLCSP90	12x16-bit / 2x32-bit		13x12-bit	2x12-bit	72	3		2	3	4+2	2	2	1		1.73 to 3.6	1.7	215	-40 to +105
STM32F4150G <sup>2</sup>	1024	192	WLCSP90	12x16-bit / 2x32-bit		13x12-bit	2x12-bit	72	3		2	3	4+2	2	2	1		1.7 <sup>3</sup> to 3.6	1.7	215	-40 to +105
STM32F405RG	1024	192	LQFP64	12x16-bit / 2x32-bit	0.14/00	16x12-bit	2x12-bit	51	3		2	3	4+2	2	2	1		1.8 to 3.6	1.7	215	-40 to +105
STM32F415RG <sup>2</sup>	1024	192	LQFP64	12x16-bit / 2x32-bit	2x WDG, RTC, 24-bit	16x12-bit	2x12-bit	51	3		2	3	4+2	2	2	1		1.8 to 3.6	1.7	215	-40 to +105
STM32F405VG	1024	192	LQFP100	12x16-bit / 2x32-bit	downcounter	16x12-bit	2x12-bit	82	3		2	3	4+2	2	2	1		1.8 to 3.6	1.7	215	-40 to +105
STM32F415VG <sup>2</sup>	1024	192	LQFP100	12x16-bit / 2x32-bit		16x12-bit	2x12-bit	82	3		2	3	4+2	2	2	1		1.8 to 3.6	1.7	215	-40 to +105
STM32F405ZG	1024	192	LQFP144	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	114	3		2	3	4+2	2	2	1		1.73 to 3.6	1.7	215	-40 to +105
STM32F415ZG <sup>2</sup>	1024	192	LQFP144	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	114	3		2	3	4+2	2	2	1		1.73 to 3.6	1.7	215	-40 to +105
				STM32F4	07/417 line: 2	x USB OTG	(FS/HS <sup>1</sup> ),	camer	a IF,	crypt	o/has	sh pr	ocessor2	- 168	MHz	CPU					
STM32F407IE	512	192	UFBGA176 LQFP176	12x16-bit / 2x32-bit	Ov WDC	24x12-bit	2x12-bit	140	3		2	3	4+2	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	1.7	215	-40 to +105
STM32F417IE <sup>2</sup>	512	192	UFBGA176 LQFP176	12x16-bit / 2x32-bit	2x WDG, RTC, 24-bit downcounter	24x12-bit	2x12-bit	140	3		2	3	4+2	2	2	1	Yes	1.73 to 3.6	1.7	215	-40 to +105
STM32F407VE	512	192	LQFP100	12x16-bit / 2x32-bit	uowiicouiilei	16x12-bit	2x12-bit	82	3		2	3	4+2	2	2	1	Yes	1.8 to 3.6	1.7	215	-40 to +105

				Timer f	unctions								Serial in	iterfac	е				Supply (Ic		
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Package	16-/32-bit timers	Others	ADC	DAC	I/Os	SPI	SAI	I <sup>2</sup> S	I <sup>2</sup> C	USART + UART3	USB OTG	CAN 2.0B	SDIO	Ethernet MAC10 /100	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Maximum operating temperature range (°C)
STM32F417VE <sup>2</sup>	512	192	LQFP100	12x16-bit / 2x32-bit		16x12-bit	2x12-bit	82	3		2	3	4+2	2	2	1	Yes	1.8 to 3.6	1.7	215	-40 to +105
STM32F407ZE	512	192	LQFP144	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	114	3		2	3	4+2	2	2	1	Yes	1.73 to 3.6	1.7	215	-40 to +105
STM32F417ZE <sup>2</sup>	512	192	LQFP144	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	114	3		2	3	4+2	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	1.7	215	-40 to +105
STM32F407IG	1024	192	UFBGA176 LQFP176	12x16-bit / 2x32-bit	0.14/00	24x12-bit	2x12-bit	140	3		2	3	4+2	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	1.7	215	-40 to +105
STM32F417IG <sup>2</sup>	1024	192	UFBGA176 LQFP176	12x16-bit / 2x32-bit	2x WDG, RTC, 24-bit downcounter	24x12-bit	2x12-bit	140	3		2	3	4+2	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	1.7	215	-40 to +105
STM32F407VG	1024	192	LQFP100	12x16-bit / 2x32-bit	downcounter	16x12-bit	2x12-bit	82	3		2	3	4+2	2	2	1	Yes	1.8 to 3.6	1.7	215	-40 to +105
STM32F417VG <sup>2</sup>	1024	192	LQFP100	12x16-bit / 2x32-bit		16x12-bit	2x12-bit	82	3		2	3	4+2	2	2	1	Yes	1.8 to 3.6	1.7	215	-40 to +105
STM32F407ZG	1024	192	LQFP144	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	114	3		2	3	4+2	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	1.7	215	-40 to +105
STM32F417ZG <sup>2</sup>	1024	192	LQFP144	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	114	3		2	3	4+2	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	1.7	215	-40 to +105
		ST	M32F427/43	7 line: 2x US	B OTG (FS/HS	<sup>1</sup> ), camera	IF, crypto	/hash	proc	essor <sup>2</sup>	², SD	RAM	interfac	e, dual	-bank	Flash	- 180 MH	z CPU			
STM32F427AG	1024	256	UFBGA169	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	130	6	1	2	3	4+4	2	2	1	Yes	1.73 to 3.6	1.7	208	-40 to +105
STM32F427IG	1024	256	UFBGA176 LQFP176	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	140	6	1	2	3	4+4	2	2	1	Yes	1.73 to 3.6	1.7	208	-40 to +105
STM32F427VG	1024	256	LQFP100	12x16-bit / 2x32-bit		16x12-bit	2x12-bit	82	6	1	2	3	4+4	2	2	1	Yes	1.8 to 3.6	1.7	208	-40 to +105
STM32F427ZG	1024	256	LQFP144	12x16-bit /	2x WDG, RTC, 24-bit	24x12-bit	2x12-bit	114	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	1.7	208	-40 to +105
STM32F437IG <sup>2</sup>	1024	256	UFBGA176 LQFP176	12x16-bit / 2x32-bit	downcounter	24x12-bit	2x12-bit	140	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	1.7	208	-40 to +105
STM32F437VG <sup>2</sup>	1024	256	LQFP100	12x16-bit / 2x32-bit		16x12-bit	2x12-bit	82	6	1	2	3	4+4	2	2	1	Yes	1.8 to 3.6	1.7	208	-40 to +105
STM32F437ZG <sup>2</sup>	1024	256	LQFP144	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	114	6	1	2	3	4+4	2	2	1	Yes	1.73 to 3.6	1.7	208	-40 to +105

				Timer f	unctions								Serial in	iterfac	е				Supply (Ic		
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Package	16-/32-bit timers	Others	ADC	DAC	I/Os	SPI	SAI	I <sup>2</sup> S	I <sup>2</sup> C	USART + UART3	USB OTG	CAN 2.0B	SDIO	Ethernet MAC10 /100	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Maximum operating temperature range (°C)
STM32F427AI	2048	256	UFBGA169	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	130	6	1	2	3	4+4	2	2	1	Yes	1.73 to 3.6	1.7	208	-40 to +105
STM32F427II	2048	256	UFBGA176 LQFP176	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	140	6	1	2	3	4+4	2	2	1	Yes	1.73 to 3.6	1.7	208	-40 to +105
STM32F427VI	2048	256	LQFP100	12x16-bit / 2x32-bit		16x12-bit	2x12-bit	82	6	1	2	3	4+4	2	2	1	Yes	1.8 to 3.6	1.7	208	-40 to +105
STM32F427ZI	2048	256	LQFP144	12x16-bit / 2x32-bit	2x WDG,	24x12-bit	2x12-bit	114	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	1.7	208	-40 to +105
STM32F437AI <sup>2</sup>	2048	256	UFBGA169	12x16-bit / 2x32-bit	RTC, 24-bit downcounter	24x12-bit	2x12-bit	130	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	1.7	208	-40 to +105
STM32F437II <sup>2</sup>	2048	256	UFBGA176 LQFP176	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	140	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	1.7	208	-40 to +105
STM32F437VI <sup>2</sup>	2048	256	LQFP100	12x16-bit / 2x32-bit		16x12-bit	2x12-bit	82	6	1	2	3	4+4	2	2	1	Yes	1.8 to 3.6	1.7	208	-40 to +105
STM32F437ZI <sup>2</sup>	2048	256	LQFP144	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	114	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	1.7	208	-40 to +105
					2F429/439 line	e: Same as	STM32F4	27/437	line	+ TFI	LCD	con	troller -	180 M	Hz CP	U					
STM32F429BE	512	256	LQFP208	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	168	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	1.7	208	-40 to +105
STM32F429IE	512	256	UFBGA176 LQFP176	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	140	6	1	2	3	4+4	2	2	1	Yes	1.73 to 3.6	1.7	208	-40 to +105
STM32F429NE	512	256	TFBGA216	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	168	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	1.7	208	-40 to +105
STM32F429VE	512	256	LQFP100	12x16-bit / 2x32-bit	2x WDG,	16x12-bit	2x12-bit	82	6	1	2	3	4+4	2	2	1	Yes	1.8 to 3.6	1.7	208	-40 to +105
STM32F429ZE	512	256	LQFP144	12x16-bit / 2x32-bit	RTC, 24-bit downcounter	24x12-bit	2x12-bit	114	6	1	2	3	4+4	2	2	1	Yes	1.7³ to 3.6	1.7	208	-40 to +105
STM32F429BG	1024	256	LQFP208	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	168	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	1.7	208	-40 to +105
STM32F429IG	1024	256	UFBGA176 LQFP176	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	140	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	1.7	208	-40 to +105
STM32F429NG	1024	256	TFBGA216	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	168	6	1	2	3	4+4	2	2	1	Yes	1.73 to 3.6	1.7	208	-40 to +105

				Timer f	unctions								Serial ir	iterfac	е				Supply (Ic		
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Package	16-/32-bit timers	Others	ADC	DAC	I/Os	SPI	SAI	I <sup>2</sup> S	I <sup>2</sup> C	USART + UART <sup>3</sup>	USB OTG	CAN 2.0B	SDI0	Ethernet MAC10 /100	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Maximum operating temperature range (°C)
STM32F429VG	1024	256	LQFP100	12x16-bit / 2x32-bit		16x12-bit	2x12-bit	82	6	1	2	3	4+4	2	2	1	Yes	1.8 to 3.6	1.7	208	-40 to +105
STM32F429ZG	1024	256	LQFP144 WLCSP143	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	114	6	1	2	3	4+4	2	2	1	Yes	1.73 to 3.6	1.7	208	-40 to +105
STM32F439BG <sup>2</sup>	1024	256	LQFP208	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	168	6	1	2	3	4+4	2	2	1	Yes	1.73 to 3.6	1.7	208	-40 to +105
STM32F439IG <sup>2</sup>	1024	256	UFBGA176 LQFP176	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	140	6	1	2	3	4+4	2	2	1	Yes	1.73 to 3.6	1.7	208	-40 to +105
STM32F439NG <sup>2</sup>	1024	256	TFBGA216	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	168	6	1	2	3	4+4	2	2	1	Yes	1.73 to 3.6	1.7	208	-40 to +105
STM32F439VG <sup>2</sup>	1024	256	LQFP100	12x16-bit / 2x32-bit		16x12-bit	2x12-bit	82	6	1	2	3	4+4	2	2	1	Yes	1.8 to 3.6	1.7	208	-40 to +105
STM32F439ZG <sup>2</sup>	1024	256	LQFP144 WLCSP143	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	114	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	1.7	208	-40 to +105
STM32F429AI	2048	256	UFBGA169	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	130	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	1.7	208	-40 to +105
STM32F429BI	2048	256	LQFP208	12x16-bit / 2x32-bit	2x WDG, RTC, 24-bit	24x12-bit	2x12-bit	168	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	1.7	208	-40 to +105
STM32F429II	2048	256	UFBGA176 LQFP176	12x16-bit / 2x32-bit	downcounter	24x12-bit	2x12-bit	140	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	1.7	208	-40 to +105
STM32F429NI	2048	256	TFBGA216	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	168	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	1.7	208	-40 to +105
STM32F429VI	2048	256	LQFP100	12x16-bit / 2x32-bit		16x12-bit	2x12-bit	82	6	1	2	3	4+4	2	2	1	Yes	1.8 to 3.6	1.7	208	-40 to +105
STM32F429ZI	2048	256	LQFP144 WLCSP143	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	114	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	1.7	208	-40 to +105
STM32F439Al <sup>2</sup>	2048	256	UFBGA169	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	130	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	1.7	208	-40 to +105
STM32F439Bl <sup>2</sup>	2048	256	LQFP208	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	168	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	1.7	208	-40 to +105
STM32F439II <sup>2</sup>	2048	256	UFBGA176 LQFP176	1'2x16-bit / 2x32-bit		24x12-bit	2x12-bit	140	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	1.7	208	-40 to +105
STM32F439NI <sup>2</sup>	2048	256	TFBGA216	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	168	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	1.7	208	-40 to +105

				Timer f	unctions								Serial in	ıterfac	е				Supply (Ic		
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Package	16-/32-bit timers	Others	ADC	DAC	I/Os	SPI	SAI	I <sup>2</sup> S	I <sup>2</sup> C	USART + UART3	USB OTG	CAN 2.0B	SDI0	Ethernet MAC10 /100	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Maximum operating temperature range (°C)
STM32F439VI <sup>2</sup>	2048	256	LQFP100	12x16-bit / 2x32-bit	2x WDG, RTC, 24-bit	16x12-bit	2x12-bit	82	6	1	2	3	4+4	2	2	1	Yes	1.8 to 3.6	1.7	208	-40 to +105
STM32F439ZI <sup>2</sup>	2048	256	LQFP144 WLCSP143	12x16-bit / 2x32-bit	downcounter	24x12-bit	2x12-bit	114	6	1	2	3	4+4	2	2	1	Yes	1.73 to 3.6	1.7	208	-40 to +105
							STM32F4	16 line	: 180	MHz											
STM32F446RC	256	128	LQFP64	12x16-bit / 2x32-bit		16x12-bit	2x12-bit	64	4	2	3	4	4+4	2	2	1		1.7 to 3.6	1.12	167	-40 to +105
STM32F446VC	256	128	LQFP100	12x16-bit / 2x32-bit		16x12-bit	2x12-bit	100	4	2	2	4	4+4	2	2	1		1.7 to 3.6	1.12	167	-40 to +105
STM32F446ZC	256	128	LQFP144 UFBGA144	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	144	4	2	3	4	4+4	2	2	1		1.7 to 3.6	1.12	167	-40 to +105
STM32F446MC	256	128	WLCSP81	12x16-bit / 2x32-bit	2x WDG, RTC, 24-bit	16x12-bit	2x12-bit	81	4	2	3	4	4+4	2	2	1		1.7 to 3.6	1.12	167	-40 to +105
STM32F446RE	512	128	LQFP64	12x16-bit / 2x32-bit	downcounter	16x12-bit	2x12-bit	64	4	2	3	4	4+4	2	2	1		1.7 to 3.6	1.12	167	-40 to +105
STM32F446VE	512	128	LQFP100	12x16-bit / 2x32-bit		16x12-bit	2x12-bit	100	4	2	3	4	4+4	2	2	1		1.7 to 3.6	1.12	167	-40 to +105
STM32F446ZE	512	128	LQFP144 UFBGA144	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	144	4	2	3	4	4+4	2	2	1		1.7 to 3.6	1.12	167	-40 to +105
STM32F446ME	512	128	WLCSP81	12x16-bit / 2x32-bit		16x12-bit	2x12-bit	81	4	2	3	4	4+4	2	2	1		1.7 to 3.6	1.12	167	-40 to +105
						STM32	F4x9 line :	180 M	Hz w	ith MI	PI In	terfa	ce								
STM32F469IE	512	384	LQFP176 UFBGA176	12x16-bit / 2x32-bit		3x12-bit	2x12-bit	131	6		2	3	4+4	2	2	1		1.7 to 3.6	2.2	238	-40 to +105
STM32F469BE	512	384	LQFP208	12x16-bit / 2x32-bit	0 14/00	3x12-bit	2x12-bit	161	6		2	3	4+4	2	2	1		1.7 to 3.6	2.2	238	-40 to +105
STM32F469AE	512	384	WLCSP168 BGA169	12x16-bit / 2x32-bit	2x WDG, RTC, 24-bit downcounter	3x12-bit	2x12-bit	114	6		2	3	4+4	2	2	1		1.7 to 3.6	2.2	238	-40 to +105
STM32F469NE	512	384	BGA216	12x16-bit / 2x32-bit	downcounter	3x12-bit	2x12-bit	161	6		2	3	4+4	2	2	1		1.7 to 3.6	2.2	238	-40 to +105
STM32F469IG	1024	384	LQFP176 UFBGA176	12x16-bit / 2x32-bit		3x12-bit	2x12-bit	131	6		2	3	4+4	2	2	1		1.7 to 3.6	2.2	238	-40 to +105

				Timer f	unctions								Serial in	nterfac	е				Supply (Ic		
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Package	16-/32-bit timers	Others	ADC	DAC	I/Os	SPI	SAI	I <sup>2</sup> S	I <sup>2</sup> C	USART + UART <sup>3</sup>	USB OTG	CAN 2.0B	SDIO	Ethernet MAC10 /100	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Maximum operating temperature range (°C)
STM32F469BG	1024	384	LQFP208	12x16-bit / 2x32-bit		3x12-bit	2x12-bit	161	6		2	3	4+4	2	2	1		1.7 to 3.6	2.2	238	-40 to +105
STM32F469AG	1024	384	WLCSP168 BGA169	12x16-bit / 2x32-bit		3x12-bit	2x12-bit	114	6		2	3	4+4	2	2	1		1.7 to 3.6	2.2	238	-40 to +105
STM32F469NG	1024	384	BGA216	12x16-bit / 2x32-bit		3x12-bit	2x12-bit	161	6		2	3	4+4	2	2	1		1.7 to 3.6	2.2	238	-40 to +105
STM32F479IG <sup>2</sup>	1024	384	LQFP176 UFBGA176	12x16-bit / 2x32-bit		3x12-bit	2x12-bit	131	6		2	3	4+4	2	2	1		1.7 to 3.6	2.2	238	-40 to +105
STM32F479BG <sup>2</sup>	1024	384	LQFP208	12x16-bit / 2x32-bit		3x12-bit	2x12-bit	161	6		2	3	4+4	2	2	1		1.7 to 3.6	2.2	238	-40 to +105
STM32F479AG <sup>2</sup>	1024	384	WLCSP168 BGA169	12x16-bit / 2x32-bit		3x12-bit	2x12-bit	114	6		2	3	4+4	2	2	1		1.7 to 3.6	2.2	238	-40 to +105
STM32F479NG <sup>2</sup>	1024	384	BGA216	12x16-bit / 2x32-bit	0 1450	3x12-bit	2x12-bit	161	6		2	3	4+4	2	2	1		1.7 to 3.6	2.2	238	-40 to +105
STM32F469II	2048	384	LQFP176 UFBGA176	12x16-bit / 2x32-bit	2x WDG, RTC, 24-bit downcounter	3x12-bit	2x12-bit	131	6		2	3	4+4	2	2	1		1.7 to 3.6	2.2	238	-40 to +105
STM32F469BI	2048	384	LQFP208	12x16-bit / 2x32-bit	downcounter	3x12-bit	2x12-bit	161	6		2	3	4+4	2	2	1		1.7 to 3.6	2.2	238	-40 to +105
STM32F469AI	2048	384	WLCSP168 BGA169	12x16-bit / 2x32-bit		3x12-bit	2x12-bit	114	6		2	3	4+4	2	2	1		1.7 to 3.6	2.2	238	-40 to +105
STM32F469NI	2048	384	BGA216	12x16-bit / 2x32-bit		3x12-bit	2x12-bit	161	6		2	3	4+4	2	2	1		1.7 to 3.6	2.2	238	-40 to +105
STM32F479II <sup>2</sup>	2048	384	LQFP176 UFBGA176	12x16-bit / 2x32-bit		3x12-bit	2x12-bit	131	6		2	3	4+4	2	2	1		1.7 to 3.6	2.2	238	-40 to +105
STM32F479BI <sup>2</sup>	2048	384	LQFP208	12x16-bit / 2x32-bit		3x12-bit	2x12-bit	161	6		2	3	4+4	2	2	1		1.7 to 3.6	2.2	238	-40 to +105
STM32F479AI <sup>2</sup>	2048	384	WLCSP168 BGA169	12x16-bit / 2x32-bit		3x12-bit	2x12-bit	114	6		2	3	4+4	2	2	1		1.7 to 3.6	2.2	238	-40 to +105
STM32F479NI <sup>2</sup>	2048	384	BGA216	12x16-bit / 2x32-bit		3x12-bit	2x12-bit	161	6		2	3	4+4	2	2	1		1.7 to 3.6	2.2	238	-40 to +105

### Notes:

- 1. HS requires an external PHY connected to ULPI interfacefunctions.
- 2. Crypto/hash processor on STM32F415, STM32F417, STM32F437, STM32F439 and STM32F479.
- 3. Marked in the table (3+2) means 3 USART and 2 UART. All UARTs have LIN master/slave function. All USARTs have IrDA, ISO 7816, modem control and LIN master/slave functions.

# STM32 F7 SERIES - ARM® CORTEX®-M7 HIGH-PERFORMANCE MCUs

				Timer f	unctions								Serial in	iterfac	е				Supply (Ic		Manimum
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Package	16-/32-bit timers	Others	ADC	DAC	I/Os	SPI	SAI	I <sup>2</sup> S	I <sup>2</sup> C	USART + UART <sup>2</sup>	USB OTG	CAN 2.0B	SDI0	Ethernet MAC10 /100	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Maximum operating temperature range (°C)
							STM32F	7x5 lin	e 216	MHz											
STM32F745VE	512	320	LQFP100	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	82	4	2	3	4	4+4	2	2	1	1	1.7 to 3.6	2.5	420	-40 to +105
STM32F745ZE	512	320	LQFP144	12x16-bit / 2x32-bit		16x12-bit	2x12-bit	114	6	2	2	4	4+4	2	2	1	1	1.7 to 3.6	2.5	420	-40 to +105
STM32F745IE	512	320	UFBGA176 LQFP176	12x16-bit / 2x32-bit	2x WDG, RTC, 24-bit	24x12-bit	2x12-bit	140	6	2	3	4	4+4	2	2	1	1	1.7 to 3.6	2.5	420	-40 to +105
STM32F745VG	1024	320	LQFP100	12x16-bit / 2x32-bit	downcounter, LP Timer	24x12-bit	2x12-bit	82	4	2	3	4	4+4	2	2	1	1	1.7 to 3.6	2.5	420	-40 to +105
STM32F745ZG	1024	320	LQFP144	12x16-bit / 2x32-bit		16x12-bit	2x12-bit	114	6	2	2	4	4+4	2	2	1	1	1.7 to 3.6	2.5	420	-40 to +105
STM32F745IG	1024	320	UFBGA176	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	140	6	2	3	4	4+4	2	2	1	1	1.7 to 3.6	2.5	420	-40 to +105
						STM32F	7x6 LCD-1	FT con	trolle	er line	216	MHz									
STM32F746BE	512	320	LQFP208	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	168	6	2	3	4	4+4	2	2	1	1	1.7 to 3.6	2.5	420	-40 to +105
STM32F746IE	512	320	LQFP176	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	140	6	2	3	4	4+4	2	2	1	1	1.7 to 3.6	2.5	420	-40 to +105
STM32F746NE	512	320	UFBGA216 TFBGA216	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	168	6	2	3	4	4+4	2	2	1	1	1.7 to 3.6	2.5	420	-40 to +105
STM32F746VE	512	320	LQFP100	12x16-bit / 2x32-bit	2x WDG,	16x12-bit	2x12-bit	82	4	2	2	4	4+4	2	2	1	1	1.7 to 3.6	2.5	420	-40 to +105
STM32F746ZE	512	320	LQFP144 WLCSP143	12x16-bit / 2x32-bit	RTC, 24-bit downcounter,	24x12-bit	2x12-bit	114	6	2	3	4	4+4	2	2	1	1	1.7 to 3.6	2.5	420	-40 to +105
STM32F746BG	1024	320	LQFP208	12x16-bit / 2x32-bit	LP Timer	24x12-bit	2x12-bit	168	6	2	3	4	4+4	2	2	1	1	1.7 to 3.6	2.5	420	-40 to +105
STM32F746IG	1024	320	LQFP176	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	140	6	2	3	4	4+4	2	2	1	1	1.7 to 3.6	2.5	420	-40 to +105
STM32F746NG	1024	320	UFBGA216 TFBGA216	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	168	6	2	3	4	4+4	2	2	1	1	1.7 to 3.6	2.5	420	-40 to +105
STM32F746VG	1024	320	LQFP100	12x16-bit / 2x32-bit		16x12-bit	2x12-bit	82	4	2	2	4	4+4	2	2	1	1	1.7 to 3.6	2.5	420	-40 to +105

# STM32 F7 SERIES - ARM® CORTEX®-M7 HIGH-PERFORMANCE MCUs

				Timer f	unctions								Serial in	iterfac	е				Supply (		Maximum
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Package	16-/32-bit timers	Others	ADC	DAC	I/Os	SPI	SAI	I <sup>2</sup> S	I <sup>2</sup> C	USART + UART <sup>2</sup>	USB OTG	CAN 2.0B	SDIO	Ethernet MAC10 /100	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	operating temperature range (°C)
STM32F746ZG	1024	320	LQFP144 WLCSP143	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	114	6	2	3	4	4+4	2	2	1	1	1.7 to 3.6	2.5	420	-40 to +105
STM32F756BG <sup>1</sup>	1024	320	LQFP208	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	168	6	2	3	4	4+4	2	2	1	1	1.7 to 3.6	2.5	420	-40 to +105
STM32F756IG <sup>1</sup>	1024	320	LQFP176	12x16-bit / 2x32-bit	2x WDG, RTC, 24-bit	24x12-bit	2x12-bit	140	6	2	3	4	4+4	2	2	1	1	1.7 to 3.6	2.5	420	-40 to +105
STM32F756NG <sup>1</sup>	1024	320	UFBGA216 TFBGA216	12x16-bit / 2x32-bit	downcounter, LP Timer	24x12-bit	2x12-bit	168	6	2	3	4	4+4	2	2	1	1	1.7 to 3.6	2.5	420	-40 to +105
STM32F756VG <sup>1</sup>	1024	320	LQFP100	12x16-bit / 2x32-bit		16x12-bit	2x12-bit	82	4	2	2	4	4+4	2	2	1	1	1.7 to 3.6	2.5	420	-40 to +105
STM32F756ZG <sup>1</sup>	1024	320	LQFP144 WLCSP143	12x16-bit / 2x32-bit		24x12-bit	2x12-bit	114	6	2	3	4	4+4	2	2	1	1	1.7 to 3.6	2.5	420	-40 to +105

### Notes:

<sup>1.</sup> Crypto/hash processor on STM32F756.
2. Marked in the table (3+2) means 3 USART and 2 UART. All UARTs have LIN master/slave function. All USARTs have IrDA, ISO 7816, modem control and LIN master/slave functions.

					Timer fu	nctions	Ana	log				Seri	al interfac	е			Supply co			
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Data EEPROM (Kbytes)	Package	16-bit timers + LP¹ 16-bit timers	Others	ADC <sup>2</sup>	DAC	I/0s	SPI	I <sup>2</sup> S	I <sup>2</sup> C	USART + LP¹ UART	USB FS³	AES	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Display controller (LCD)	Operating temperature range (°C)*
						ST	M32L0x1 -	32 MHz C	PU - Acces	s line	;									
STM32L031C4	16	8	1	LQFP48	4x16-bit / 1x16-bit LP		10x12-bit		38	1		1	1+1			1.65 to 3.6	0.25	87		-40 to +125
STM32L031E4	16	8	1	WLCSP25	4x16-bit / 1x16-bit LP		10x12-bit		20	1		1	1+1			1.65 to 3.6	0.25	87		-40 to +125
STM32L031F4	16	8	1	TSS0P20	4x16-bit / 1x16-bit LP		10x12-bit		15	1		1	1+1			1.65 to 3.6	0.25	87		-40 to +125
STM32L041F44	16	8	1	TSS0P20	4x16-bit / 1x16-bit LP		10x12-bit		15	1		1	1+1		Yes	1.65 to 3.6	0.25	87		-40 to +125
STM32L031G4	16	8	1	UQFN28	4x16-bit / 1x16-bit LP		10x12-bit		23	1		1	1+1			1.65 to 3.6	0.25	87		-40 to +125
STM32L031K4	16	8	1	UQFN32	4x16-bit / 1x16-bit LP	0 . T. I	10x12-bit		27	1		1	1+1			1.65 to 3.6	0.25	87		-40 to +125
STM32L031C6	32	8	1	LQFP48	4x16-bit / 1x16-bit LP	SysTick, 2 x WDG, RTC	10x12-bit		38	1		1	1+1			1.65 to 3.6	0.25	87		-40 to +125
STM32L041C64	32	8	1	LQFP48	4x16-bit / 1x16-bit LP	NIU	10x12-bit		38	1		1	1+1		Yes	1.65 to 3.6	0.25	87		-40 to +125
STM32L031E6	32	8	1	WLCSP25	4x16-bit / 1x16-bit LP		10x12-bit		20	1		1	1+1			1.65 to 3.6	0.25	87		-40 to +125
STM32L031G6	32	8	1	UQFN28	4x16-bit / 1x16-bit LP		10x12-bit		23	1		1	1+1			1.65 to 3.6	0.25	87		-40 to +125
STM32L041G64	32	8	1	UQFN28	4x16-bit / 1x16-bit LP		10x12-bit		23	1		1	1+1		Yes	1.65 to 3.6	0.25	87		-40 to +125
STM32L031K6	32	8	1	UQFN32	4x16-bit / 1x16-bit LP		10x12-bit		27	1		1	1+1			1.65 to 3.6	0.25	87		-40 to +125
STM32L041K64	32	8	1	UQFN32	4x16-bit / 1x16-bit LP		10x12-bit		27	1		1	1+1		Yes	1.65 to 3.6	0.25	87		-40 to +125
STM32L051C6	32	8	2	LQFP48	5x16-bit / 1x16-bit LP	SvsTick.	10x12-bit		37	2	1	2	3+1			1.65 to 3.6	0.25	87		-40 to +125
STM32L051K6	32	8	2	LQFP32 UFQFPN32	5x16-bit / 1x16-bit LP	2 x WDG, RTC	10x12-bit		27	1		1	3+0			1.65 to 3.6	0.25	87		-40 to +125
STM32L051R6	32	8	2	LQFP64 TFBGA64	5x16-bit / 1x16-bit LP	1110	16x12-bit		51	2	1	2	3+1			1.65 to 3.6	0.25	87		-40 to +125

					Timer fu	nctions	Ana	log				Seri	ial interfac	е			Supply cu (lcc)			
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Data EEPROM (Kbytes)	Package	16-bit timers + LP¹ 16-bit timers	Others	ADC <sup>2</sup>	DAC	I/Os	SPI	I <sup>2</sup> S	I <sup>2</sup> C	USART + LP¹ UART		AES	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Display controller (LCD)	Operating temperature range (°C)*
STM32L051T6	32	8	2	WLCSP36	5x16-bit / 1x16-bit LP		16x12-bit		29	1		2	3+1			1.65 to 3.6	0.25	87		-40 to +125
STM32L051C8	64	8	2	LQFP48	5x16-bit / 1x16-bit LP		10x12-bit		37	2	1	2	3+1			1.65 to 3.6	0.25	87		-40 to +125
STM32L051K8	64	8	2	LQFP32 UFQFPN32	5x16-bit / 1x16-bit LP		10x12-bit		27	1		1	3+0			1.65 to 3.6	0.25	87		-40 to +125
STM32L051R8	64	8	2	LQFP64 TFBGA64	5x16-bit / 1x16-bit LP		16x12-bit		51	2	1	2	3+1			1.65 to 3.6	0.25	87		-40 to +125
STM32L051T8	64	8	2	WLCSP36	5x16-bit / 1x16-bit LP		16x12-bit		29	1		2	3+1			1.65 to 3.6	0.25	87		-40 to +125
STM32L071K8	64	20	3	LQFP32 UFQFPN32	7x16-bit / 1x16-bit LP		16x12-bit		25	2	1	3	4+1			1.65 to 3.6	0.25	87		-40 to +125
STM32L071V8	64	20	3	LQFP100 UBGA100	7x16-bit / 1x16-bit LP		16x12-bit		84	2	1	2	3+1			1.65 to 3.6	0.25	87		-40 to +125
STM32L071CB	128	20	3	LQFP48	7x16-bit / 1x16-bit LP	SysTick,	16x12-bit		40	2	1	2	3+1			1.65 to 3.6	0.25	87		-40 to +125
STM32L081CB4	128	20	3	WLCSP36	7x16-bit / 1x16-bit LP	2 x WDG, RTC	16x12-bit		40	2	1	2	3+1		Yes	1.65 to 3.6	0.25	87		-40 to +125
STM32L071KB	128	20	3	LQFP32 UFQFPN32		1110	16x12-bit		25	2	1	3	4+1			1.65 to 3.6	0.25	87		-40 to +125
STM32L071RB	128	20	3	LQFP32 TFBGA64	7x16-bit / 1x16-bit LP		16x12-bit		51	2	1	2	3+1			1.65 to 3.6	0.25	87		-40 to +125
STM32L081RB4	128	20	3	TFBGA64	7x16-bit / 1x16-bit LP		16x12-bit		51	2	1	2	3+1		Yes	1.65 to 3.6	0.25	87		-40 to +125
STM32L071VB	128	20	3	LQFP100 UBGA100	7x16-bit / 1x16-bit LP		16x12-bit		84	2	1	2	3+1			1.65 to 3.6	0.25	87		-40 to +125
STM32L071CZ	192	20	6	LQFP48	7x16-bit / 1x16-bit LP		16x12-bit		40	2	1	2	3+1			1.65 to 3.6	0.25	87		-40 to +125
STM32L071KZ	192	20	3	UFQFN32 LQFP32	7x16-bit / 1x16-bit LP		16x12-bit		25	2	1	3	4+1			1.65 to 3.6	0.25	87		-40 to +125
STM32L071RZ	192	20	6	LQFP64 TFBGA64	7x16-bit / 1x16-bit LP		16x12-bit		51	2	1	2	3+1			1.65 to 3.6	0.25	87		-40 to +125
STM32L071VZ	192	20	6	LQFP100 UBGA100	7x16-bit / 1x16-bit LP		16x12-bit		84	2	1	2	3+1			1.65 to 3.6	0.25	87		-40 to +125

					Timer fu	nctions	Ana	llog				Seri	ial interfac	е			Supply cu			
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Data EEPROM (Kbytes)	Package	16-bit timers + LP¹ 16-bit timers	Others	ADC <sup>2</sup>	DAC	I/Os	SPI	I <sup>2</sup> S	I <sup>2</sup> C	USART + LP¹ UART	USB FS <sup>3</sup>	AES	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Display controller (LCD)	Operating temperature range (°C)*
						ST	M32L0x2 -	32 MHz C	PU - USB li	ne										
STM32L052C6	32	8	2	LQFP48	5x16-bit / 1x16-bit LP		10x12-bit	1x12-bit	37	2	1	2	3+1	1		1.65 to 3.6	0.25	87		-40 to +125
STM32L052K6	32	8	2	LQFP32 UFQFPN32	5x16-bit / 1x16-bit LP		10x12-bit	1x12-bit	27	1		1	3+0	1		1.65 to 3.6	0.25	87		-40 to +125
STM32L052R6	32	8	2	LQFP64 TFBGA64	5x16-bit / 1x16-bit LP		16x12-bit	1x12-bit	51	2	1	2	3+1	1		1.65 to 3.6	0.25	87		-40 to +125
STM32L052T6	32	8	2	WLCSP36	5x16-bit / 1x16-bit LP		10x12-bit	1x12-bit	37	2		1	3+1	1		1.65 to 3.6	0.25	87		-40 to +125
STM32L052T8	64	8	2	WLCSP36	5x16-bit / 1x16-bit LP		10x12-bit	1x12-bit	37	2		1	3+1	1		1.65 to 3.6	0.25	87		-40 to +125
STM32L052C8	64	8	2	LQFP48	5x16-bit / 1x16-bit LP		10x12-bit	1x12-bit	37	2	1	2	3+1	1		1.65 to 3.6	0.25	87		-40 to +125
STM32L052K8	64	8	2	LQFP32 UFQFPN32	5x16-bit / 1x16-bit LP		10x12-bit	1x12-bit	27	1		1	3+0	1		1.65 to 3.6	0.25	87		-40 to +125
STM32L062K84	64	8	2	LQFP32	5x16-bit / 1x16-bit LP	SvsTick.	10x12-bit	1x12-bit	27	1		1	3+0	1	Yes	1.65 to 3.6	0.25	87		-40 to +125
STM32L052R8	64	8	2	LQFP64 TFBGA64	5x16-bit / 1x16-bit LP	2 x WDG, RTC	16x12-bit	1x12-bit	51	2	1	2	3+1	1		1.65 to 3.6	0.25	87		-40 to +125
STM32L072V8	64	20	6	LQFP100 UBGA100	7x16-bit / 1x16-bit LP	1110	16x12-bit	2x12-bit	84	2	1	3	3+1	1		1.65 to 3.6	0.25	87		-40 to +125
STM32L072KB	128	20	6	UFQFPN32 LQFP32	7x16-bit / 1x16-bit LP		16x12-bit	2x12-bit	25	2	1	3	4+1	1		1.65 to 3.6	0.25	87		-40 to +125
STM32L082KB4	128	20	6	UFQFPN32	7x16-bit / 1x16-bit LP		16x12-bit	2x12-bit	25	2	1	3	4+1	1	Yes	1.65 to 3.6	0.25	87		-40 to +125
STM32L072CB	128	20	6	LQFP48	7x16-bit / 1x16-bit LP		16x12-bit	2x12-bit	40	2	1	3	4+1	1		1.65 to 3.6	0.25	87		-40 to +125
STM32L072RB	128	20	6	LQFP64 TFBGA64	7x16-bit / 1x16-bit LP		16x12-bit	2x12-bit	51	2	1	3	4+1	1		1.65 to 3.6	0.25	87		-40 to +125
STM32L072VB	128	20	6	LQFP100 UBGA100	7x16-bit / 1x16-bit LP		16x12-bit	2x12-bit	84	2	1	3	4+1	1		1.65 to 3.6	0.25	87		-40 to +125
STM32L072KZ	192	20	6	QFN32 WLCSP36	7x16-bit / 1x16-bit LP		16x12-bit	2x12-bit	25	2	1	3	4+1	1		1.65 to 3.6	0.25	87		-40 to +125
STM32L072CZ	192	20	6	LQFP48	7x16-bit / 1x16-bit LP		16x12-bit	2x12-bit	40	2	1	3	4+1	1		1.65 to 3.6	0.25	87		-40 to +125

					Timer fu	nctions	Ana	log				Seri	al interfac	е			Supply cu			
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Data EEPROM (Kbytes)	Package	16-bit timers + LP¹ 16-bit timers	Others	ADC <sup>2</sup>	DAC	I/Os	SPI	I <sup>2</sup> S	I <sup>2</sup> C	USART + LP¹ UART	USB FS³	AES	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Display controller (LCD)	Operating temperature range (°C)*
STM32L072RZ	192	20	6	LQFP64 TFBGA64	7x16-bit / 1x16-bit LP	SysTick, 2 x WDG.	16x12-bit	2x12-bit	51	2	1	3	4+1	1		1.65 to 3.6	0.25	87		-40 to +125
STM32L072VZ	192	20	6	LQFP100 UBGA100	7x16-bit / 1x16-bit LP	RTC	16x12-bit	2x12-bit	84	2	1	3	4+1	1		1.65 to 3.6	0.25	87		-40 to +125
						STI	M32L0x3 -	32 MHz (	PU - USB a	nd L(	CD lir	ne 💮								
STM32L053C6	32	8	2	LQFP48	5x16-bit / 1x16-bit LP		10x12-bit	1x12-bit	37	2	1	2	3+1	1		1.65 to 3.6	0.25	87	4x18	-40 to +125
STM32L053R6	32	8	2	LQFP64 TFBGA64	5x16-bit / 1x16-bit LP		16x12-bit	1x12-bit	51	2	1	2	3+1	1		1.65 to 3.6	0.25	87	4x31/8x28	-40 to +125
STM32L053C8	64	8	2	LQFP48	5x16-bit / 1x16-bit LP		10x12-bit	1x12-bit	37	2	1	2	3+1	1		1.65 to 3.6	0.25	87	4x18	-40 to +125
STM32L063C84	64	8	2	LQFP48	5x16-bit / 1x16-bit LP		10x12-bit	1x12-bit	37	2	1	2	3+1	1	Yes	1.65 to 3.6	0.25	87	4x18	-40 to +125
STM32L053R8	64	8	2	LQFP64 TFBGA64	5x16-bit / 1x16-bit LP		16x12-bit	1x12-bit	51	2	1	2	3+1	1		1.65 to 3.6	0.25	87	4x31/8x28	-40 to +125
STM32L063R84	64	8	2	LQFP64 TBGA64	5x16-bit / 1x16-bit LP		16x12-bit	1x12-bit	51	2	1	2	3+1	1	Yes	1.65 to 3.6	0.25	87	4x31/8x28	-40 to +125
STM32L073V8	64	20	3	LQFP100 UBGA100	7x16-bit / 1x16-bit LP	SysTick, 2 x WDG.	16x12-bit	2x12-bit	84	2	1	3	4+1	1		1.65 to 3.6	0.25	87	8x48/4x52	-40 to +125
STM32L083V84	64	20	3	LQFP100 UBGA100	7x16-bit / 1x16-bit LP	RTC	16x12-bit	2x12-bit	84	2	1	3	4+1	1	Yes	1.65 to 3.6	0.25	87	8x48 4x52	-40 to +125
STM32L073RB	128	20	6	LQFP64 TBGA64	7x16-bit / 1x16-bit LP		16x12-bit	2x12-bit	51	2	1	3	4+1	1		1.65 to 3.6	0.25	87	8x28/4x32	-40 to +125
STM32L083RB4	128	20	6	LQFP64 TBGA64	7x16-bit / 1x16-bit LP		16x12-bit	2x12-bit	51	2	1	3	4+1	1	Yes	1.65 to 3.6	0.25	87	8x28 4x32	-40 to +125
STM32L073VB	128	20	6	LQFP100 UBGA100	7x16-bit / 1x16-bit LP		16x12-bit	2x12-bit	84	2	1	3	4+1	1		1.65 to 3.6	0.25	87	8x48/4x52	-40 to +125
STM32L083VB <sup>4</sup>	128	20	6	LQFP100 UBGA100	7x16-bit / 1x16-bit LP		16x12-bit	2x12-bit	84	2	1	3	4+1	1	Yes	1.65 to 3.6	0.25	87	8x48 4x52	-40 to +125
STM32L073CZ	192	20	6	LQFP48	7x16-bit / 1x16-bit LP		16x12-bit	2x12-bit	40	2	1	3	4+1	1		1.65 to 3.6	0.25	87	4x18	-40 to +125
STM32L083CZ <sup>4</sup>	192	20	6	LQFP48	7x16-bit / 1x16-bit LP		16x12-bit	2x12-bit	40	2	1	3	4+1	1	Yes	1.65 to 3.6	0.25	87	4x18	-40 to +125

					Timer fu	nctions	Ana	alog				Seri	ial interfac	е			Supply cu (Icc)			
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Data EEPROM (Kbytes)	Package	16-bit timers + LP¹ 16-bit timers	Others	ADC <sup>2</sup>	DAC	I/0s	SPI	I <sup>2</sup> S	I <sup>2</sup> C	USART + LP¹ UART	USB FS3		Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Display controller (LCD)	Operating temperature range (°C)*
STM32L073RZ	192	20	6	LQFP64 TBGA64	7x16-bit / 1x16-bit LP		16x12-bit	2x12-bit	51	2	1	3	4+1	1		1.65 to 3.6	0.25		8x28/4x32	-40 to +125
STM32L083RZ <sup>4</sup>	192	20	6	LQFP64	7x16-bit / 1x16-bit LP	SysTick, 2 x WDG,	16x12-bit	2x12-bit	51	2	1	3	4+1	1	Yes	1.65 to 3.6	0.25	87	8x28/4x32	-40 to +125
STM32L073VZ	192	20	6	LQFP100 UBGA100	7x16-bit / 1x16-bit LP	RTC	16x12-bit	2x12-bit	84	2	1	3	4+1	1		1.65 to 3.6	0.25	87	8x48/4x52	-40 to +125
STM32L083VZ <sup>4</sup>	192	20	6	LQFP100	7x16-bit / 1x16-bit LP		16x12-bit	2x12-bit	84	2	1	3	4+1	1	Yes	1.65 to 3.6	0.25	87	8x28/4x32	-40 to +125

- 1. « Low-Power peripheral available in stop mode »
  2. « 16-bit Hardware oversampling capable »
  3. « USB 2.0 FS certified. USB support Battery Charging Detection (BCD) and Link Power Management (LPM). »
  4. Crypto/hash processor on STM32L041/L062/L081/L082/L083

					Timer fu	ınctions	1	Analog					S	erial into	erface				Supply o			
Part number		Internal RAM size (Kbytes)	Data EEPROM (Kbytes)	Package	16-/32-bit timers	Others	ADC	DAC	Op Amp	I/Os	SPI	I <sup>2</sup> S	I <sup>2</sup> C	USART	USB FS	SDIO F	MC	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Display controller (LCD)	Operating temperature range (°C)
							STM32L	100 Value	line -	32 MF	lz CF	U										
STM32L100C6	32	4	2	UFQFPN48	6x16-bit		14x12-bit		N/A	37	2		2	3	1			1.8 to 3.6	0.28	177	4x16	-40 to +105
STM32L100C6-A	32	4	2	UFQFPN48	6x16-bit		14x12-bit	2x12-bit	N/A	37	2		2	3	1			1.8 to 3.6	0.28	177	4x16	-40 to +105
STM32L100R8	64	8	2	LQFP64	6x16-bit	SysTick,	20x12-bit	2x12-bit	N/A	51	2		2	3	1			1.8 to 3.6	0.28	177	4x32/8x28	-40 to +105
STM32L100R8-A		8	2	LQFP64	6x16-bit	2 x WDG,	20x12-bit		N/A	51	2		2	3	1			1.8 to 3.6	0.28	177		-40 to +105
STM32L100RB	128	10	2	LQFP64	6x16-bit	RTCv1	20x12-bit	2x12-bit	N/A	51	2		2	3	1			1.8 to 3.6	0.28	177	4x32/8x28	-40 to +105
STM32L100RB-A	128	10	2	LQFP64	6x16-bit		20x12-bit	2x12-bit	N/A	51	2		2	3	1			1.8 to 3.6	0.28	177		-40 to +105
STM32L100RC	256	16	4	LQFP64	8x16-bit		21x12-bit	2x12-bit	2	50	3	2	2	2	1			1.65 to 3.6	0.28	177	4x32/8x28	-40 to +105
							STM3	2L151/152	1 - 32	MHz	CPU											
STM32L151C6	32	10	4	LQFP48, UFQFPN48	6x16-bit		14x12-bit	2x12-bit	N/A	37	2		2	3	1			1.65 to 3.6	0.28	177		-40 to +105
STM32L151C6-A	32	16	4	LQFP48, UFQFPN48	8x16-bit		14x12-bit	2x12-bit	N/A	37	2		2	3	1			1.65 to 3.6	0.28	177		-40 to +105
STM32L151R6	32	10	4	LQFP64, TFBGA64	6x16-bit		20x12-bit	2x12-bit	N/A	51	2		2	3	1			1.65 to 3.6	0.28	177		-40 to +105
STM32L151R6-A	32	16	4	LQFP64, TFBGA64	8x16-bit		20x12-bit	2x12-bit	N/A	51	2		2	3	1			1.65 to 3.6	0.28	177		-40 to +105
STM32L152C6	32	10	4	LQFP48, UFQFPN48	6x16-bit	SysTick,	14x12-bit	2x12-bit	N/A	37	2		2	3	1			1.65 to 3.6	0.28	177	4x18	-40 to +105
STM32L152C6-A	32	16	4	LQFP48, UFQFPN48	8x16-bit	2 x WDG, RTCv1	14x12-bit	2x12-bit	N/A	37	2		2	3	1			1.65 to 3.6	0.28	177	4x18	-40 to +105
STM32L152R6	32	10	4	LQFP64, TFBGA64	6x16-bit	111011	20x12-bit	2x12-bit	N/A	51	2		2	3	1			1.65 to 3.6	0.28	177	4x32/8x28	-40 to +105
STM32L152R6-A	32	16	4	LQFP64, TFBGA64	8x16-bit		20x12-bit	2x12-bit	N/A	51	2		2	3	1			1.65 to 3.6	0.28	177	4x32/8x28	-40 to +105
STM32L151C8	64	10	4	LQFP48, UFQFPN48	6x16-bit		14x12-bit	2x12-bit	N/A	37	2		2	3	1			1.65 to 3.6	0.28	177		-40 to +105
STM32L151C8-A	64	32	4	LQFP48, UFQFPN48	8x16-bit		14x12-bit	2x12-bit	N/A	37	2		2	3	1			1.65 to 3.6	0.28	177		-40 to +105
STM32L151R8	64	10	4	LQFP64, TFBGA64	6x16-bit		20x12-bit	2x12-bit	N/A	51	2		2	3	1			1.65 to 3.6	0.28	177		-40 to +105

					Timer fu	ınctions	1	Analog					S	erial int	erfac	е			Supply o			
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Data EEPROM (Kbytes)	Package	16-/32-bit timers	Others	ADC	DAC	Op Amp	I/Os	SPI	I <sup>2</sup> S	I <sup>2</sup> C	USART	USB FS	SDI0	FMC	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Display controller (LCD)	Operating temperature range (°C)
STM32L151R8-A	64	32	4	LQFP64, TFBGA64	8x16-bit		20x12-bit	2x12-bit	N/A	51	2		2	3	1			1.65 to 3.6	0.28	177		-40 to +105
STM32L151V8	64	10	4	LQFP100, UFBGA100	6x16-bit		24x12-bit	2x12-bit	N/A	83	2		2	3	1			1.65 to 3.6	0.28	177		-40 to +105
STM32L151V8-A	64	32	4	LQFP100, UFBGA100	8x16-bit		24x12-bit	2x12-bit	N/A	83	2		2	3	1			1.65 to 3.6	0.28	177		-40 to +105
STM32L152C8	64	10	4	LQFP48, UFQFPN48	6x16-bit		14x12-bit	2x12-bit	N/A	37	2		2	3	1			1.65 to 3.6	0.28	177	4x18	-40 to +105
STM32L152C8-A	64	32	4	LQFP48, UFQFPN48	8x16-bit		14x12-bit	2x12-bit	N/A	37	2		2	3	1			1.65 to 3.6	0.28	177	4x18	-40 to +105
STM32L152R8	64	10	4	LQFP64, TFBGA64	6x16-bit		20x12-bit	2x12-bit	N/A	51	2		2	3	1			1.65 to 3.6	0.28	177	4x32/8x28	-40 to +105
STM32L152R8-A	64	32	4	LQFP64, TFBGA64	8x16-bit		20x12-bit	2x12-bit	N/A	51	2		2	3	1			1.65 to 3.6	0.28	177	4x32/8x28	-40 to +105
STM32L152V8	64	10	4	LQFP100, UFBGA100	6x16-bit	SysTick,	24x12-bit	2x12-bit	N/A	83	2		2	3	1			1.65 to 3.6	0.28	177	4x44/8x40	-40 to +105
STM32L152V8-A	64	32	4	LQFP100, UFBGA100	8x16-bit	2 x WDG, RTCv1	24x12-bit	2x12-bit	N/A	83	2		2	3	1			1.65 to 3.6	0.28	177	4x44/8x40	-40 to +105
STM32L151CB	128	16	4	LQFP48, UFQFPN48	6x16-bit		14x12-bit	2x12-bit	N/A	37	2		2	3	1			1.65 to 3.6	0.28	177		-40 to +105
STM32L151CB-A	128	32	4	LQFP48, UFQFPN48	8x16-bit		14x12-bit	2x12-bit	N/A	37	2		2	3	1			1.65 to 3.6	0.28	177		-40 to +105
STM32L151RB	128	16	4	LQFP64, TFBGA64	6x16-bit		20x12-bit	2x12-bit	N/A	51	2		2	3	1			1.65 to 3.6	0.28	177		-40 to +105
STM32L151RB-A	128	32	4	LQFP64, TFBGA64	8x16-bit		20x12-bit	2x12-bit	N/A	51	2		2	3	1			1.65 to 3.6	0.28	177		-40 to +105
STM32L151VB	128	16	4	LQFP100, UFBGA100	6x16-bit		24x12-bit	2x12-bit	N/A	83	2		2	3	1			1.65 to 3.6	0.28	177		-40 to +105
STM32L151VB-A	128	32	4	LQFP100, UFBGA100	8x16-bit		24x12-bit	2x12-bit	N/A	83	2		2	3	1			1.65 to 3.6	0.28	177		-40 to +105
STM32L152CB	128	16	4	LQFP48, UFQFPN48	6x16-bit		14x12-bit	2x12-bit	N/A	37	2		2	3	1			1.65 to 3.6	0.28	177	4x18	-40 to +105
STM32L152CB-A	128	32	4	LQFP48, UFQFPN48	8x16-bit		14x12-bit	2x12-bit	N/A	37	2		2	3	1			1.65 to 3.6	0.28	177	4x18	-40 to +105

					Timer fu	nctions	1	Analog					S	Serial int	erfac	е			Supply o			
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Data EEPROM (Kbytes)	Package	16-/32-bit timers	Others	ADC	DAC	Op Amp	I/Os	SPI	I <sup>2</sup> S	I <sup>2</sup> C	USART	USB FS	SDIO	FMC	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Display controller (LCD)	Operating temperature range (°C)
STM32L152RB	128	16	4	LQFP64, TFBGA64	6x16-bit		20x12-bit	2x12-bit	N/A	51	2		2	3	1			1.65 to 3.6	0.28	177	4x32/8x28	-40 to +105
STM32L152RB-A	128	32	4	LQFP64, TFBGA64	8x16-bit	SysTick, 2 x WDG.	20x12-bit	2x12-bit	N/A	51	2		2	3	1			1.65 to 3.6	0.28	177	4x32/8x28	-40 to +105
STM32L152VB	128	16	4	LQFP100, UFBGA100	6x16-bit	RTCv1	24x12-bit	2x12-bit	N/A	83	2		2	3	1			1.65 to 3.6	0.28	177	4x44/8x40	-40 to +105
STM32L152VB-A	128	32	4	LQFP100, UFBGA100	8x16-bit		24x12-bit	2x12-bit	N/A	83	2		2	3	1			1.65 to 3.6	0.28	177	4x44/8x40	-40 to +105
STM32L151CC	256	32	8	LQFP48, UFQFPN48	8x16-bit/ 1x32-bit		14x12-bit	2x12-bit	2	37	3	2	2	3	1			1.65 to 3.6	0.28	177		-40 to +105
STM32L151UC	256	32	8	WLCSP63	8x16-bit/ 1x32-bit		21x12-bit	2x12-bit	2	51	3	2	2	3	1			1.65 to 3.6	0.28	177		-40 to +105
STM32L151RC	256	32	8	LQFP64	8x16-bit/ 1x32-bit		21x12-bit	2x12-bit	2	51	3	2	2	3	1			1.65 to 3.6	0.28	177		-40 to +105
STM32L151RC-A	256	32	8	LQFP64, WLCSP64	8x16-bit/ 1x32-bit		21x12-bit	2x12-bit	2	51	3	2	2	3	1			1.65 to 3.6	0.28	177		-40 to +105
STM32L151VC	256	32	8	LQFP100	8x16-bit/ 1x32-bit		25x12-bit	2x12-bit	2	83	3	2	2	3	1			1.65 to 3.6	0.28	177		-40 to +105
STM32L151QC	256	32	8	UFBGA132	8x16-bit/ 1x32-bit	SysTick,	40x12-bit	2x12-bit	2	109	3	2	2	3	1			1.65 to 3.6	0.28	177		-40 to +105
STM32L151ZC	256	32	8	LQFP144	8x16-bit/ 1x32-bit	2 x WDG, RTCv2	40x12-bit	2x12-bit	2	115	3	2	2	3	1			1.65 to 3.6	0.28	177		-40 to +105
STM32L152CC	256	32	8	LQFP48, UFQFPN48	8x16-bit/ 1x32-bit		14x12-bit	2x12-bit	2	37	3	2	2	3	1			1.65 to 3.6	0.28	177	4x18	-40 to +105
STM32L152UC	256	32	8	WLCSP63	8x16-bit/ 1x32-bit		21x12-bit	2x12-bit	2	51	3	2	2	3	1			1.65 to 3.6	0.28	177	4x32/8x28	-40 to +105
STM32L152RC	256	32	8	LQFP64	8x16-bit/ 1x32-bit		21x12-bit	2x12-bit	2	51	3	2	2	3	1			1.65 to 3.6	0.28	177	4x32/8x28	-40 to +105
STM32L152RC-A	256	32	8	LQFP64, WLCSP64	8x16-bit/ 1x32-bit		21x12-bit	2x12-bit	2	51	3	2	2	3	1			1.65 to 3.6	0.28	177	4x32/8x28	-40 to +105
STM32L152VC	256	32	8	LQFP100	8x16-bit / 1x32-bit		25x12-bit	2x12-bit	2	83	3	2	2	3	1			1.65 to 3.6	0.28	177	4x44/8x40	-40 to +105
STM32L152QC	256	32	8	UFBGA132	8x16-bit / 1x32-bit		40x12-bit	2x12-bit	2	109	3	2	2	3	1			1.65 to 3.6	0.28	177	4x44/8x40	-40 to +105

					Timer fu	nctions		Analog					s	erial into	erfac	e			Supply o			
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Data EEPROM (Kbytes)	Package	16-/32-bit timers	Others	ADC	DAC	Op Amp	I/Os	SPI	I <sup>2</sup> S	I <sup>2</sup> C	USART	USB FS	SDIO	FMC	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Display controller (LCD)	Operating temperature range (°C)
STM32L152ZC	256	32	8	LQFP144	8x16-bit / 1x32-bit		40x12-bit	2x12-bit	2	115	3	2	2	3	1			1.65 to 3.6	0.28	177	4x44/8x40	-40 to +105
STM32L151QD	384	48	12	UFBGA132	8x16-bit / 1x32-bit		40x12-bit	2x12-bit	3	109	3	2	2	5	1	1	1	1.65 to 3.6	0.28	177		-40 to +105
STM32L151RD	384	48	12	LQFP64 WLCSP64	8x16-bit / 1x32-bit		21x12-bit	2x12-bit	3	51	3	2	2	5	1	1	1	1.65 to 3.6	0.28	177		-40 to +105
STM32L151VD	384	48	12	LQFP100	8x16-bit / 1x32-bit		25x12-bit	2x12-bit	3	83	3	2	2	5	1	1	1	1.65 to 3.6	0.28	177		-40 to +105
STM32L151VD-X	384	80	16	BGA100 LQFP100 WLCSP100	8x16-bit / 1x32-bit		25x12-bit	2x12-bit	2	82	3	2	2	5	1			1.65 to 3.6	0.3	230		-40 to +85
STM32L151ZD	384	48	12	LQFP144	8x16-bit/ 1x32-bit		40x12-bit	2x12-bit	3	115	3	2	2	5	1	1	1	1.65 to 3.6	0.28	177		-40 to +105
STM32L152QD	384	48	12	UFBGA132	8x16-bit/ 1x32-bit	SysTick, 2 x WDG.	40x12-bit	2x12-bit	3	109	3	2	2	5	1	1	1	1.65 to 3.6	0.28	177	4x44/8x40	-40 to +105
STM32L152RD	384	48	12	LQFP64 WLCSP64	8x16-bit/ 1x32-bit	RTCv2	21x12-bit	2x12-bit	3	51	3	2	2	5	1	1		1.65 to 3.6	0.28	177	4x32/8x28	-40 to +105
STM32L152VD	384	48	12	LQFP100	8x16-bit/ 1x32-bit		25x12-bit	2x12-bit	3	83	3	2	2	5	1	1	1	1.65 to 3.6	0.28	177	4x44/8x40	-40 to +105
STM32L152VD-X	384	80	16	BGA100 LQFP100 WLCSP100	8x16-bit/ 1x32-bit		40x12-bit	2x12-bit	2	82	3	2	2	5	1			1.65 to 3.6	0.3	230	4x44/8x40	-40 to +85
STM32L152ZD	384	48	12	LQFP144	8x16-bit/ 1x32-bit		40x12-bit	2x12-bit	3	115	3	2	2	5	1	1	1	1.65 to 3.6	0.28	177	4x44/8x40	-40 to +105
STM32L151QE	512	80	16	UFBGA132	8x16-bit/ 1x32-bit		40x12-bit	2x12-bit	2	109	3	2	2	5	1			1.65 to 3.6	0.28	177		-40 to +105
STM32L151RE	512	80	16	LQFP64	8x16-bit/ 1x32-bit		21x12-bit	2x12-bit	2	51	3	2	2	5	1			1.65 to 3.6	0.28	177		-40 to +105
STM32L151VE	512	80	16	LQFP100 WLCSP104	8x16-bit/ 1x32-bit		25x12-bit	2x12-bit	2	83	3	2	2	5	1			1.65 to 3.6	0.28	177		-40 to +105
STM32L151ZE	512	80	16	LQFP144	8x16-bit / 1x32-bit		40x12-bit	2x12-bit	2	115	3	2	2	5	1			1.65 to 3.6	0.28	177		-40 to +105
STM32L152QE	512	80	16	UFBGA132	8x16-bit / 1x32-bit		40x12-bit	2x12-bit	2	109	3	2	2	5	1			1.65 to 3.6	0.28	177	4x44/8x40	-40 to +105

					Timer fu	nctions		Analog					S	erial into	erfac	е			Supply o			
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Data EEPROM (Kbytes)	Package	16-/32-bit timers	Others	ADC	DAC	Op Amp	I/Os	SPI	I <sup>2</sup> S	I <sup>2</sup> C	USART	USB FS	SDIO	FMC	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Display controller (LCD)	Operating temperature range (°C)
STM32L152RE	512	80	16	LQFP64	8x16-bit / 1x32-bit	0 . T l	21x12-bit	2x12-bit	2	51	3	2	2	5	1			1.65 to 3.6	0.28	177	4x32/8x28	-40 to +105
STM32L152VE	512	80	16	LQFP100 WLCSP104	8x16-bit / 1x32-bit	SysTick, 2 x WDG, RTCv2	25x12-bit	2x12-bit	2	83	3	2	2	5	1			1.65 to 3.6	0.28	177	4x44/8x40	-40 to +105
STM32L152ZE	512	80	16	LQFP144	8x16-bit / 1x32-bit	NIUVZ	40x12-bit	2x12-bit	2	115	3	2	2	5	1			1.65 to 3.6	0.28	177	4x44/8x40	-40 to +105
						ST	M32L162 <sup>1</sup> I	ine with L0	D and	I AES	- 32	MH	z CPI	J								
STM32L162RC <sup>2</sup>	256	32	8	LQFP64	8x16-bit / 1x32-bit		21x12-bit	2x12-bit	2	51	3	2	2	3	1			1.65 to 3.6	0.28	177	4x32/8x28	-40 to +105
STM32L162RC-A <sup>2</sup>	256	32	8	LQFP64	8x16-bit / 1x32-bit		21x12-bit	2x12-bit	2	51	3	2	2	3	1			1.65 to 3.6	0.28	177	4x32/8x28	-40 to +105
STM32L162VC <sup>2</sup>	256	32	8	LQFP100	8x16-bit / 1x32-bit		25x12-bit	2x12-bit	2	83	3	2	2	3	1			1.65 to 3.6	0.28	177	4x44/8x40	-40 to +105
STM32L162VC-A <sup>2</sup>	256	32	8	LQFP100	8x16-bit / 1x32-bit		25x12-bit	2x12-bit	2	83	3	2	2	3	1			1.65 to 3.6	0.28	177	4x44/8x40	-40 to +105
STM32L162QD <sup>2</sup>	384	48	12	UFBGA132	8x16-bit / 1x32-bit	SysTick,	40x12-bit	2x12-bit	3	109	3	2	2	5	1	1	1	1.65 to 3.6	0.28	177	4x44/8x40	-40 to +105
STM32L162RD <sup>2</sup>	384	48	12	LQFP64	8x16-bit / 1x32-bit	2 x WDG, RTCv2	21x12-bit	2x12-bit	3	51	3	2	2	5	1	1		1.65 to 3.6	0.28	177	4x32/8x28	-40 to +105
STM32L162VD <sup>2</sup>	384	48	12	LQFP100	8x16-bit / 1x32-bit		25x12-bit	2x12-bit	3	83	3	2	2	5	1	1	1	1.65 to 3.6	0.28	177	4x44/8x40	-40 to +105
STM32L162VD-X <sup>2</sup>	384	80	16	LQFP100 BGA100	8x16-bit / 1x32-bit		25x12-bit	2x12-bit	2	82	3	2	2	5	1			1.65 to 3.6	0.3	230	4x44/8x40	-40 to +85
STM32L162ZD <sup>2</sup>	384	48	12	LQFP144	8x16-bit / 1x32-bit		40x12-bit	2x12-bit	3	115	3	2	2	5	1	1	1	1.65 to 3.6	0.28	177	4x44/8x40	-40 to +105
STM32L162RE <sup>2</sup>	512	80	16	LQFP64	8x16-bit / 1x32-bit		21x12-bit	2x12-bit	2	51	3	2	2	5	1			1.65 to 3.6	0.28	177	4x32/8x28	-40 to +105
STM32L162VE <sup>2</sup>	512	80	16	LQFP100	8x16-bit / 1x32-bit		25x12-bit	2x12-bit	2	83	3	2	2	5	1			1.65 to 3.6	0.28	177	4x44/8x40	-40 to +105
STM32L162ZE <sup>2</sup>	512	80	16	LQFP144	8x16-bit / 1x32-bit		40x12-bit	2x12-bit	2	115	3	2	2	5	1			1.65 to 3.6	0.28	177	4x44/8x40	-40 to +105

Notes:

<sup>1.</sup> Touch-sensing FW library available for all STM32L15x and STM32L16x devices
2. Crypto/hash processor on STM32L162

\* Temperature range from -40°C to +85°C. Also available on +105°C

					Timer fu	ınctions	1	Analog					s	erial int	erfac	e			Supply o			
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	AES 128-/ 256-bit	Package	16-/32-bit timers	Others	ADC	DAC	Op Amp	I/Os	SPI	SAI	I <sup>2</sup> C	USART	USB FS	SDMMC	FMC	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Display controller (LCD)	Operating temperature range (°C)*
							S	TM32L4x6	line 8	O MHz												
STM32L476VC	256	128		LQFP100	11x16-bit/ 2x32-bit		16x12-bit	2x12-bit	2	82	3	2	3	6	1	1	1	1.71 to 3.6	0.03	100	LCD 8x40/ 4x44	-40 to +85
STM32L476RC	256	128		LQFP64	11x16-bit/ 2x32-bit		16x12-bit	2x12-bit	2	51	3	2	3	6	1	1	1	1.71 to 3.6	0.03	100	LCD 8x28/ 4x32	-40 to +85
STM32L476ZE	512	128		LQFP144	11x16-bit/ 2x32-bit		24x12-bit	2x12-bit	2	114	3	2	3	6	1	1	1	1.71 to 3.6	0.03	100	LCD 8x40/ 4x44	-40 to +85
STM32L476QE	512	128		BGA132	11x16-bit/ 2x32-bit		19x12-bit	2x12-bit	2	109	3	2	3	6	1	1	1	1.71 to 3.6	0.03	100	LCD 8x40/ 4x44	-40 to +85
STM32L476VE	512	128		BGA132	11x16-bit/ 2x32-bit		16x12-bit	2x12-bit	2	82	3	2	3	6	1	1	1	1.71 to 3.6	0.03	100	LCD 8x40/ 4x44	-40 to +85
STM32L476RE	512	128		LQFP64	11x16-bit/ 2x32-bit		16x12-bit	2x12-bit	2	51	3	2	3	6	1	1	1	1.71 to 3.6	0.03	100	LCD 8x28/ 4x32	-40 to +85
STM32L476ME	512	128		WLCSP81	11x16-bit/ 2x32-bit		16x12-bit	2x12-bit	2	65	3	2	3	6	1	1	1	1.71 to 3.6	0.03	100	LCD 8x28/ 4x32	-40 to +85
STM32L476MG	1024	128		WLCSP81	11x16-bit/ 2x32-bit	SysTick, 2 x WDG.	16x12-bit	2x12-bit	2	65	3	2	3	6	1	1	1	1.71 to 3.6	0.03	100	LCD 8x28/ 4x32	-40 to +85
STM32L476ZG	1024	128		LQFP144	11x16-bit/ 2x32-bit	RTC	24x12-bit	2x12-bit	2	114	3	2	3	6	1	1	1	1.71 to 3.6	0.03	100	LCD 8x40/ 4x44	-40 to +85
STM32L476QG	1024	128		BGA132	11x16-bit/ 2x32-bit		19x12-bit	2x12-bit	2	109	3	2	3	6	1	1	1	1.71 to 3.6	0.03	100	LCD 8x40/ 4x44	-40 to +85
STM32L476VG	1024	128		LQFP100	11 x16-bit/ 2x32-bit		16x12-bit	2x12-bit	2	82	3	2	3	6	1	1	1	1.71 to 3.6	0.03	100	LCD 8x40/ 4x44	-40 to +85
STM32L476RG	1024	128		LQFP64	11x16-bit/ 2x32-bit		16x12-bit	2x12-bit	2	51	3	2	3	6	1	1	1	1.71 to 3.6	0.03	100	LCD 8x28/ 4x32	-40 to +85
STM32L486ZG <sup>1</sup>	1024	128	Yes	LQFP144	11x16-bit/ 2x32-bit		24x12-bit	2x12-bit	2	114	3	2	3	6	1	1	1	1.71 to 3.6	0.03	100	LCD 8x40/ 4x44	-40 to +85
STM32L486QG <sup>1</sup>	1024	128	Yes	BGA132	11x16-bit/ 2x32-bit		19x12-bit	2x12-bit	2	109	3	2	3	6	1	1	1	1.71 to 3.6	0.03	100	LCD 8x40/ 4x44	-40 to +85
STM32L486VG <sup>1</sup>	1024	128	Yes	LQFP100	11x16-bit/ 2x32-bit		16x12-bit	2x12-bit	2	82	3	2	3	6	1	1	1	1.71 to 3.6	0.03	100	LCD 8x40/ 4x44	-40 to +85
STM32L486RG <sup>1</sup>	1024	128	Yes	LQFP64	11x16-bit/ 2x32-bit		16x12-bit	2x12-bit	2	51	3	2	3	6	1	1	1	1.71 to 3.6	0.03	100	LCD 8x28/ 4x32	-40 to +85

					Timer fu	nctions	1	Analog					S	erial int	erfac	e			Supply (Ic			
Part number	size	Internal RAM size (Kbytes)		Package	16-/32-bit timers	Others	ADC	DAC	Op Amp	I/Os	SPI	SAI	I <sup>2</sup> C	USART	USB FS	SDMMC	FMC	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Display controller (LCD)	Operating temperature range (°C)*
STM32L476JG	1024	128		WLCSP72	11x16-bit/ 2x32-bit	SysTick,	16x12-bit	2x12-bit	2	57	3	2	3	6	1	1	1	1.71 to 3.6	0.03	100	LCD 8x28/ 4x32	-40 to +85
STM32L486JG <sup>1</sup>	1024	128	Yes	WLCSP72	11x16-bit/ 2x32-bit	2 x WDG, RTC	16x12-bit	2x12-bit	2	57	3	2	3	6	1	1	1	1.71 to 3.6	0.03	100	LCD 8x28/ 4x32	-40 to +85
STM32L476JE	1024	128		WLCSP72	11x16-bit/ 2x32-bit		16x12-bit	2x12-bit	2	57	3	2	3	6	1	1	1	1.71 to 3.6	0.03	100	LCD 8x28/ 4x32	-40 to +85

Note

Crypto/hash processor on STM32F476

# STM8 - 8-bit microcontroller families

#### STM8S SERIES - MAINSTREAM MCUs

					Timer fund	ctions						Seri	al interface			Supply cu	ırrent (lcc)	Maximum
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Data EEPROM (bytes)	Package	8-/16-bit timers	Others	ADC	DAC	I/Os	CAN	SPI	I <sup>2</sup> C	UART (IrDA, ISO 7816)	Other	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	operating temperature range (°C)
					:	STM8S00	3/005/007	Value lin	e - 16	MHz C	PU							
STM8S003F3	8	1	128	TSSOP20 UFQFPN20	1x8-bit / 2x16-bit		5x10-bit		16		1	1	1		2.95 to 5.5	5	230	-40 to +85
STM8S003K3	8	1	128	LQFP32	1x8-bit / 2x16-bit	2 x	4x10-bit		28		1	1	1		2.95 to 5.5	5	230	-40 to +85
STM8S005C6	32	2	128	LQFP48	1x8-bit / 3x16-bit	WDG, beeper	10x10-bit		38		1	1	1		2.95 to 5.5	5	430	-40 to +85
STM8S005K6	32	2	128	LQFP32	1x8-bit / 3x16-bit	neehei	7x10-bit		25		1	1	1		2.95 to 5.5	5	430	-40 to +85
STM8S007C8	64	6	128	LQFP48	1x8-bit / 3x16-bit		10x10-bit		38		1	1	2		2.95 to 5.5	5	500	-40 to +85
						STM8S1	03/105 Acc	ess line	- 16 M	Hz CP	U							
STM8S103F2	4	1	640	S020 TSS0P20 UFQFPN20	1x8-bit / 2x16-bit		5x10-bit		16		1	1	1		2.95 to 5.5	5	230	-40 to +125
STM8S103F3	8	1	640	S020 TSS0P20 UFQFPN20	1x8-bit / 2x16-bit		5x10-bit		16		1	1	1		2.95 to 5.5	5	230	-40 to +125
STM8S103K3	8	1	640	LQFP32 SDIP32 UFQFPN32	1x8-bit / 2x16-bit	2 x	4x10-bit		28		1	1	1		2.95 to 5.5	5	230	-40 to +125
STM8S105C4	16	2	1024	LQFP48	1x8-bit / 3x16-bit	WDG, beeper	10x10-bit		38		1	1	1		2.95 to 5.5	5	430	-40 to +125
STM8S105K4	16	2	1024	LQFP32 SDIP32 UFQFPN32	1x8-bit / 3x16-bit		7x10-bit		25		1	1	1		2.95 to 5.5	5	430	-40 to +125
STM8S105S4	16	2	1024	LQFP44	1x8-bit / 3x16-bit		9x10-bit		34		1	1	1		2.95 to 5.5	5	430	-40 to +125
STM8S105C6	32	2	1024	LQFP48	1x8-bit / 3x16-bit		10x10-bit		38		1	1	1		2.95 to 5.5	5	430	-40 to +125

## STM8S SERIES - MAINSTREAM MCUs

					Timer fund	ctions						Seri	al interface			Supply cu	ırrent (lcc)	Maximum
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Data EEPROM (bytes)	Package	8-/16-bit timers	Others	ADC	DAC	I/Os	CAN	SPI	I <sup>2</sup> C	UART (IrDA, ISO 7816)	Other	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	operating temperature range (°C)
STM8S105K6	32	2	1024	LQFP32 PDIP32 UFQFPN32	1x8-bit / 3x16-bit	2 x WDG,	7x10-bit		25		1	1	1		2.95 to 5.5	5	430	-40 to +125
STM8S105S6	32	2	1024	LQFP44	1x8-bit / 3x16-bit	beeper	9x10-bit		34		1	1	1		2.95 to 5.5	5	430	-40 to +125
					S1	M8S207/	/208 Perfor	mance li	ine - <b>2</b> 4	MHz	CPU							
STM8S207C6	32	6	1024	LQFP48	1x8-bit / 3x16-bit		10x10-bit		38		1	1	2		2.95 to 5.5	5	500	-40 to +125
STM8S207K6	32	6	1024	LQFP32	1x8-bit / 3x16-bit		7x10-bit		25		1	1	1		2.95 to 5.5	5	500	-40 to +125
STM8S207R6	32	6	1024	LQFP64	1x8-bit / 3x16-bit		16x10-bit		52		1	1	2		2.95 to 5.5	5	500	-40 to +125
STM8S207S6	32	6	1024	LQFP44	1x8-bit / 3x16-bit		9x10-bit		34		1	1	2		2.95 to 5.5	5	500	-40 to +125
STM8S208C6	32	6	2048	LQFP48	1x8-bit / 3x16-bit		10x10-bit		38	1	1	1	2		2.95 to 5.5	5	500	-40 to +125
STM8S208R61	32	6	2048	LQFP64	1x8-bit / 3x16-bit		16x10-bit		52	1	1	1	2		2.95 to 5.5	5	500	-40 to +125
STM8S208S6	32	6	1536	LQFP44	1x8-bit / 3x16-bit	2 x WDG.	9x10-bit		34	1	1	1	2		2.95 to 5.5	5	500	-40 to +125
STM8S207C8	64	6	1536	LQFP48	1x8-bit / 3x16-bit	beeper	10x10-bit		38		1	1	2		2.95 to 5.5	5	500	-40 to +125
STM8S207K8	64	6	1024	LQFP32	1x8-bit / 3x16-bit		7x10-bit		25		1	1	1		2.95 to 5.5	5	500	-40 to +125
STM8S207M8	64	6	2048	LQFP80	1x8-bit / 3x16-bit		16x10-bit		68		1	1	2		2.95 to 5.5	5	500	-40 to +125
STM8S207R8	64	6	1536	LQFP64	1x8-bit / 3x16-bit		16x10-bit		52		1	1	2		2.95 to 5.5	5	500	-40 to +125
STM8S207S8	64	6	1536	LQFP44	1x8-bit / 3x16-bit		9x10-bit		34		1	1	2		2.95 to 5.5	5	500	-40 to +125
STM8S208C8	64	6	2048	LQFP48	1x8-bit / 3x16-bit		10x10-bit		38	1	1	1	2		2.95 to 5.5	5	500	-40 to +125
STM8S208M8 <sup>1</sup>	64	6	2048	LQFP80	1x8-bit / 3x16-bit		16x10-bit		68	1	1	1	2		2.95 to 5.5	5	500	-40 to +125

## STM8S SERIES - MAINSTREAM MCUs

					Timer fund	ctions						Seri	ial interface			Supply cu	ırrent (lcc)	Maximum
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Data EEPROM (bytes)	Package	8-/16-bit timers	Others	ADC	DAC	I/Os	CAN	SPI	I <sup>2</sup> C	UART (IrDA, ISO 7816)	Other	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	operating temperature range (°C)
STM8S208R8	64	6	2048	LQFP64	1x8-bit / 3x16-bit		16x10-bit		52	1	1	1	2		2.95 to 5.5	5	500	-40 to +125
STM8S208S81	64	6	1536	LQFP44	1x8-bit / 3x16-bit		9x10-bit		34	1	1	1	2		2.95 to 5.5	5	500	-40 to +125
STM8S207CB	128	6	2048	LQFP48	1x8-bit / 3x16-bit		10x10-bit		38		1	1	2		2.95 to 5.5	5	500	-40 to +125
STM8S207MB	128	6	2048	LQFP80	1x8-bit / 3x16-bit		16x10-bit		68		1	1	2		2.95 to 5.5	5	500	-40 to +125
STM8S207RB	128	6	2048	LQFP64	1x8-bit / 3x16-bit	2 x WDG.	16x10-bit		52		1	1	2		2.95 to 5.5	5	500	-40 to +125
STM8S207SB	128	6	1536	LQFP44	1x8-bit / 3x16-bit	beeper	9x10-bit		34		1	1	2		2.95 to 5.5	5	500	-40 to +125
STM8S208CB	128	6	2048	LQFP48	1x8-bit / 3x16-bit		10x10-bit		38	1	1	1	2		2.95 to 5.5	5	500	-40 to +125
STM8S208MB	128	6	2048	LQFP80	1x8-bit / 3x16-bit		16x10-bit		68	1	1	1	2		2.95 to 5.5	5	500	-40 to +125
STM8S208RB	128	6	2048	LQFP64	1x8-bit / 3x16-bit		16x10-bit		52	1	1	1	2		2.95 to 5.5	5	500	-40 to +125
STM8S208SB1	128	6	1536	LQFP44	1x8-bit / 3x16-bit		9x10-bit		34	1	1	1	2		2.95 to 5.5	5	500	-40 to +125
					STM8S903	/STM8SP	LNB1 Appl	ication s	pecific	line -	16 I	VIHz	CPU					
STM8S903F3	8	1	640	S020 TSS0P20 UFQFPN20	1x8-bit / 2x16-bit	2 x WDG.	5x10-bit		16		1	1	1		2.95 to 5.5	5	230	-40 to +125
STM8S903K3	8	1	640	LQFP32 SDIP32 UFQFPN32	1x8-bit / 2x16-bit	beeper	7x10-bit		28		1	1	1		2.95 to 5.5	5	230	-40 to +125
STM8SPLNB1	8	1	640	S020 TSS0P20			5x10-bit					4		2xDiSEqC	2.95 to 5.5	5	230	-40 to +85

Note: 1. On demand only

## STM8AF SERIES - MAINSTREAM AUTOMOTIVE MCUs

					Timer fur	nctions						Serial i	nterface				Supply (		Marrianana
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Data EEPROM (bytes)	Package	8-/16- bit timers	Others	ADC	DAC	I/Os	CAN	LIN- UART	USART (IrDA, ISO 7816, LIN 1.3, LIN 2.0)	SPI	I <sup>2</sup> C	IRTx	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Maximum operating temperature range (°C)
								STM8AF	Serie	s									
						STM	8AF52 CAN	and LIN I	ine - l	Jp to 2	24 MHz	CPU							
STM8AF5268	32	6	1024	LQFP48	1x8-bit / 3x16-bit		10x10-bit		38	1	1	1	1	1		3 to 5.5	5	500	-40 to +150
STM8AF5269	32	6	1024	LQFP64	1x8-bit / 3x16-bit		16x10-bit		54	1	1	1	1	1		3 to 5.5	5	500	-40 to +150
STM8AF5286	64	6	2048	VFQFPN32	1x8-bit / 3x16-bit		7x10-bit		25	1	1			1		3 to 5.5	5	500	-40 to +150
STM8AF5288	64	6	2048	LQFP48	1x8-bit / 3x16-bit		10x10-bit		38	1	1	1	1	1		3 to 5.5	5	500	-40 to +150
STM8AF5289	64	6	2048	LQFP64	1x8-bit / 3x16-bit	IWDG, Beeper,	16x10-bit		54	1	1	1	1	1		3 to 5.5	5	500	-40 to +150
STM8AF528A	64	6	2048	LQFP80	1x8-bit / 3x16-bit	WWDG <sup>*</sup> AWU	16x10-bit		70	1	1	1	1	1		3 to 5.5	5	500	-40 to +150
STM8AF52A6	128	6	2048	VFQFPN32	1x8-bit / 3x16-bit		7x10-bit		25	1	1			1		3 to 5.5	5	500	-40 to +150
STM8AF52A8	128	6	2048	LQFP48	1x8-bit / 3x16-bit		10x10-bit		38	1	1	1	1	1		3 to 5.5	5	500	-40 to +150
STM8AF52A9	128	6	2048	LQFP64	1x8-bit / 3x16-bit		16x10-bit		54	1	1	1	1	1		3 to 5.5	5	500	-40 to +150
STM8AF52AA	128	6	2048	LQFP80	1x8-bit / 3x16-bit		16x10-bit		70	1	1	1	1	1		3 to 5.5	5	500	-40 to +150
							STM8AF62	LIN line -	Up to	24 M	Hz CPU								
STM8AF6213	4	1	640	TSS0P20	1x8-bit / 1x16-bit		5x10-bit		16		1		1	1		3 to 5.5	5	230	-40 to +150
STM8AF6223	8	1	640	TSS0P20	1x8-bit / 1x16-bit	IWDG.	5x10-bit		16		1		1	1		3 to 5.5	5	230	-40 to +150
STM8AF6223A	8	1	640	TSS0P20	1x8-bit / 2x16-bit	Beeper, WWDG	7x10-bit		16		1		1	1		3 to 5.5	5	230	-40 to +150
STM8AF6226	8	1	640	LQFP32	1x8-bit / 3x16-bit	AWU	7x10-bit		28		1		1	1		3 to 5.5	5	230	-40 to +150
STM8AF6246	16	2	512	LQFP32 VFQFPN32	1x8-bit / 3x16-bit		7x10-bit		25		1		1	1		3 to 5.5	5	430	-40 to +150

## STM8AF SERIES - MAINSTREAM AUTOMOTIVE MCUs

					Timer fur	ections						Serial	interface				Supply (Ic		
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Data EEPROM (bytes)	Package	8-/16- bit timers	Others	ADC	DAC	I/Os	CAN	LIN- UART	USART (IrDA, ISO 7816, LIN 1.3, LIN 2.0)	SPI	l <sup>2</sup> C	IRTx	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Maximum operating temperature range (°C)
STM8AF6248	16	2	512	LQFP48	1x8-bit / 3x16-bit		10x10-bit		38		1		1	1		3 to 5.5	5	430	-40 to +150
STM8AF6266	32	2	1024	LQFP32 VFQFPN32	1x8-bit / 3x16-bit		7x10-bit		25		1		1	1		3 to 5.5	5	430	-40 to +150
STM8AF6268	32	2	1024	LQFP48	1x8-bit / 3x16-bit		10x10-bit		38		1		1	1		3 to 5.5	5	430	-40 to +150
STM8AF6269	32	6	1024	LQFP64	1x8-bit / 3x16-bit		16x10-bit		54		1	1	1	1		3 to 5.5	5	500	-40 to +150
STM8AF6286	64	6	2048	LQFP32	1x8-bit / 3x16-bit		7x10-bit		25		1		1	1		3 to 5.5	5	500	-40 to +150
STM8AF6288	64	6	2048	LQFP48	1x8-bit / 3x16-bit	IWDG, Beeper,	10x10-bit		38		1	1	1	1		3 to 5.5	5	500	-40 to +150
STM8AF6289	64	6	2048	LQFP64	1x8-bit / 3x16-bit	WWDG AWU	16x10-bit		54		1	1	1	1		3 to 5.5	5	500	-40 to +150
STM8AF628A	64	6	2048	LQFP80	1x8-bit / 3x16-bit		16x10-bit		70		1	1	1	1		3 to 5.5	5	500	-40 to +150
STM8AF62A6	128	6	2048	LQFP32	1x8-bit / 3x16-bit		7x10-bit		25		1		1	1		3 to 5.5	5	500	-40 to +150
STM8AF62A8	128	6	2048	LQFP48	1x8-bit / 3x16-bit		10x10-bit		38		1	1	1	1		3 to 5.5	5	500	-40 to +150
STM8AF62A9	128	6	2048	LQFP64	1x8-bit / 3x16-bit		10x10-bit		54		1	1	1	1		3 to 5.5	5	500	-40 to +150
STM8AF62AA	128	6	2048	LQFP80	1x8-bit / 3x16-bit		16x10-bit		70		1	1	1	1		3 to 5.5	5	500	-40 to +150

## STM8AL SERIES – ULTRA-LOW-POWER AUTOMOTIVE MCUs

					Timer fur	nctions						Serial i	nterface				Supply (Ic		
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Data EEPROM (bytes)	Package	8-/16- bit timers	Others	ADC	DAC	I/Os	CAN	LIN- UART	USART (IrDA, ISO 7816, LIN 1.3, LIN 2.0)	SPI	I <sup>2</sup> C	IRTx	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	Maximum operating temperature range (°C)
								STM8AL	Serie	S									
						STM8	AL31 Stand	lard low-p	ower	line -	16 MHz	z CPU							
STM8AL3136	8	2	256	LQFP32	1x8-bit / 3x16-bit		22x12-bit	1x12-bit	30			1	1	1		1.8 to 3.6	0.4	195	-40 to +125
STM8AL3138	8	2	1024	LQFP48	1x8-bit / 3x16-bit		25x12-bit	1x12-bit	41			1	1	1		1.8 to 3.6	0.4	195	-40 to +125
STM8AL3146	16	2	1024	LQFP32	1x8-bit / 3x16-bit		22x12-bit	1x12-bit	30			1	1	1		1.8 to 3.6	0.4	195	-40 to +125
STM8AL3148	16	2	1024	LQFP48	1x8-bit / 3x16-bit		25x12-bit	1x12-bit	41			1	1	1		1.8 to 3.6	0.4	195	-40 to +125
STM8AL3166	32	2	1024	LQFP32	1x8-bit / 3x16-bit	IWDG.	22x12-bit	1x12-bit	30			1	1	1		1.8 to 3.6	0.4	195	-40 to +125
STM8AL3168	32	2	1024	LQFP48	1x8-bit / 3x16-bit	WWDG, AWU.	25x12-bit	1x12-bit	41			1	1	1		1.8 to 3.6	0.4	195	-40 to +125
STM8AL31E88 <sup>3</sup>	64	4	2000	LQFP48	1x8-bit / 4x16-bit	RTC, beeper	25x12-bit	2x12-bit	41			3	1	1		1.8 to 3.6	0.4	200	-40 to +125
STM8AL3188	64	4	2000	LQFP48	1x8-bit / 4x16-bit	Боорог	25x12-bit	2x12-bit	41			3	1	1		1.8 to 3.6	0.4	200	-40 to +125
STM8AL31E89 <sup>3</sup>	64	4	2000	LQFP64	1x8-bit / 4x16-bit		28x12-bit	2x12-bit	54			3	1	1		1.8 to 3.6	0.4	200	-40 to +125
STM8AL3189	64	4	2000	LQFP64	1x8-bit / 4x16-bit		28x12-bit	2x12-bit	54			3	1	1		1.8 to 3.6	0.4	200	-40 to +125
STM8AL31E8A <sup>3</sup>	64	4	2000	LQFP80	1x8-bit / 4x16-bit		28x12-bit	2x12-bit	68			3	1	1		1.8 to 3.6	0.4	200	-40 to +125
STM8AL318A	64	4	2000	LQFP80	1x8-bit / 4x16-bit		28x12-bit	2x12-bit	68			3	1	1		1.8 to 3.6	0.4	200	-40 to +125
						L3L LCD	(4x17) or (	4x28)* Stai	ndard	low-	power li	ine - 16 MH	z CPU						
STM8AL3L46	16	2	1024	LQFP32	1x8-bit / 3x16-bit	IWDG, WWDG.	21x12-bit	1x12-bit	29			1	1	1		1.8 to 3.6	0.4	195	-40 to +125
STM8AL3L48	16	2	1024	LQFP48	1x8-bit / 3x16-bit	AWU, RTC.	25x12-bit	1x12-bit	41			1	1	1		1.8 to 3.6	0.4	195	-40 to +125
STM8AL3L66	32	2	1024	LQFP32	1x8-bit / 3x16-bit	beeper	21x12-bit	1x12-bit	29			1	1	1		1.8 to 3.6	0.4	195	-40 to +125

#### STM8AL SERIES - ULTRA-LOW-POWER AUTOMOTIVE MCUs

Part number		Internal RAM size (Kbytes)			Timer functions							Serial i	nterface			Supply current (lcc)		- Maximum	
	Flash size (Kbytes)		Data EEPROM (bytes)	Package	8-/16- bit timers	Others	ADC	DAC	I/Os	CAN	LIN- UART	USART (IrDA, ISO 7816, LIN 1.3, LIN 2.0)	SPI	l <sup>2</sup> C	IRTx	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	operating temperature range (°C)
STM8AL3L68	32	2	1024	LQFP48	1x8-bit / 3x16-bit		25x12-bit	1x12-bit	41			1	1	1		1.8 to 3.6	0.4	195	-40 to +125
STM8AL3LE883	64	2	2000	LQFP48	1x8-bit / 3x16-bit		25x12-bit	2x12-bit	41			3	1	1		1.8 to 3.6	0.4	200	-40 to +125
STM8AL3L88	64	2	2000	LQFP48	1x8-bit / 3x16-bit	IWDG,	25x12-bit	2x12-bit	41			3	1	1		1.8 to 3.6	0.4	200	-40 to +125
STM8AL3LE89 <sup>3</sup>	64	4	2000	LQFP64	1x8-bit / 4x16-bit	WWDG, AWU,	28x12-bit	2x12-bit	54			3	1	1		1.8 to 3.6	0.4	200	-40 to +125
STM8AL3L89	64	4	2000	LQFP64	1x8-bit / 4x16-bit		28x12-bit	2x12-bit	54			3	1	1		1.8 to 3.6	0.4	200	-40 to +125
STM8AL3LE8A <sup>3</sup>	64	4	2000	LQFP80	1x8-bit / 4x16-bit		28x12-bit	2x12-bit	68			3	1	1		1.8 to 3.6	0.4	200	-40 to +125
STM8AL3L8A	64	4	2000	LQFP80	1x8-bit / 4x16-bit		28x12-bit	2x12-bit	68			3	1	1		1.8 to 3.6	0.4	200	-40 to +125

Note

All STM8AL part numbers have DMA with 4 channels

(\*) For all STM8AL3Lx6 LCD is( 4x17) and for all STM8AL3Lx8 LCD (4x28)

(3) 128-bit AES

## STM8L SERIES - ULTRA-LOW-POWER MCUs

					Timer fu	nctions	ADC DAC				Serial	interface			Supply current (lcc)		Maximum	
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Data EEPROM (bytes)	Package	8-/16-bit timers	Others		DAC	I/0s	SPI	l <sup>2</sup> C	USART (IrDA, ISO 7816)	IRTx	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	operating temperature range (°C)	Display controller (LCD)
	STM8L051/052 Value line - 16 MHz CPU																	
STM8L051F3	8	1	256	TSS0P20	1x8-bit / 2x16-bit	2 x WDG.	10x12-bit		18	1	1	1		1.8 to 3.6	0.35	180	-40 to +85	
STM8L052C6	32	2	256	LQFP48	1x8-bit / 3x16-bit	RTC, Beeper	25x12-bit		41	1	1	1		1.8 to 3.6	0.35	180	-40 to +85	4x28
STM8L052R8	64	4	256	LQFP64	1x8-bit / 4x16-bit	Deehei	27x12-bit		54	2	1	3		1.8 to 3.6	0.4	200	-40 to +85	4x28/8x24
STM8L101 entry line - 16 MHz CPU																		
STM8L101F1	2	1.5		UFQFPN20	1x8-bit / 2x16-bit				18	1	1	1	1	1.65 to 3.6	0.3	150	-40 to +85	
STM8L101F2	4	1.5		TSS0P20 UFQFPN20	1x8-bit / 2x16-bit				18	1	1	1	1	1.65 to 3.6	0.3	150	-40 to +125	
STM8L101G2	4	1.5		UFQFPN28	1x8-bit / 2x16-bit	IWDG,			26	1	1	1	1	1.65 to 3.6	0.3	150	-40 to +85	
STM8L101F3	8	1.5	1	TSSOP20 UFQFPN20	1x8-bit / 2x16-bit	AWU, Beeper			18	1	1	1	1	1.65 to 3.6	0.3	150	-40 to +125	
STM8L101G3	8	1.5	1	UFQFPN28	1x8-bit / 2x16-bit				26	1	1	1	1	1.65 to 3.6	0.3	150	-40 to +125	
STM8L101K3	8	1.5	1	LQFP32 UFQFPN32	1x8-bit / 2x16-bit				30	1	1	1	1	1.65 to 3.6	0.3	150	-40 to +125	
							STM8L15	1/152 - 1	6 MHz	z CPU								
STM8L151C2	4	1	256	LQFP48	1x8-bit / 2x16-bit		28x12-bit		41	1	1	1		1.65 to 3.6	0.35	180	-40 to +125	
STM8L151F2	4	1	256	TSSOP20 UFQFPN20	1x8-bit / 2x16-bit	0 14/00	10x12-bit		18	1	1	1		1.65 to 3.6	0.35	180	-40 to +125	
STM8L151G2	4	1	256	UFQFPN28	1x8-bit / 2x16-bit	2 x WDG, AWU, RTC, Beeper	18x12-bit		26	1	1	1		1.65 to 3.6	0.35	180	-40 to +125	
STM8L151K2	4	1	256	UFQFPN32	1x8-bit / 2x16-bit		23x12-bit		30	1	1	1		1.65 to 3.6	0.35	180	-40 to +125	
STM8L151C3	8	1	256	LQFP 48	1x8-bit / 2x16-bit		28x12-bit		41	1	1	1		1.65 to 3.6	0.35	180	-40 to +125	

## STM8L SERIES - ULTRA-LOW-POWER MCUs

					Timer functions						Serial	interface			Supply (Ic		Maximum	
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Data EEPROM (bytes)	Package	8-/16-bit timers	Others	ADC	DAC	I/Os	SPI	I <sup>2</sup> C	USART (IrDA, ISO 7816)	IRTx	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	operating temperature range (°C)	Display controller (LCD)
STM8L151F3	8	1	256	TSS0P20 UFQFPN20	1x8-bit / 2x16-bit		10x12-bit		18	1	1	1		1.65 to 3.6	0.35	180	-40 to +125	
STM8L151G3	8	1	256	UFQFPN28	1x8-bit / 2x16-bit		18x12-bit		26	1	1	1		1.65 to 3.6	0.35	180	-40 to +125	
STM8L151K3	8	1	256	UFQFPN32	1x8-bit / 2x16-bit		23x12-bit		30	1	1	1		1.65 to 3.6	0.35	180	-40 to +125	
STM8L151C4	16	2	1024	LQFP48 UFQFPN48	1x8-bit / 3x16-bit		25x12-bit	1x12-bit	41	1	1	1		1.65 to 3.6	0.35	180	-40 to +125	
STM8L151G4	16	2	1024	UFQFPN28 WLCSP28	1x8-bit / 3x16-bit		18x12-bit	1x12-bit	26	1	1	1		1.65 to 3.6	0.35	180	-40 to +125	
STM8L151K4	16	2	1024	LQFP32 UFQFPN32	1x8-bit / 3x16-bit		22x12-bit	1x12-bit	30	1	1	1		1.65 to 3.6	0.35	180	-40 to +125	
STM8L152C4	16	2	1024	LQFP48 UFQFPN48	1x8-bit / 3x16-bit		25x12-bit	1x12-bit	41	1	1	1		1.65 to 3.6	0.35	180	-40 to +125	4x28
STM8L152K4	16	2	1024	LQFP32 UFQFPN32	1x8-bit / 3x16-bit	2 x WDG.	21x12-bit	1x12-bit	29	1	1	1		1.65 to 3.6	0.35	180	-40 to +125	4x17
STM8L151C6	32	2	1024	LQFP48 UFQFPN48	1x8-bit / 3x16-bit	AWU, RTC, Beeper	25x12-bit	1x12-bit	41	1	1	1		1.65 to 3.6	0.35	180	-40 to +125	
STM8L151G6	32	2	1024	UFQFPN28 WLCSP28	1x8-bit / 3x16-bit	Dechei	18x12-bit	1x12-bit	26	1	1	1		1.65 to 3.6	0.35	180	-40 to +125	
STM8L151K6	32	2	1024	LQFP32 UFQFPN32	1x8-bit / 3x16-bit		22x12-bit	1x12-bit	30	1	1	1		1.65 to 3.6	0.35	180	-40 to +125	
STM8L151R6	32	2	1024	LQFP64	1x8-bit / 4x16-bit		28x12-bit	2x12-bit	54	2	1	3		1.65 to 3.6	0.4	200	-40 to +125	
STM8L152C6	32	2	1024	LQFP48 UFQFPN48	1x8-bit / 3x16-bit		25x12-bit	1x12-bit	41	1	1	1		1.65 to 3.6	0.35	180	-40 to +125	4x28
STM8L152K6	32	2	1024	LQFP32 UFQFPN32	1x8-bit / 3x16-bit		21x12-bit	1x12-bit	29	1	1	1		1.65 to 3.6	0.35	180	-40 to +125	4x17
STM8L152R6	32	2	1024	LQFP64	1x8-bit / 4x16-bit		28x12-bit	2x12-bit	54	2	1	3		1.65 to 3.6	0.4	200	-40 to +125	4x40/8x36
STM8L152K8	64	4	2048	WLCSP32	1x8-bit / 4x16-bit		28x12-bit	2x12-bit	29	2	1	3		1.65 to 3.6	0.4	200	-40 to +85	4x17
STM8L151C8	64	4	2048	LQFP48 UFQFPN48	1x8-bit / 4x16-bit		25x12-bit	2x12-bit	41	2	1	3		1.65 to 3.6	0.4	200	-40 to +125	

## STM8L SERIES - ULTRA-LOW-POWER MCUs

		Internal RAM size (Kbytes)	Data EEPROM (bytes)	Package	Timer functions					Serial interface					Supply current (lcc)		Maximum	
Part number	Flash size (Kbytes)				8-/16-bit timers	Others	ADC	DAC	I/Os	SPI	I <sup>2</sup> C	USART (IrDA, ISO 7816)	IRTx	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)	operating temperature range (°C)	Display controller (LCD)
STM8L151M8	64	4	2048	LQFP80	1x8-bit / 4x16-bit	2 x WDG, AWU, RTC,	28x12-bit	2x12-bit	68	2	1	3		1.65 to 3.6	0.4	200	-40 to +125	
STM8L151R8	64	4	2048	LQFP64	1x8-bit / 4x16-bit		28x12-bit	2x12-bit	54	2	1	3		1.65 to 3.6	0.4	200	-40 to +125	
STM8L152C8	64	4	2048	LQFP48 UFQFPN48	1x8-bit / 4x16-bit		25x12-bit	2x12-bit	41	2	1	3		1.65 to 3.6	0.4	200	-40 to +125	4x32/8x28
STM8L152M8	64	4	2048	LQFP80	1x8-bit / 4x16-bit	Beeper	28x12-bit	2x12-bit	68	2	1	3		1.65 to 3.6	0.4	200	-40 to +125	4x44/8x40
STM8L152R8	64	4	2048	LQFP64	1x8-bit / 4x16-bit		28x12-bit	2x12-bit	54	2	1	3		1.65 to 3.6	0.4	200	-40 to +125	4x40/8x36
	STM8L162 - 16 MHz CPU																	
STM8L162M8	64	4	2048	LQFP80	1x8-bit / 4x16-bit	2 x WDG, AWU, RTC,	28x12-bit	2x12-bit	68	2	1	3		1.65 to 3.6	0.4	200	-40 to +125	4x44/8x40
STM8L162R8	64	4	2048	LQFP64	1x8-bit / 4x16-bit	Beeper	28x12-bit	2x12-bit	54	2	1	3		1.65 to 3.6	0.4	200	-40 to +125	4x40/8x36

Note: 1. 8 Kbytes Flash up to 2 Kbytes of EEPROM

# Abbreviations and packages

#### **ABBREVIATIONS**

ADC : Analog-to-digital converter LCD Liquid crystal display SPI Serial peripheral interface ART SSC Auto-reload timer LIN Local interconnect network Single-cycle switching support ATAPI SSP Synchronous serial port AT attachment packet interface LVD Low voltage detection AWU TBU Time base unit Auto wake-up from halt MAC Multiply accumulator

BLPD TLI Byte level protocol decoder MC Motor control Top level interrupt BOD Brown-out detector MFT Multifunction timer UART

CAN Controller area network MMC MultiMediaCard

CAPCOM Non-maskable interrupt Capture compare NMI CSS Clock security system OSG Oscillator safeguard

DALI Digital addressable lighting interface PCA Programmable counter array

Power-down reset DDC Data display channel PDR

Digital satellite equipment control DiSEaC PHW Programmable halt wake-up DMA PEC Peripheral event controller Direct memory access DSC Dual supply control PLD Programmable logic device

DTC Data transfer coprocessor PLL Phase locked loop FTM Embedded trace macrocell POR Power-on reset

EMI Programmable voltage detector External memory interface PVD HDLC PVR Programmable voltage regulator High-level data link control

IAP In-application programming **PWM** Pulse width modulation IC/OC Input capture/output compare ROP Readout protection ICP programming RTC Real-time clock timer

SAI Serial Audio Interface IR Infrared Infrared data association SC IrDA Smartcard

ISP In-situ programming SCI Serial communication interface

I<sup>2</sup>C Inter-integrated circuit SCR Smartcard reader I2S

Inter-IC sound **SDIO** Secure digital input output

SDMMC Secure Digital / Multi Media Card

SMI Serial memory interface

Universal asynchronous receiver

transmitter

USART Universal sync/async receiver

transmitter

USB Universal Serial Bus WDG Watchdog timer WWDG Window watchdog timer

#### **PACKAGES**

DIP : Dual in-line package LCC : Leaded chip carrier PDIP Shrink: Shrink Plastic Dual In-line

Package

: Plastic guad flat package **PQFP** 

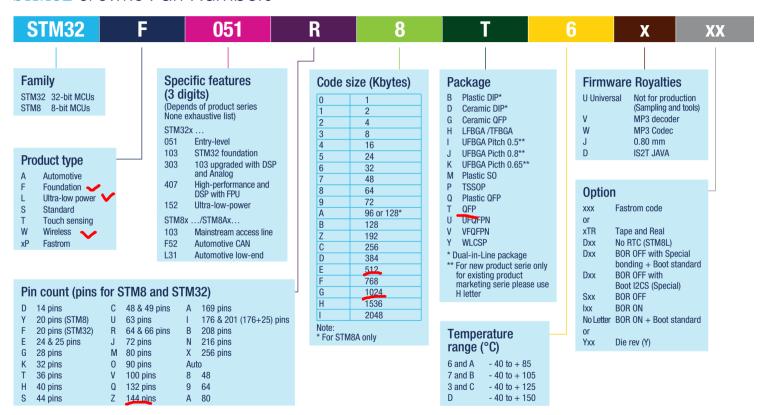
SO : Small outline

LQFP : Low-profile quad flat package

**PBGA** : Plastic ball grid array DFN : Dual flat no-lead QFN : Quad flat no-lead

**WLCSP** : Wafer-Level Chip-Scale Package

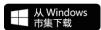
## STM32 & STM8 Part Numbers



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