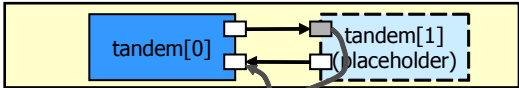
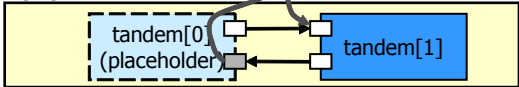


CPU0



CPU1



comm. (MPI, pipe, etc.)