

Name:_____ Box:_____

Root Page Table Ptr		Main Memory		Physical Address																																																		
0		<table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 50%;">Valid bit</th> <th style="width: 50%;">Frame Number</th> </tr> </thead> <tbody> <tr><td style="text-align: center;">1</td><td style="text-align: center;">104</td></tr> <tr><td style="text-align: center;">1</td><td style="text-align: center;">334</td></tr> <tr><td style="text-align: center;">0</td><td style="text-align: center;">45</td></tr> <tr><td style="text-align: center;">1</td><td style="text-align: center;">1</td></tr> <tr><td style="text-align: center;">1</td><td style="text-align: center;">115</td></tr> <tr><td style="text-align: center;">1</td><td style="text-align: center;">713</td></tr> <tr><td style="text-align: center;">1</td><td style="text-align: center;">2</td></tr> <tr><td style="text-align: center;">0</td><td style="text-align: center;">228</td></tr> <tr><td colspan="2" style="text-align: center;">...</td></tr> <tr><td style="text-align: center;">1</td><td style="text-align: center;">900</td></tr> <tr><td style="text-align: center;">1</td><td style="text-align: center;">1005</td></tr> <tr><td style="text-align: center;">0</td><td style="text-align: center;">820</td></tr> <tr><td style="text-align: center;">1</td><td style="text-align: center;">20</td></tr> <tr><td style="text-align: center;">0</td><td style="text-align: center;">5</td></tr> <tr><td style="text-align: center;">0</td><td style="text-align: center;">1005</td></tr> <tr><td style="text-align: center;">0</td><td style="text-align: center;">220</td></tr> <tr><td colspan="2" style="text-align: center;">...</td></tr> <tr><td style="text-align: center;">1</td><td style="text-align: center;">303</td></tr> <tr><td style="text-align: center;">1</td><td style="text-align: center;">689</td></tr> <tr><td style="text-align: center;">0</td><td style="text-align: center;">446</td></tr> <tr><td style="text-align: center;">1</td><td style="text-align: center;">848</td></tr> <tr><td style="text-align: center;">0</td><td style="text-align: center;">666</td></tr> <tr><td style="text-align: center;">1</td><td style="text-align: center;">111</td></tr> <tr><td style="text-align: center;">1</td><td style="text-align: center;">229</td></tr> </tbody> </table>	Valid bit	Frame Number	1	104	1	334	0	45	1	1	1	115	1	713	1	2	0	228	...		1	900	1	1005	0	820	1	20	0	5	0	1005	0	220	...		1	303	1	689	0	446	1	848	0	666	1	111	1	229		
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1. 2|2|105

2. 6|2|240

3. 3|3|120

(7 points) Consider a 32-bit addressing scheme with 6 bits for Index₁, 12 for Index₂, 14 for the offset and 4 GB of physical memory.

4. How many entries are there in the root page table?
5. How many entries are there in the second level page table?
6. How many total entries are there in the page table structure?
7. What is the minimum size of each page table entry?
8. What is the size of each page?
9. What are the advantages of using a two level page table?
10. What are the disadvantages of using a two level page table?

(5 points) Consider an inverted page table system with hashing. Assume that frames are 1024 Bytes.

The diagram illustrates the mapping of virtual addresses to physical addresses using a hash function and a page table.

Virtual Address: A box labeled "Virtual Address" contains "Page #" and "Offset".

Physical Address: A box labeled "Physical Address" contains "Frame #" and "Offset".

Hash Function: A box labeled "Hash Function" is shown on the left.

Page Table: A table with 4 columns: "Address", "Page #", "Frame #", "Valid bit", and "Rel. Ptr".

Address	Page #	Frame #	Valid bit	Rel. Ptr
0	104	8	1	0
...				
256	33	0	0	0
260	81	1	1	0
264	807	2	1	0
268	19	3	1	0
272	41	4	1	-3
276	26	5	0	0
280	803	6	1	-3
284	404	7	0	0

Page Table Ptr: A box labeled "Page Table Ptr" contains "256".

Register: A box labeled "Register" contains "Interrupt Indicator".

For each of the following virtual addresses, determine whether or not a page fault occurs, and if a page fault does not occur, translate the virtual address to a physical address.

Assume the hash function is $h(x) = 3 + (x \% 8)$

Note that this system uses chained hashing, with the Rel Ptr indicating the page table elements to the next entry in the chain.

11. 81|105
12. 41|640
13. 25|320
14. 28|204
15. 19|880