

RAMACHANDRAM BHAVYA



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OBJECTIVE

To work in a firm with a professional work driven environment where I can utilize and apply my knowledge, skills which would enable me as fast graduate to grow while fulfilling organizational goals.

SPECIALISATION

(VLSI)

Degree/ Examination	Year of Passing	School/Institute	CGPA
B.Tech	2022	KL University Vijayawada	9.12 (Up to 7 th sem)
Intermediate	2018	Narayana junior College, Ananthapur	9.43
SSC	2016	Lakshmi School	9.8

KEY SKILLS

Honesty
Active listening
Team spirit
Accepting challenges

PROJECT WORK / INTERNSHIP/TRAINING

Projects

- Error correction and detection techniques implementation in vhdl and verilog programming, parallel in serial out implementation in dsch and microwind, Flying adder frequency synthesizer implementation in vhdl.
- Line following robot, Bluetooth controlled car, Wifi controlled car.

AREAS OF INTEREST

- On vlsi softwares
- C programming

Internships

- VI Solutions
- Bangalore
- 5 weeks
- I acquired enough knowledge on lab view

SOFTWARE SKILL SET

Packages

• LANGUAGES : C,PYTHON,VERILOG,VHDL

• Operating System : Windows

ACADEMIC ACHIEVEMENTS &CO-CURRICULAR ACTIVITIES

: M.SOffice

- Completed certification course in TESSOLVE.
- Completed specialization on "Ibm data science" in coursera.
- Completed "Machine learning for all" course in coursera.
- Completed "Embedded software and hardware architecture "course in coursera.
- Completed "VLSI cad part-I" and "VLSI cad part-II" courses in coursera

EXTRA CURRICULAR ACTIVITIES

- Participated in many speech competitions
- Participated in hospitality for college events.
- Worked in hobby clubs.

PERSONAL DETAILS

• FATHER'S NAME : C.S.Ramanjineyulu

• DATE OF BIRTH : 04/07/2001

• LINGUISTIC PROFICIENCY: Telugu & English

• ADDRESS : D.NO:17 -691, Neeruganti street, Oldtown, Ananthapur, 515001.

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I hereby declare that the information furnished above is true to the best of my knowledge.

Name & Signature: R. Bhavya