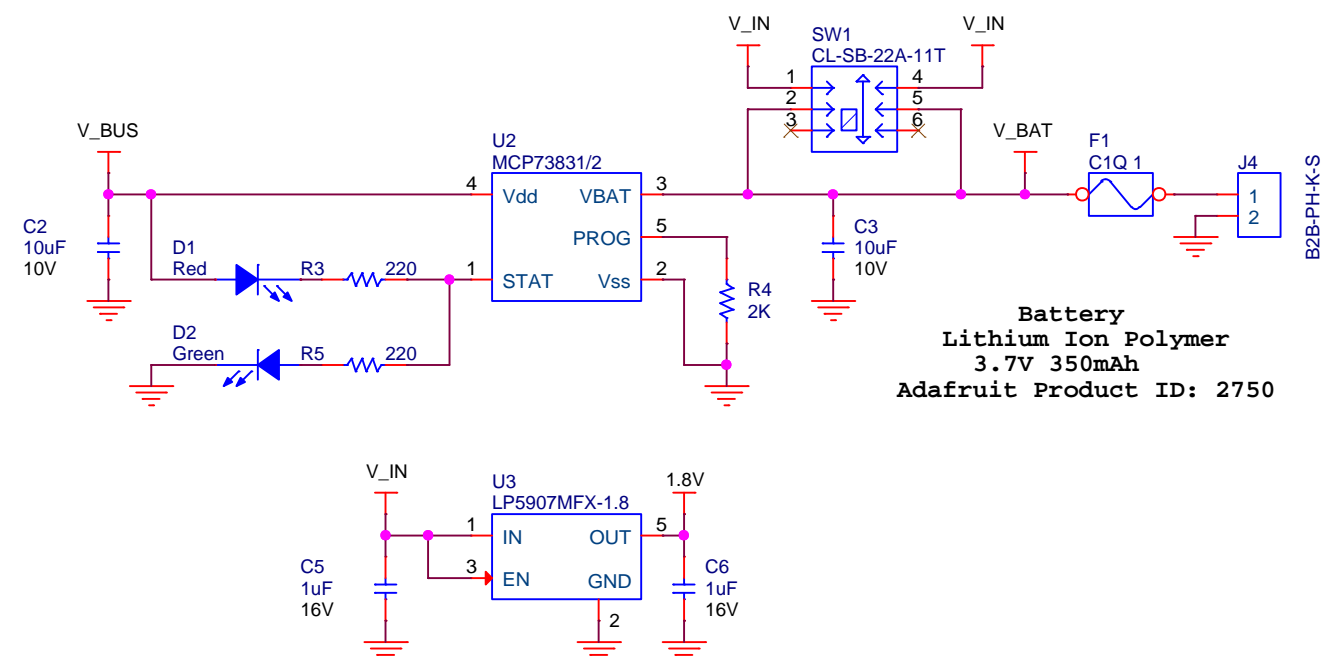
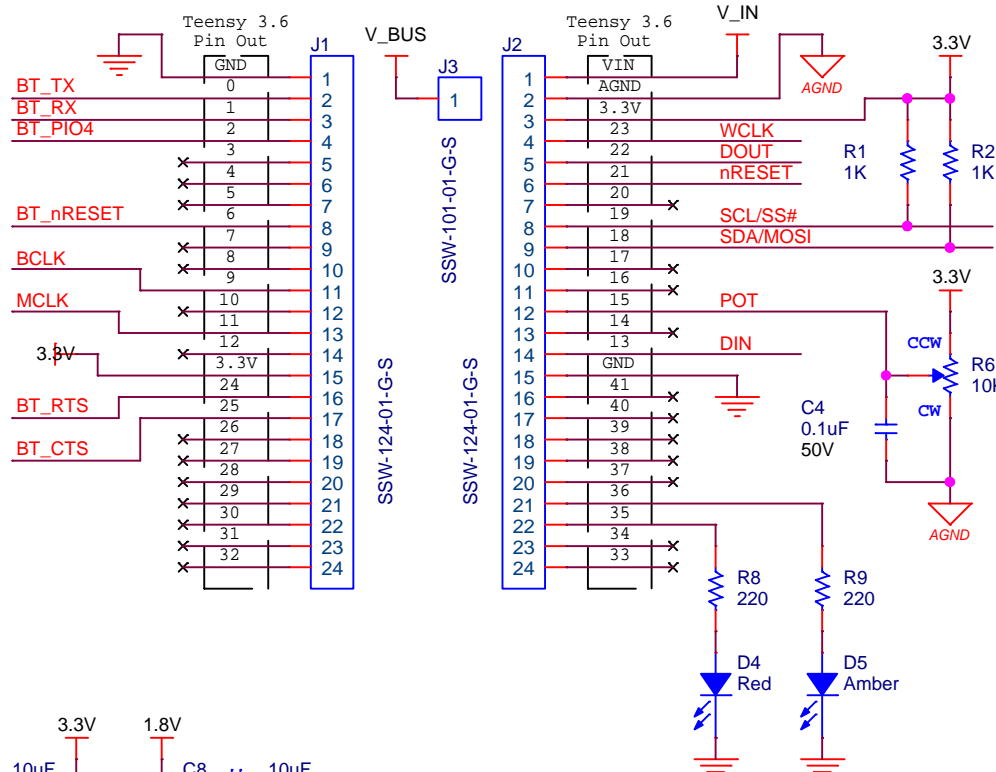
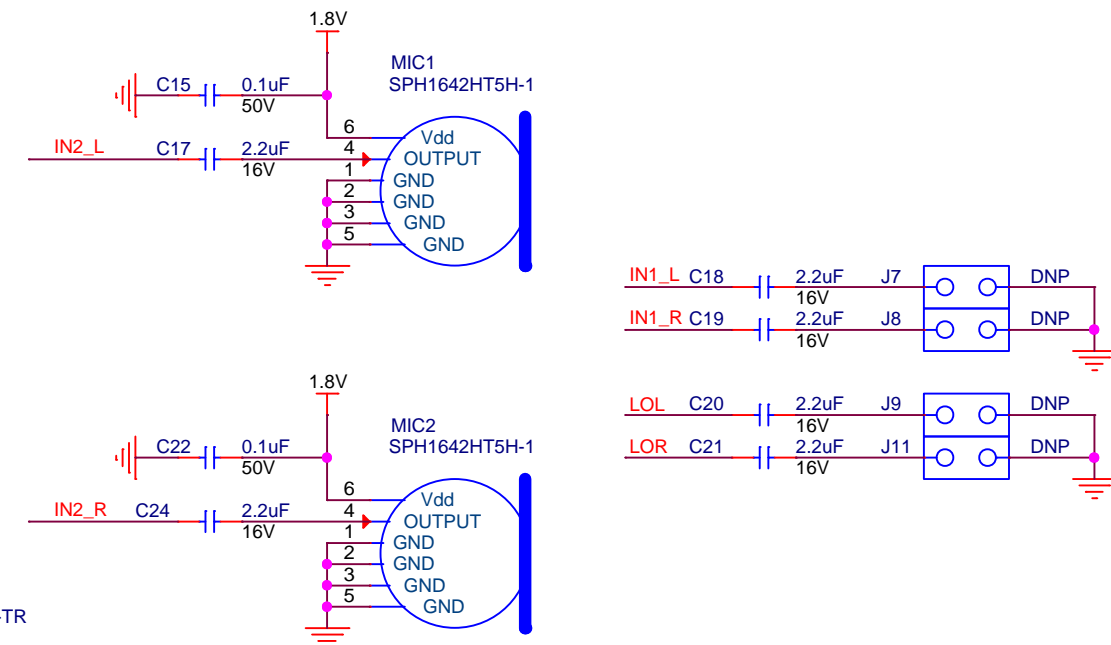


The schematic diagram shows the connection between a BT module and an RN42 module (U1). The BT module's pins are connected to the RN42 module's pins as follows:


- BT_RX (pin 13) is connected to RN42 UART_RX (pin 13).
- BT_TX (pin 14) is connected to RN42 UART_TX (pin 14).
- BT_RTS (pin 15) is connected to RN42 UART_RTS (pin 15).
- BT_CTS (pin 16) is connected to RN42 UART_CTS (pin 16).
- BT_nRESET (pin 5) is connected to RN42 Reset (pin 5).
- BT_PIO4 (pin 19) is connected to RN42 GPIO2 (pin 19).
- TP1 (pin 21) is connected to RN42 GPIO4 (pin 21).
- D3 (Blue LED) is connected to RN42 GPIO3 (pin 20) through a resistor R7 (220 ohms).

The RN42 module is powered by Vdd (pin 11) and GND (pins 12, 28, 29). The module also has SPI pins (SPI_CSB, SPI_CLK, SPI_MISO, SPI_MOSI) and USB pins (USB_D+, USB_D-).

[illegible]

Note: L return and R speaker return should be tied at one point to GND near pin 1 of J10.

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DRAWN BY BMP/TGB		ECO <ECO>	
DATE Tuesday, January 03, 2017		SHEET 1 OF 1	

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