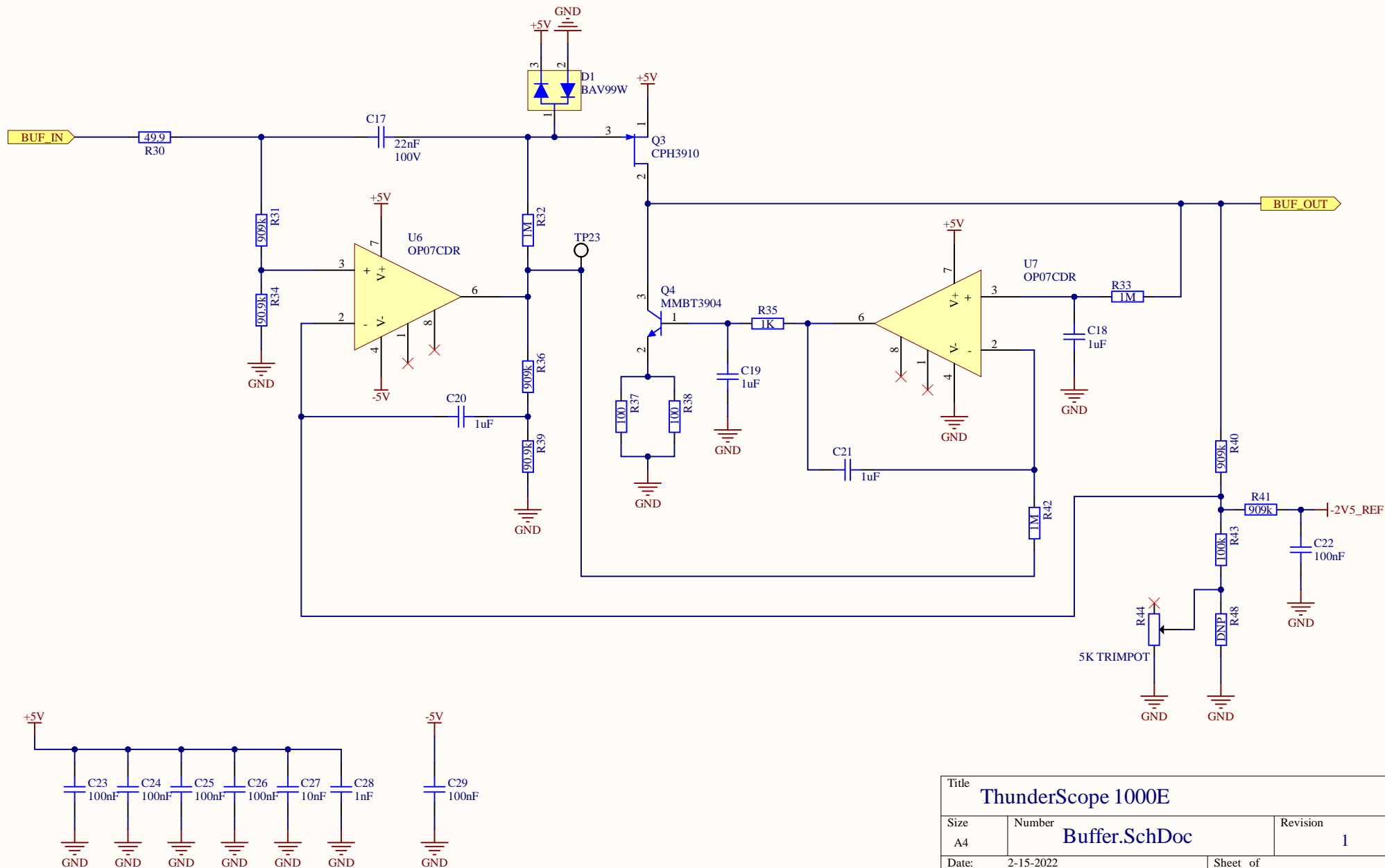
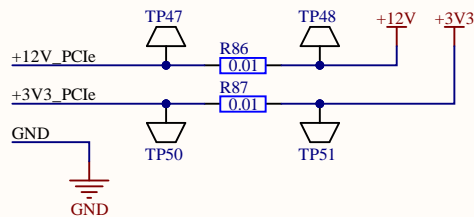
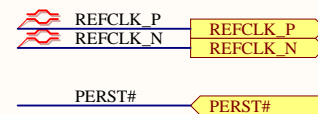
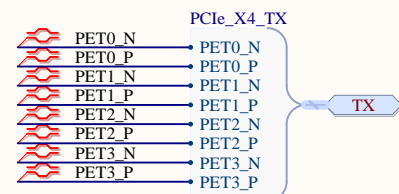
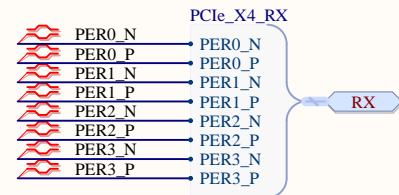
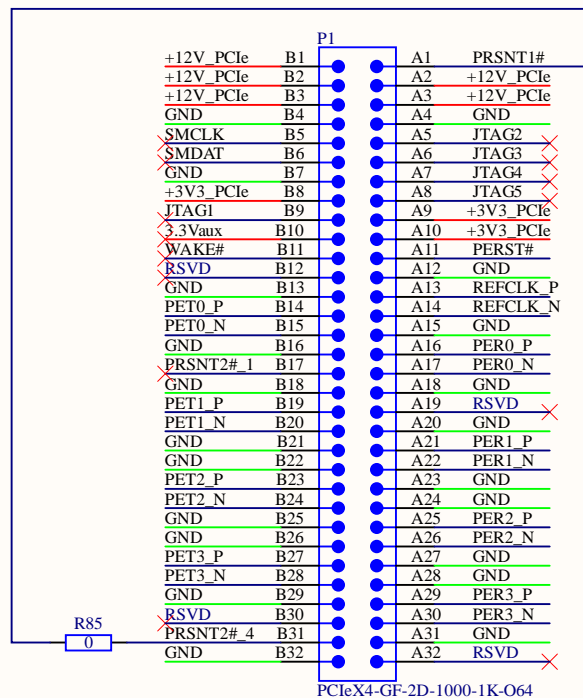


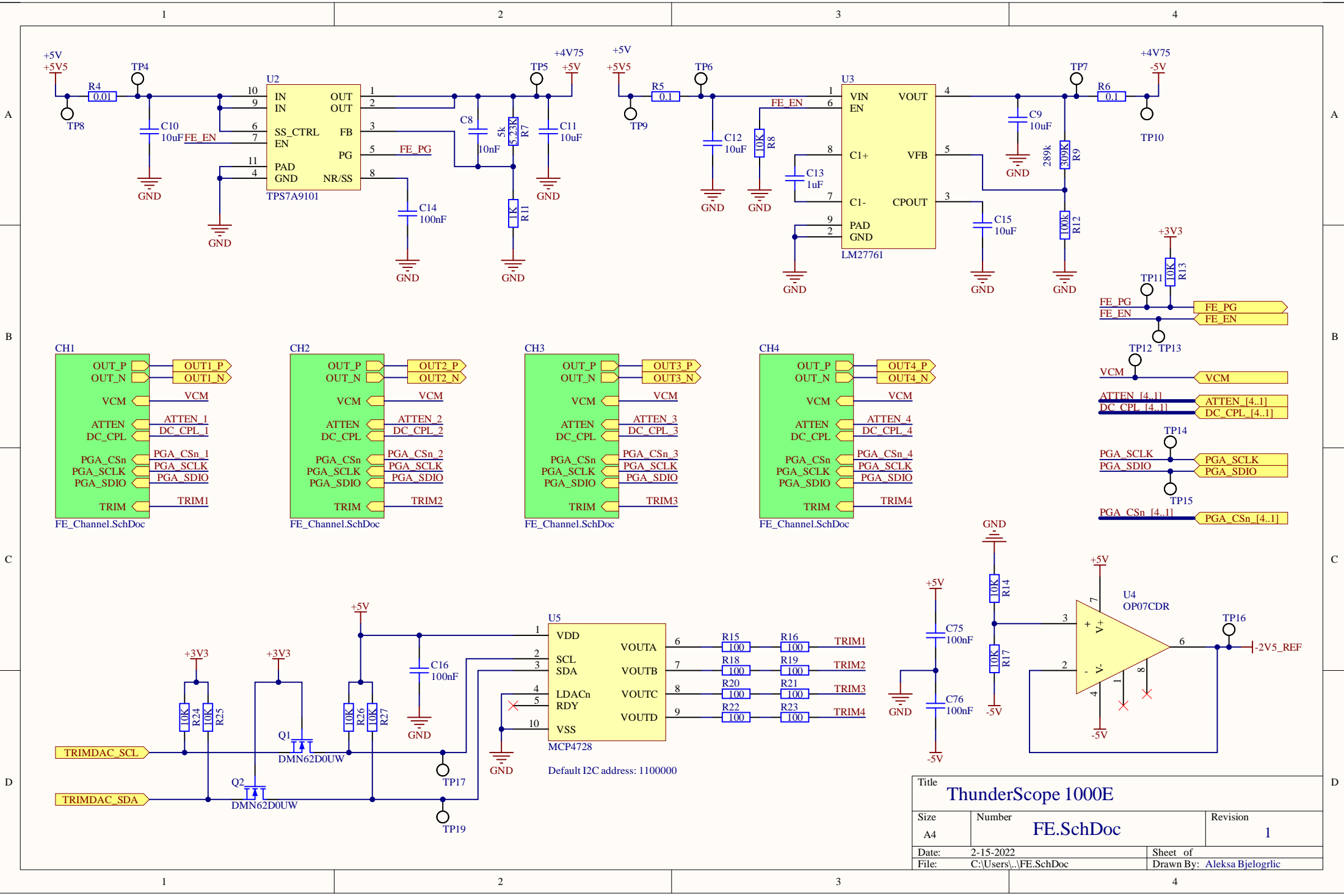
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ThunderScope 1000E		
Size	Number	Revision
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Date:	2-15-2022	Sheet of
File:	C:\Users\...\ADC.SchDoc	Drawn By: Aleksa Bjelogrić

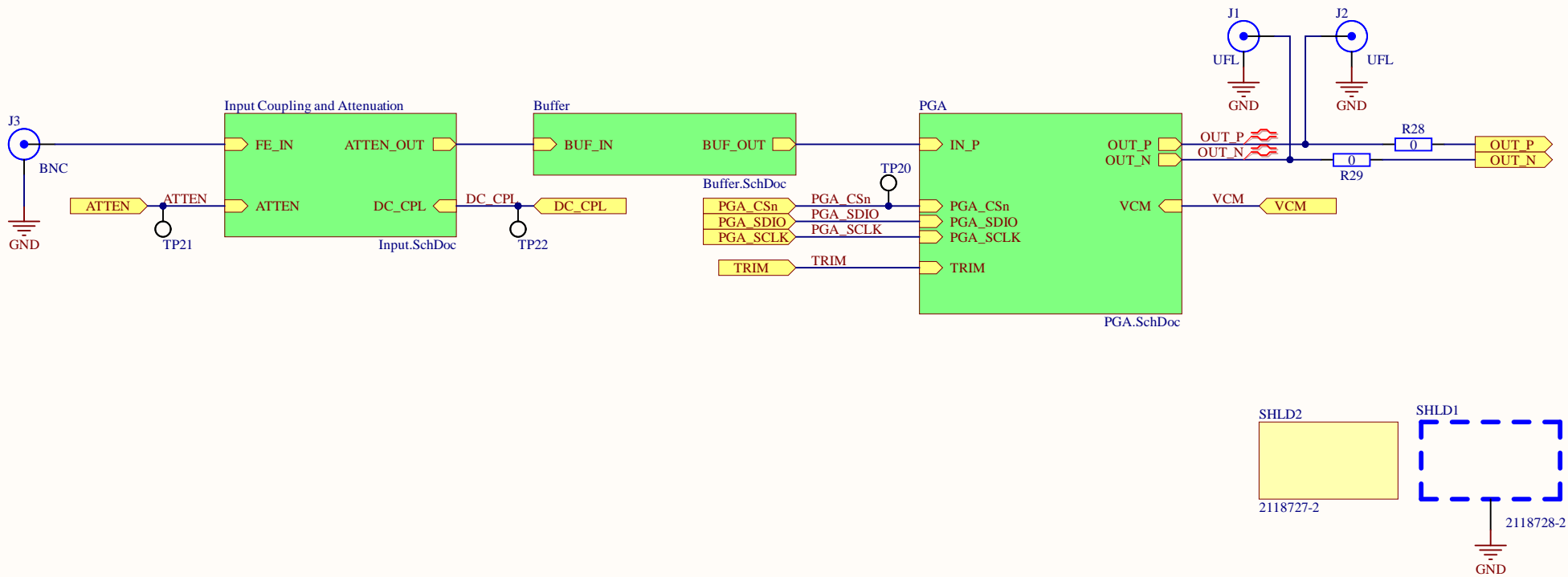


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Date: 2-15-2022	Sheet of	
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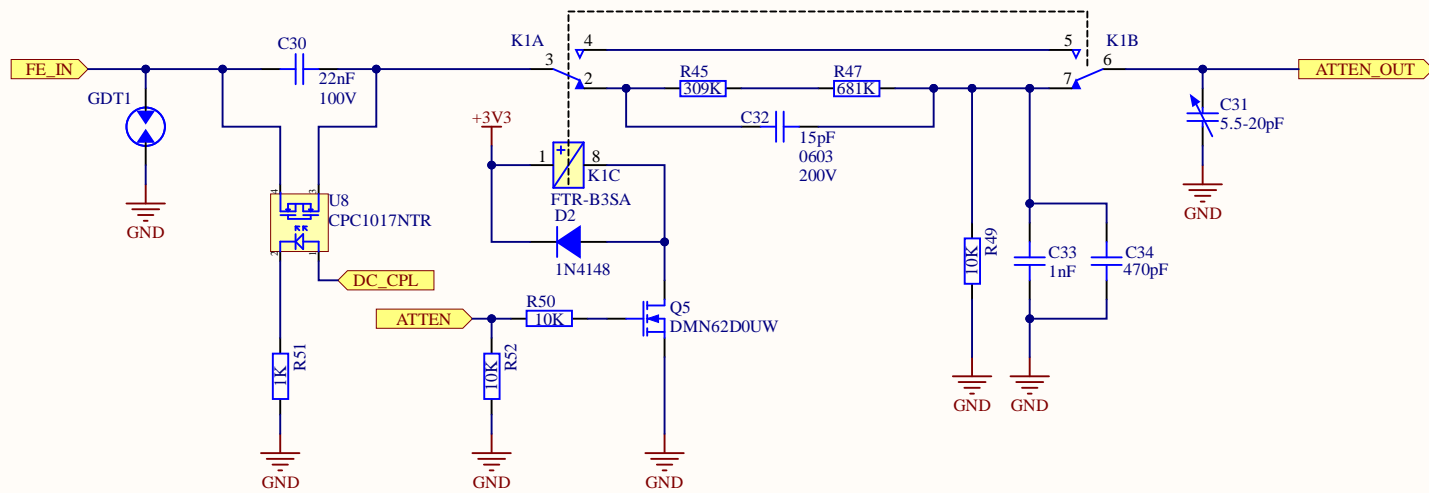
- Nominal values used, dimensions in mm
- The mounting holes and keep-out areas around them are only required when the I/O bracket is mounted on the card directly
- Component height rule and clearance rule derived from PCI_Express_CEM_r2.0.pdf, Page 84.
- Stackup is not specified in PCI_Express_CEM_r2.0.pdf, nor implemented in this template.



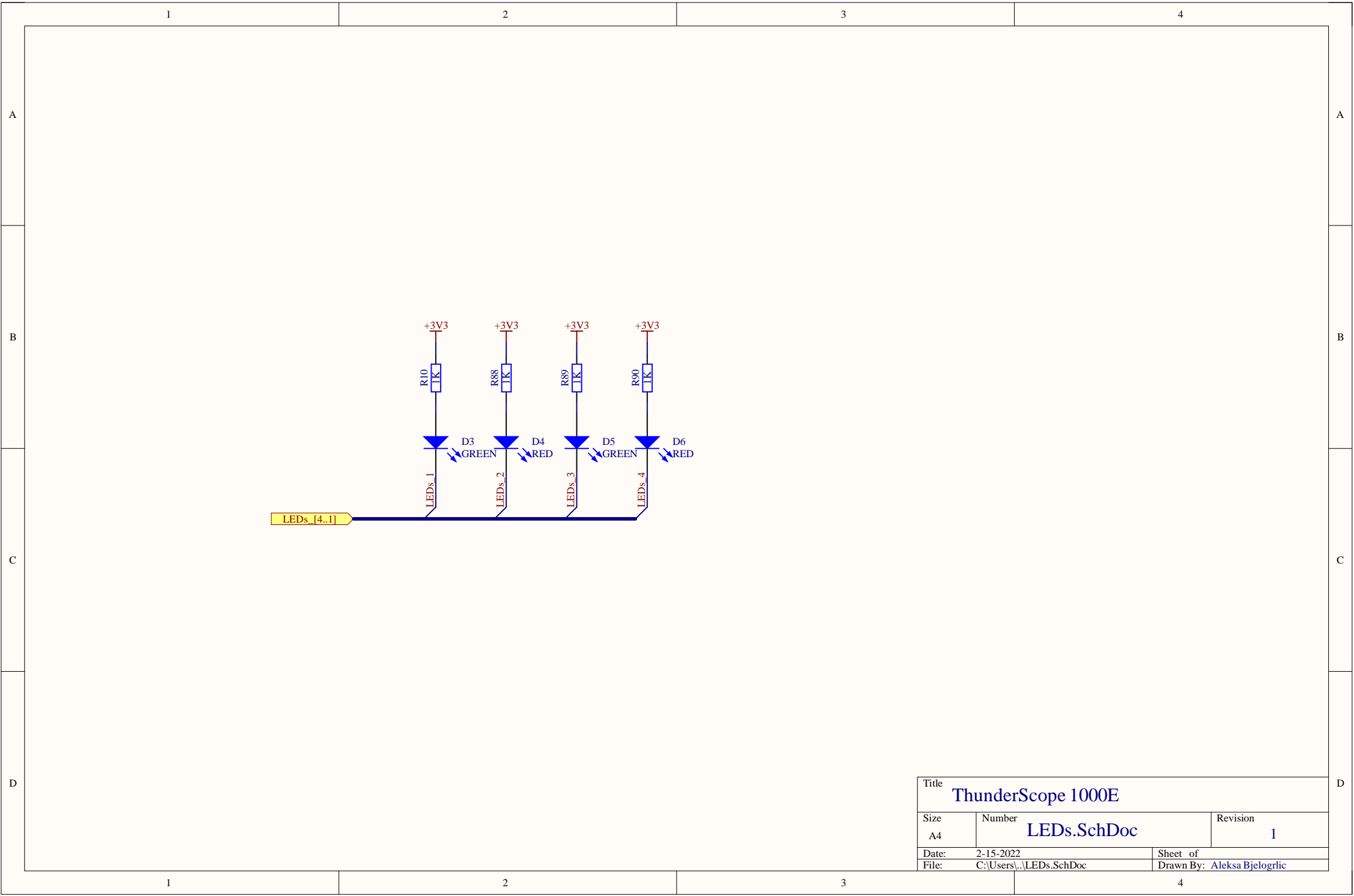




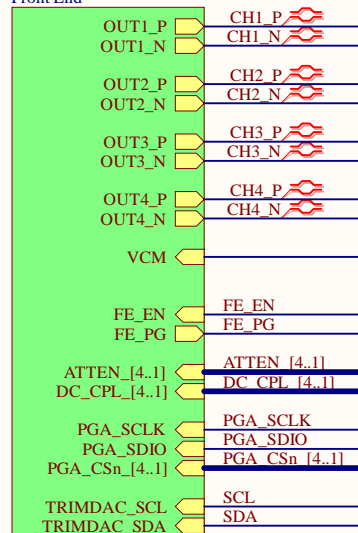
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Size	Number	Revision
A4	FE_Channel.SchDoc	1
Date:	2-15-2022	Sheet of
File:	C:\Users\...\FE_Channel.SchDoc	Drawn By: Aleksa Bjelogrić



Title ThunderScope 1000E		
Size A4	Number Input.SchDoc	Revision 1
Date: 2-15-2022	Sheet of	
File: C:\Users\...\Input.SchDoc	Drawn By: Aleksa Bjelogrić	

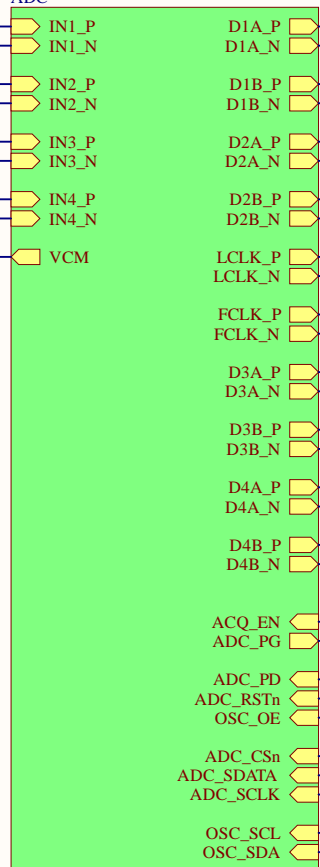


Front End



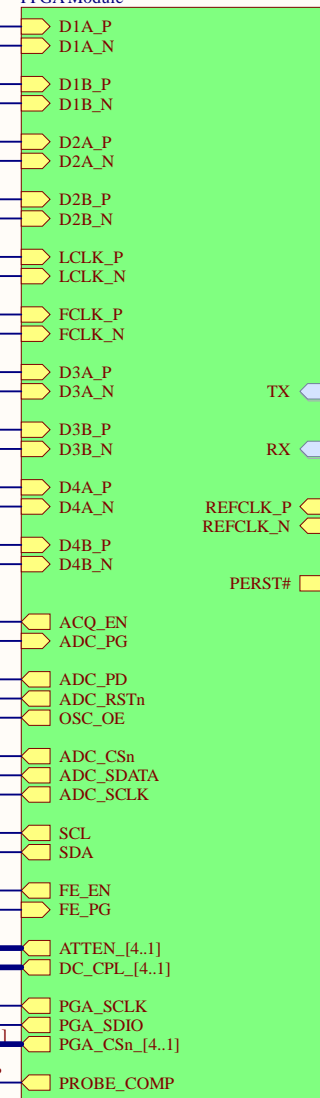
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ADC



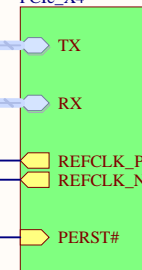
ADC.SchDoc

FPGA Module



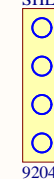
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PCIe_X4

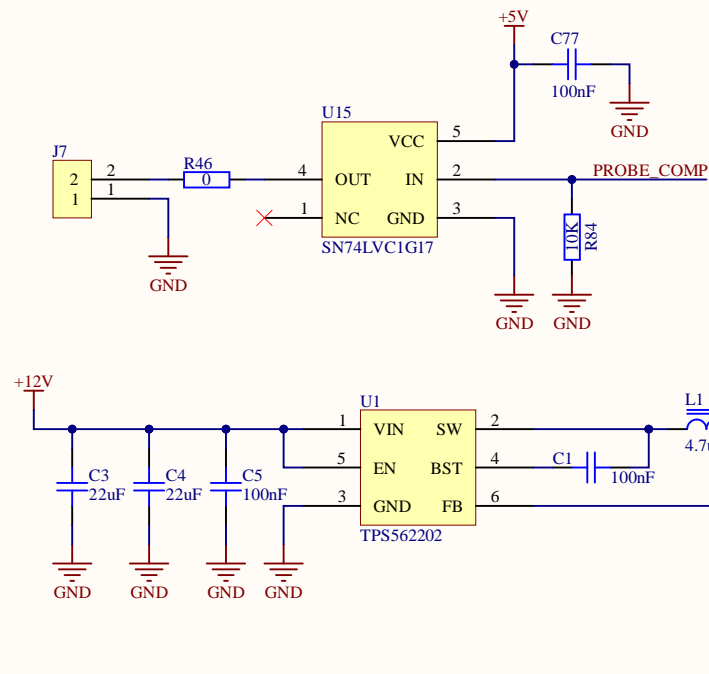


CON_PCIe_X4.SchDoc

SHLD3



9204



Title		
ThunderScope 1000E		
Size	Number	Revision
A4	Main.SchDoc	1
Date:	2-15-2022	Sheet of
File:	C:\Users\...\Main.SchDoc	Drawn By: Aleksa Bjelogrić

