

**MR813**

# SLAM/VSLAM intelligent robot solution

## The high-performance dual-camera robot chip

## Overview

The MR813 integrates a quad-core ARM Cortex™-A53 CPU that operates at speed up to 1.5 GHz, with dual MIPI CSI and 13M ISP. It is the cost-effective solution for robot sweeper applications.

## Highlights



### High Performance

MR813 adopts quad-core 64-bit Cortex™-A53 CPU up to 1.5 GHz, which has powerful performance to ensure the smoothly running of various recognition algorithms like VSLAM, in-depth information extraction, and object recognition.



### Dual Camera

MR813 integrates dual MIPI CSI, one ISP and H.264 encoder. With these features, MR813 supports up to two cameras (2M@60fps+2M@60fps) to meet the requirements of multiple applications such as dual TOF, one camera and one TOF, or dual camera.



### Rich Peripheral

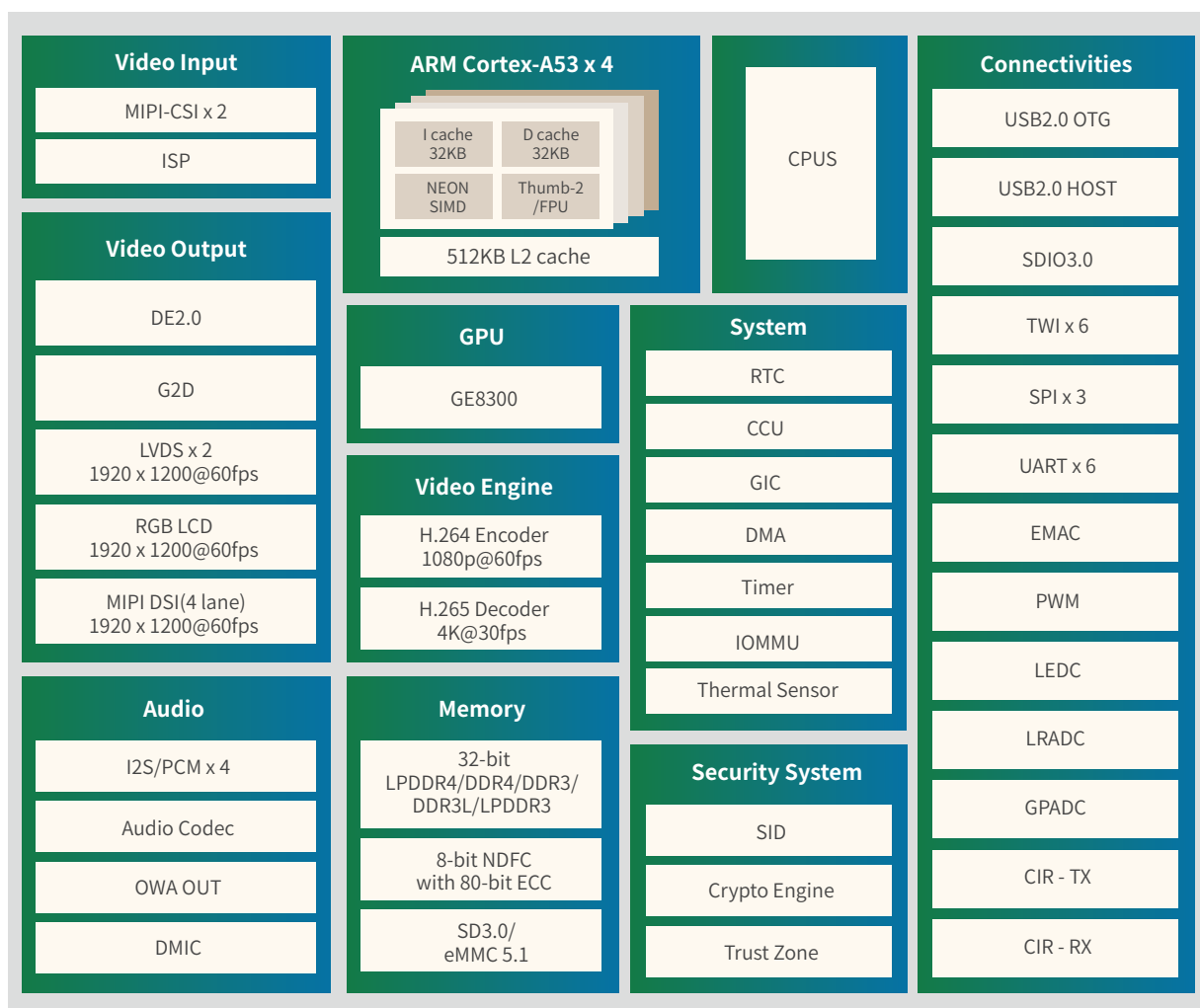
The MR813 supports rich peripheral interfaces, such as 2 x USB 2.0, 3 x SPI, 6 x TWI, 6 x UART, 5 x PWM, KEYADC, GPADC, SDIO 3.0, and so on.

## Features

CPU	<ul style="list-style-type: none"><li>• Quad-core ARM Cortex™-A53@1.5 GHz</li><li>• 32 KB L1 I-cache + 32 KB L1 D-cache per core</li><li>• 512 KB L2 cache</li></ul>
GPU	<ul style="list-style-type: none"><li>• GE8300</li><li>• OpenGL ES1.1/2.0/3.2, Vulkan1.1, OpenCL1.2</li></ul>
Memory	<ul style="list-style-type: none"><li>• 32-bit DDR4/DDR3/DDR3L/LPDDR3/LPDDR4, support 4 GB</li><li>• eMMC 5.1</li><li>• 8-bit MLC/SLC/EF NAND flash</li><li>• SPI NAND flash</li></ul>
Video Engine	<ul style="list-style-type: none"><li>• Supports H.264 HP encoder 1080P@60fps</li><li>• Supports JPEG encoder 4096 x 4096</li></ul>
Video Input	<ul style="list-style-type: none"><li>• Dual MIPI CSI up to 4lane and 2lane</li><li>• Maximum to one camera 8M@30fps or dual camera 5M@25fps + 2M@25fps</li><li>• Supports format: YUV422-8bit/10bit, YUV420-8bit, RGB888, RGB565</li></ul>

Others	<ul style="list-style-type: none"> <li>• USB Host, USB 2.0 OTG, SDIO 3.0</li> <li>• AUDIO 2 x ADC, 2 x DAC, 4 x I2S/TDM</li> <li>• LCD MIPI/LVDS/RGB/MCU</li> <li>• 3 x SPI, 6 x TWI, 6 x UART, 5 x PWM, KEYADC, GPADC</li> </ul>
PMIC	• AXP305B
WIFI	• XR829 or others
OS	• Linux4.9/Android 10.0
Package	<ul style="list-style-type: none"> <li>• LFBGA 346 balls</li> <li>• 12mm x 12mm size, 0.5 mm ball pitch, 0.3 mm ball size</li> </ul>

## Block Diagram



### ABOUT ALLWINNER

Allwinner Technology is a leading fabless design company dedicated to smart application processor SoCs and smart analog ICs. Its product line includes multi-core application processors for smart devices and smart power management ICs used by brands worldwide.

With its focus on cutting edge UHD video processing, high performance multi-core CPU/GPU integration, and ultra-low power consumption, Allwinner Technology is a mainstream solution provider for the global tablet, internet TV, smart home device, automotive in-dash device, smart power management, and mobile connected device markets. Allwinner Technology is headquartered in Zhuhai, China.

### CONTACT US

For more product info, please contact [service@allwinnertech.com](mailto:service@allwinnertech.com), or scan the QR code to follow us on Wechat.

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