

The diagram shows the ZL231-16KG module with the following connections:

- Pin X2-1:** +12V
- Pins X2-2 and X2-3:** +5V
- Pins X2-4, X2-6, and X2-8:** +12V
- Pins X2-5, X2-7, X2-9, X2-10, X2-12, X2-14, and X2-15:** GND
- Pins X2-11 and X2-13:** +3V3
- Pins X2-16 and X2-17:** +5V

Additional components and labels include:

- PE:** Power Entry terminal connected to ground.
- PWR_SSTART:** Power Sense Start terminal.
- I2C1_SCL:** I2C1 Serial Clock Line.
- I2C1_SDA:** I2C1 Serial Data Line.
- +3V3:** 3.3V output terminal.

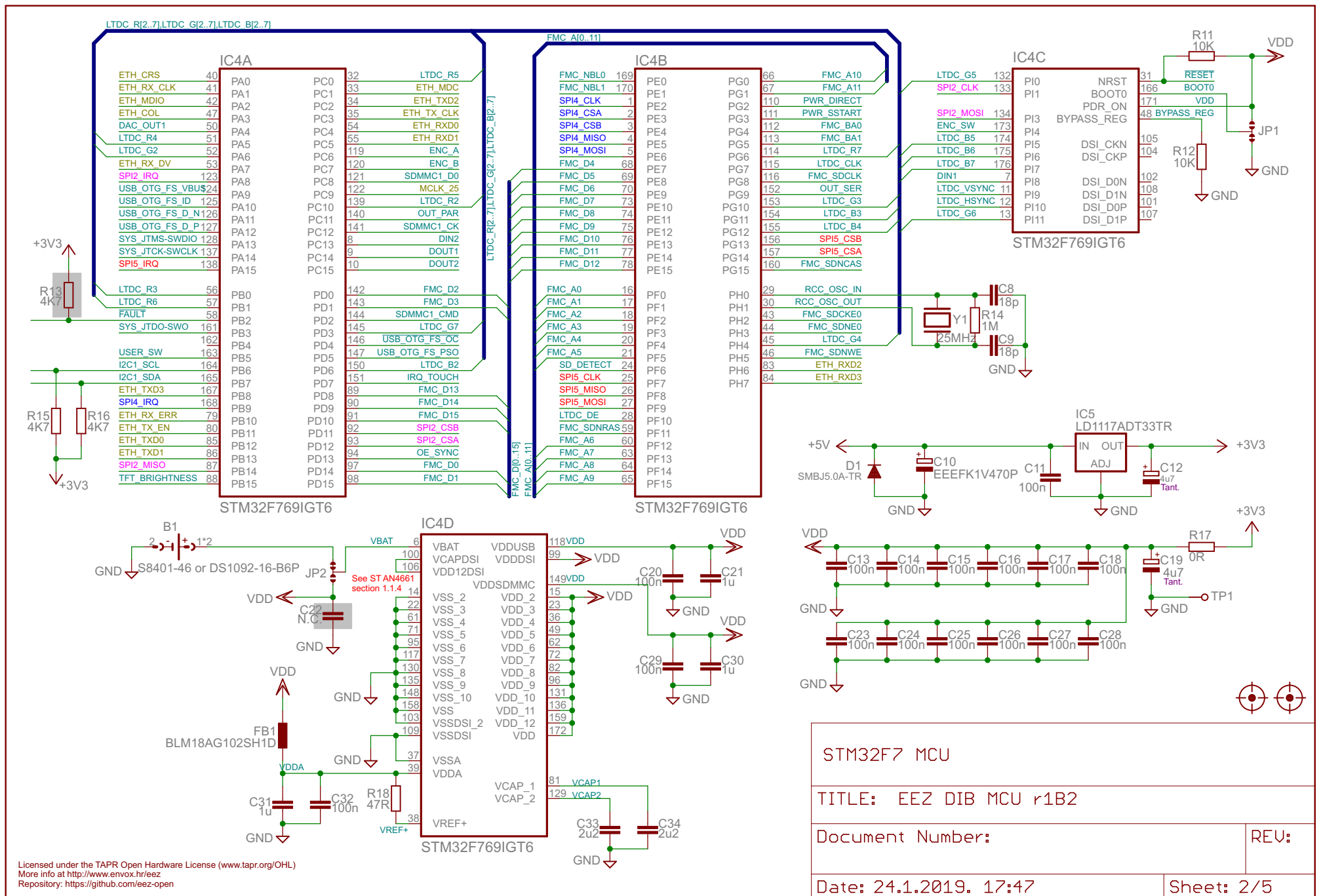
| | | | |
|-----------------|---|---|---------------|
| PE 1 | ○ | ○ | 2 PE |
| +12V 3 | ○ | ○ | 4 +12V |
| +5V 5 | ○ | ○ | 6 +5V |
| Gnd 7 | ○ | ○ | 8 Gnd |
| Gnd 9 | ○ | ○ | 10 +VAUX |
| PWR_SSTART 11 | ○ | ○ | 12 PWR_DIRECT |
| SSCL 13 | ○ | ○ | 14 SSDA |
| <u>FAULT</u> 15 | ○ | ○ | 16 +3V3 |

ZL263-40DG

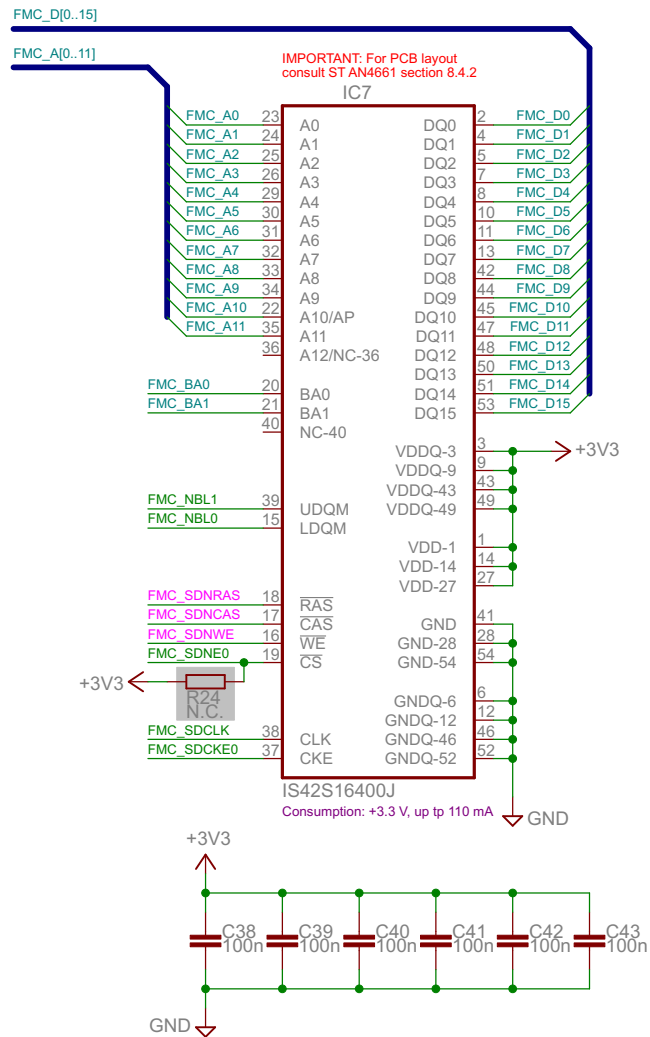
| Signal | Pin(s) |
|-----------|--|
| +3V3 | X1-1, X1-3, X1-5, X1-7, X1-9, X1-11, X1-13, X1-15, X1-17, X1-19, X1-21, X1-23, X1-25, X1-27, X1-29, X1-31, X1-33, X1-35, X1-37, X1-39 |
| GND | X1-2, X1-4, X1-6, X1-8, X1-10, X1-12, X1-14, X1-16, X1-18, X1-20, X1-22, X1-24, X1-26, X1-28, X1-30, X1-32, X1-34, X1-36, X1-38, X1-40 |
| OUT_SER | X1-1 |
| +VAUX | X1-3 |
| SPI5_IRQ | X1-5 |
| SPI5_CSA | X1-7 |
| SPI5_CSB | X1-9 |
| SPI5_CLK | X1-11 |
| SPI4_IRQ | X1-13 |
| SPI4_CSB | X1-15 |
| SPI4_CLK | X1-17 |
| SPI2_IRQ | X1-19 |
| SPI2_CSB | X1-21 |
| SPI2_MISO | X1-23 |
| SPI2_MOSI | X1-25 |
| OUT_PAR | X1-2 |
| RESET | X1-4 |
| FAULT | X1-6 |
| OE_SYNC | X1-8 |
| I2C1_SCL | X1-10 |
| I2C1_SDA | X1-12 |
| SPI5_MISO | X1-16 |
| SPI5_MOSI | X1-18 |
| SPI4_CSA | X1-20 |
| SPI4_MISO | X1-22 |
| SPI4_MOSI | X1-24 |
| SPI2_CSA | X1-26 |
| SPI2_CLK | X1-28 |
| +5V | X1-32, X1-34, X1-36, X1-38, X1-40 |
| +12V | X1-30, X1-32, X1-34, X1-36, X1-38, X1-40 |
| GND | X1-30, X1-32, X1-34, X1-36, X1-38, X1-40 |

| | | | |
|-------------|---|---|-------------|
| Gnd 1 | ○ | ○ | 2 Gnd |
| OUT_SER 3 | ○ | ○ | 4 OUT_PAR |
| +Vaux 5 | ○ | ○ | 6 NRESET |
| +3V3 7 | ○ | ○ | 8 NFAULT |
| CH3_IRQ 9 | ○ | ○ | 10 OE_SYNC |
| CH3_CSA 11 | ○ | ○ | 12 I2C_SCL |
| CH3_CSB 13 | ○ | ○ | 14 I2C_SDA |
| Gnd 15 | ○ | ○ | 16 Gnd |
| CH3_SCLK 17 | ○ | ○ | 18 CH3_MISO |
| CH2_IRQ 19 | ○ | ○ | 20 CH3_MOSI |
| CH2_CSB 21 | ○ | ○ | 22 CH2_CSA |
| CH2_SCLK 23 | ○ | ○ | 24 CH2_MISO |
| CH1_IRQ 25 | ○ | ○ | 26 CH2_MOSI |
| CH1_CSB 27 | ○ | ○ | 28 CH1_CSA |
| Gnd 29 | ○ | ○ | 30 Gnd |
| CH1_MISO 31 | ○ | ○ | 32 CH1_SCLK |
| CH1_MOSI 33 | ○ | ○ | 34 Gnd |
| +5V 35 | ○ | ○ | 36 +5V |
| +12V 37 | ○ | ○ | 38 +12V |
| Gnd 39 | ○ | ○ | 40 Gnd |

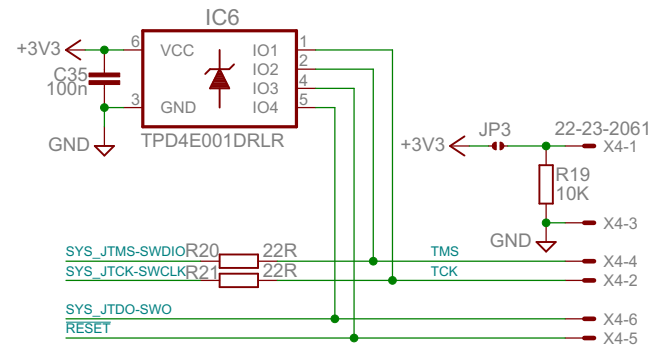
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SDRAM



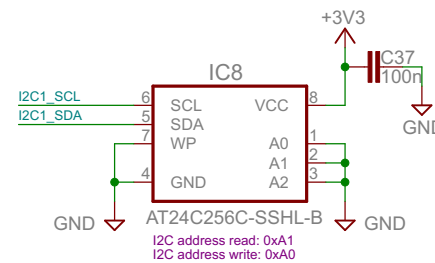
JTAG (SWD)



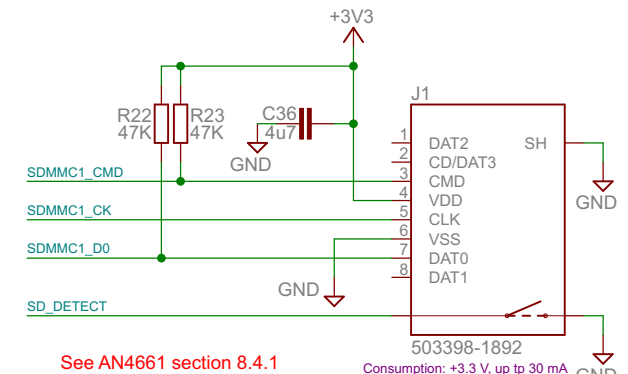
STM-32 board SWD header

| | |
|--------------|---|
| Vdd target 1 | ○ |
| SWCLK 2 | ○ |
| Gnd 3 | ○ |
| SWDIO 4 | ○ |
| NRST 5 | ○ |
| SWO 6 | ○ |

I2C EEPROM



Micro SD card socket



SDRAM, JTAG, I2C EEPROM, SD Card

TITLE: EEZ DIB MCU r1B2

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