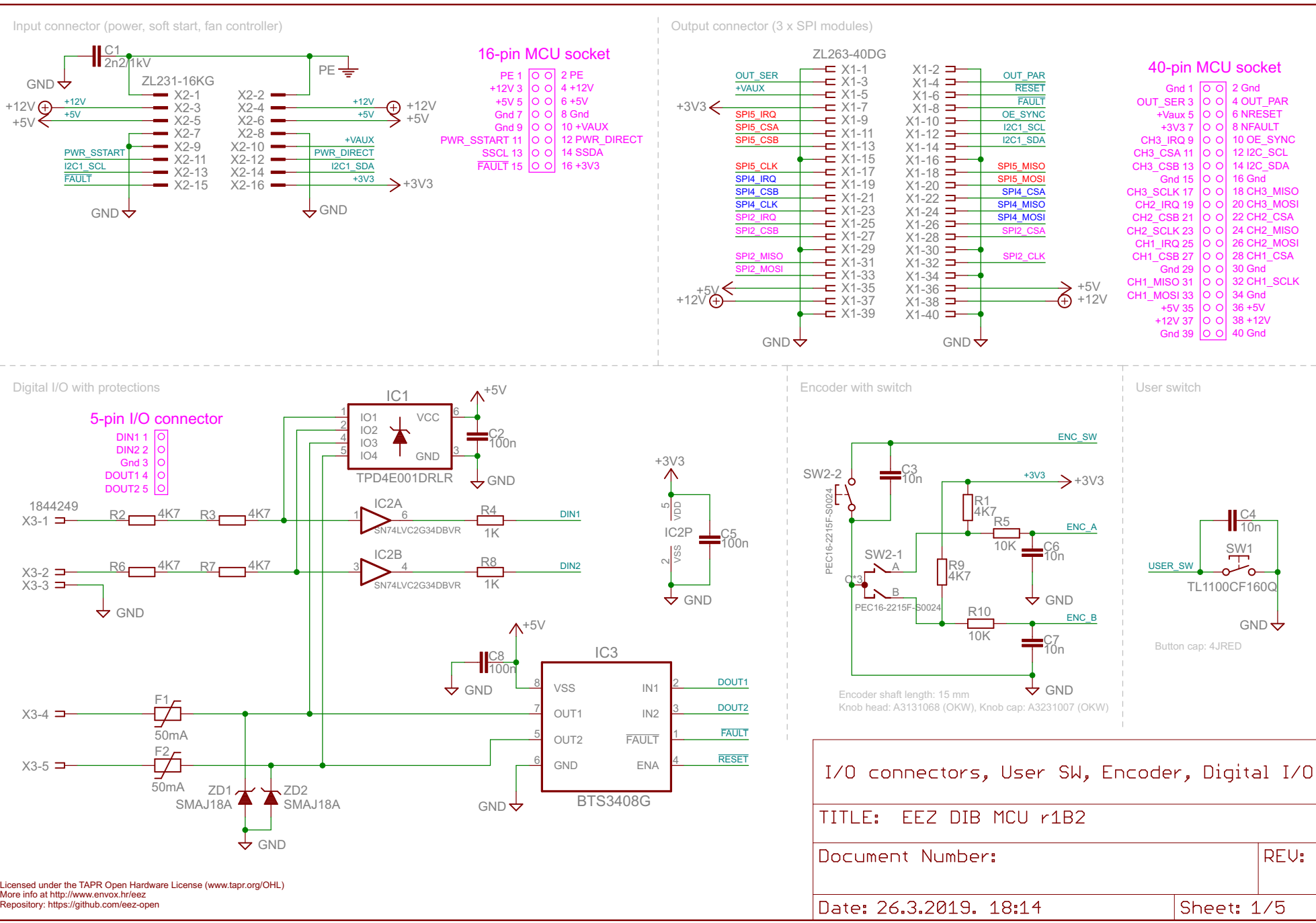
Input connector (power, soft start, fan controller)
C1
2n2/1kV
GND
PE
ZL231-16KG
+12V
+5V
X2-1
X2-3
X2-5
X2-7
X2-9
X2-11
X2-13
X2-15
PWR_SSTART
I2C1_SCL
FAULT
X2-2
X2-4
X2-6
X2-8
X2-10
X2-12
X2-14
X2-16
+VAUX
PWR_DIRECT
I2C1_SDA
+3V3
16-pin MCU socket
PE 1
+12V 3
+5V 5
Gnd 7
Gnd 9
PWR_SSTART 11
SSCL 13
FAULT 15
2 PE
4 +12V
6 +5V
8 Gnd
10 +VAUX
12 PWR_DIRECT
14 SSDA
16 +3V3
Output connector (3 x SPI modules)
ZL263-40DG
OUT_SER
+VAUX
SPI5_IRQ
SPI5_CSA
SPI5_CSB
SPI5_CLK
SPI4_IRQ
SPI4_CSB
SPI4_CLK
SPI2_IRQ
SPI2_CSB
SPI2_MISO
SPI2_MOSI
X1-1
X1-3
X1-5
X1-7
X1-9
X1-11
X1-13
X1-15
X1-17
X1-19
X1-21
X1-23
X1-25
X1-27
X1-29
X1-31
X1-33
X1-35
X1-37
X1-39
X1-2
X1-4
X1-6
X1-8
X1-10
X1-12
X1-14
X1-16
X1-18
X1-20
X1-22
X1-24
X1-26
X1-28
X1-30
X1-32
X1-34
X1-36
X1-38
X1-40
OUT_PAR
RESET
FAULT
OE_SYNC
I2C1_SCL
I2C1_SDA
SPI5_MISO
SPI5_MOSI
SPI4_CSA
SPI4_MISO
SPI4_MOSI
SPI2_CSA
SPI2_CLK
40-pin MCU socket
Gnd 1
OUT_SER 3
+Vaux 5
+3V3 7
CH3_IRQ 9
CH3_CSA 11
CH3_CSB 13
Gnd 15
CH3_SCLK 17
CH2_IRQ 19
CH2_CSB 21
CH2_SCLK 23
CH1_IRQ 25
CH1_CSB 27
Gnd 29
CH1_MISO 31
CH1_MOSI 33
+5V 35
+12V 37
Gnd 39
2 Gnd
4 OUT_PAR
6 NRESET
8 NFAULT
10 OE_SYNC
12 I2C_SCL
14 I2C_SDA
16 Gnd
18 CH3_MISO
20 CH3_MOSI
22 CH2_CSA
24 CH2_MISO
26 CH2_MOSI
28 CH1_CSA
30 Gnd
32 CH1_SCLK
34 Gnd
36 +5V
38 +12V
40 Gnd
Digital I/O with protections
5-pin I/O connector
DIN1 1
DIN2 2
Gnd 3
DOUT1 4
DOUT2 5
IC1
TPD4E001DRLR
C2
100n
1844249
X3-1
X3-2
X3-3
R2
R3
R6
R7
4K7
IC2A
IC2B
SN74LVC2G34DBVR
R4
R8
1K
DIN1
DIN2
IC2P
C5
100n
C8
100n
IC3
BTS3408G
VSS
OUT1
OUT2
GND
IN1
IN2
FAULT
ENA
DOUT1
DOUT2
FAULT
RESET
X3-4
X3-5
F1
F2
50mA
ZD1
ZD2
SMAJ18A
Encoder with switch
SW2-2
PEC16-2215F-S0024
C3
10n
ENC_SW
ENC_A
ENC_B
R1
4K7
R5
10K
C6
10n
SW2-1
R9
4K7
R10
10K
C7
10n
C*3
Encoder shaft length: 15 mm
Knob head: A3131068 (OKW), Knob cap: A3231007 (OKW)
User switch
C4
10n
SW1
USER_SW
TL1100CF160Q
Button cap: 4JRED
I/O connectors, User SW, Encoder, Digital I/O
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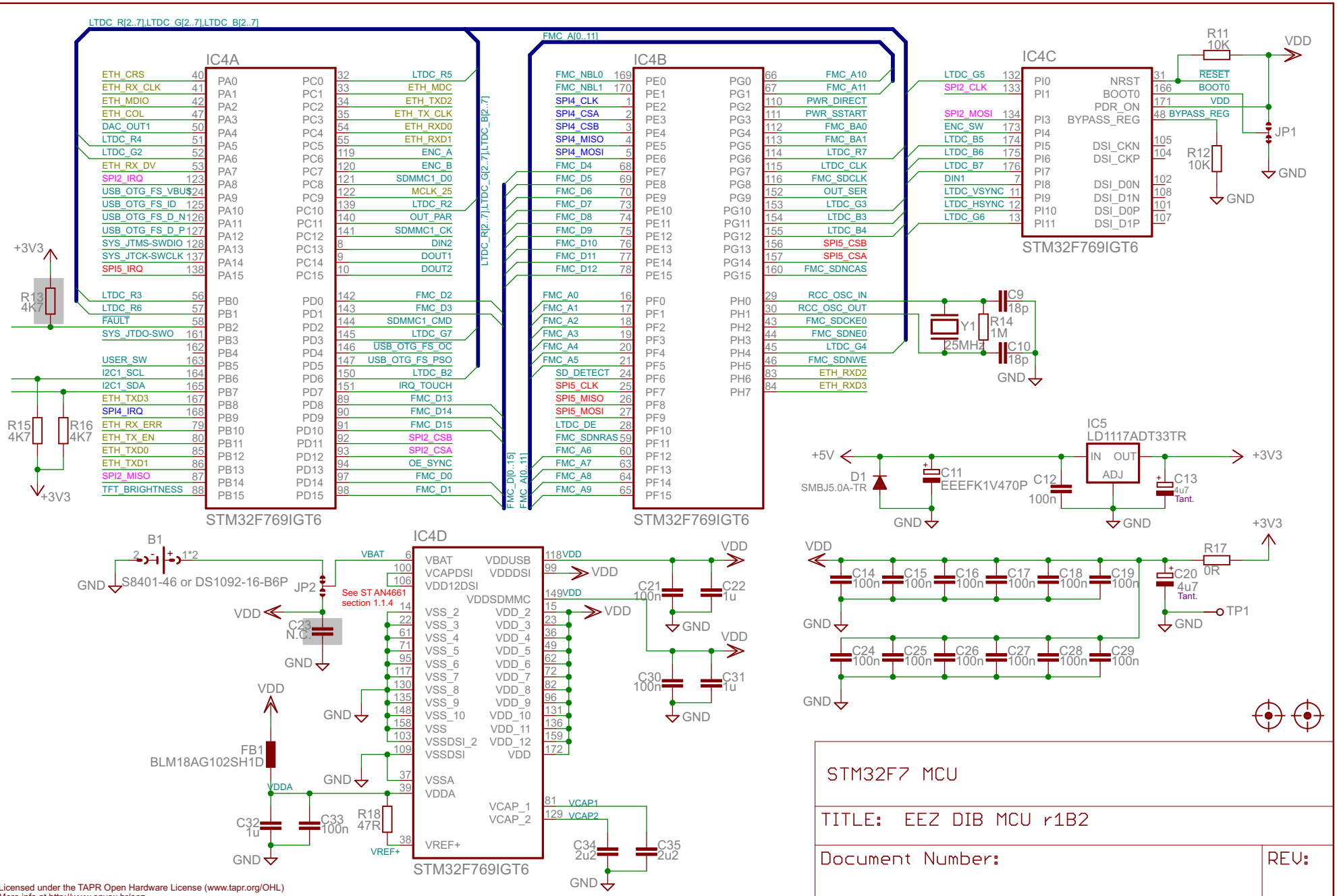
LTDC_R[2..7],LTDC_G[2..7],LTDC_B[2..7]
FMC_A[0..11]
IC4A
IC4B
IC4C
STM32F769IGT6
ETH_CRS
ETH_RX_CLK
ETH_MDIO
ETH_COL
DAC_OUT1
LTDC_R4
LTDC_G2
ETH_RX_DV
SPI2_IRQ
USB_OTG_FS_VBUS
USB_OTG_FS_ID
USB_OTG_FS_D_N
USB_OTG_FS_D_P
SYS_JTMS-SWDIO
SYS_JTCK-SWCLK
SPI5_IRQ
LTDC_R3
LTDC_R6
FAULT
SYS_JTDO-SWO
USER_SW
I2C1_SCL
I2C1_SDA
ETH_TXD3
SPI4_IRQ
ETH_RX_ERR
ETH_TX_EN
ETH_TXD0
ETH_TXD1
SPI2_MISO
TFT_BRIGHTNESS
LTDC_R5
ETH_MDC
ETH_TXD2
ETH_TX_CLK
ETH_RXD0
ETH_RXD1
ENC_A
ENC_B
SDMMC1_D0
MCLK_25
LTDC_R2
OUT_PAR
SDMMC1_CK
DIN2
DOUT1
DOUT2
FMC_D2
FMC_D3
SDMMC1_CMD
LTDC_G7
USB_OTG_FS_OC
USB_OTG_FS_PSO
LTDC_B2
IRQ_TOUCH
FMC_D13
FMC_D14
FMC_D15
SPI2_CSB
SPI2_CSA
OE_SYNC
FMC_D0
FMC_D1
FMC_NBL0
FMC_NBL1
SPI4_CLK
SPI4_CSA
SPI4_CSB
SPI4_MISO
SPI4_MOSI
FMC_D4
FMC_D5
FMC_D6
FMC_D7
FMC_D8
FMC_D9
FMC_D10
FMC_D11
FMC_D12
FMC_A0
FMC_A1
FMC_A2
FMC_A3
FMC_A4
FMC_A5
SD_DETECT
SPI5_CLK
SPI5_MISO
SPI5_MOSI
LTDC_DE
FMC_SDNRAS
FMC_A6
FMC_A7
FMC_A8
FMC_A9
FMC_A10
FMC_A11
PWR_DIRECT
PWR_SSTART
FMC_BA0
FMC_BA1
LTDC_R7
LTDC_CLK
FMC_SDCLK
OUT_SER
LTDC_G3
LTDC_B3
LTDC_B4
SPI5_CSB
SPI5_CSA
FMC_SDNCAS
RCC_OSC_IN
RCC_OSC_OUT
FMC_SDCKE0
FMC_SDNE0
LTDC_G4
FMC_SDNWE
ETH_RXD2
ETH_RXD3
LTDC_G5
SPI2_CLK
SPI2_MOSI
ENC_SW
LTDC_B5
LTDC_B6
LTDC_B7
DIN1
LTDC_VSYNC
LTDC_HSYNC
LTDC_G6
NRST
BOOT0
PDR_ON
BYPASS_REG
DSI_CKN
DSI_CKP
DSI_D0N
DSI_D1N
DSI_D0P
DSI_D1P
RESET
VDD
+3V3
GND
R11 10K
R12 10K
JP1
R13 4K7
R14 1M
R15 4K7
R16 4K7
Y1 25MHz
C9 18p
C10 18p
IC5 LD1117ADT33TR
IN
OUT
ADJ
+5V
D1 SMBJ5.0A-TR
C11 EEEFK1V470P
C12 100n
C13 4u7 Tant.
R17 0R
TP1
C14 100n
C15 100n
C16 100n
C17 100n
C18 100n
C19 100n
C20 4u7 Tant.
C24 100n
C25 100n
C26 100n
C27 100n
C28 100n
C29 100n
IC4D
B1
S8401-46 or DS1092-16-B6P
JP2
C23 N.C.
See ST AN4661 section 1.1.4
VBAT
VCAPDSI
VDD12DSI
VDDSDMMC
VDDUSB
VDDDSI
VSS_2
VSS_3
VSS_4
VSS_5
VSS_6
VSS_7
VSS_8
VSS_9
VSS_10
VSS
VSSDSI_2
VSSDSI
VSSA
VDDA
VDD_2
VDD_3
VDD_4
VDD_5
VDD_6
VDD_7
VDD_8
VDD_9
VDD_10
VDD_11
VDD_12
VCAP_1
VCAP_2
VREF+
FB1 BLM18AG102SH1D
R18 47R
C21 100n
C22 1u
C30 100n
C31 1u
C32 1u
C33 100n
C34 2u2
C35 2u2
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STM32F7 MCU
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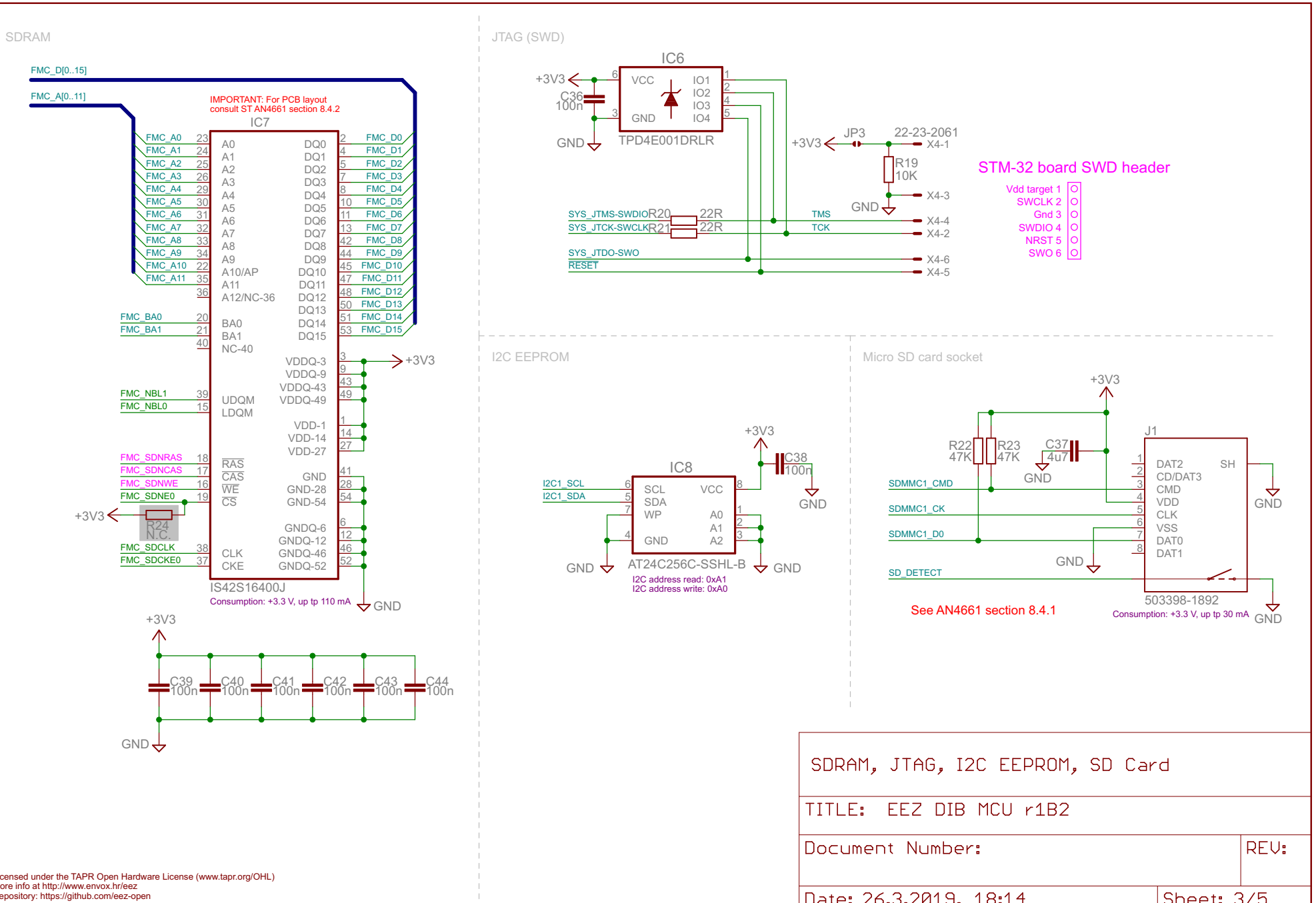

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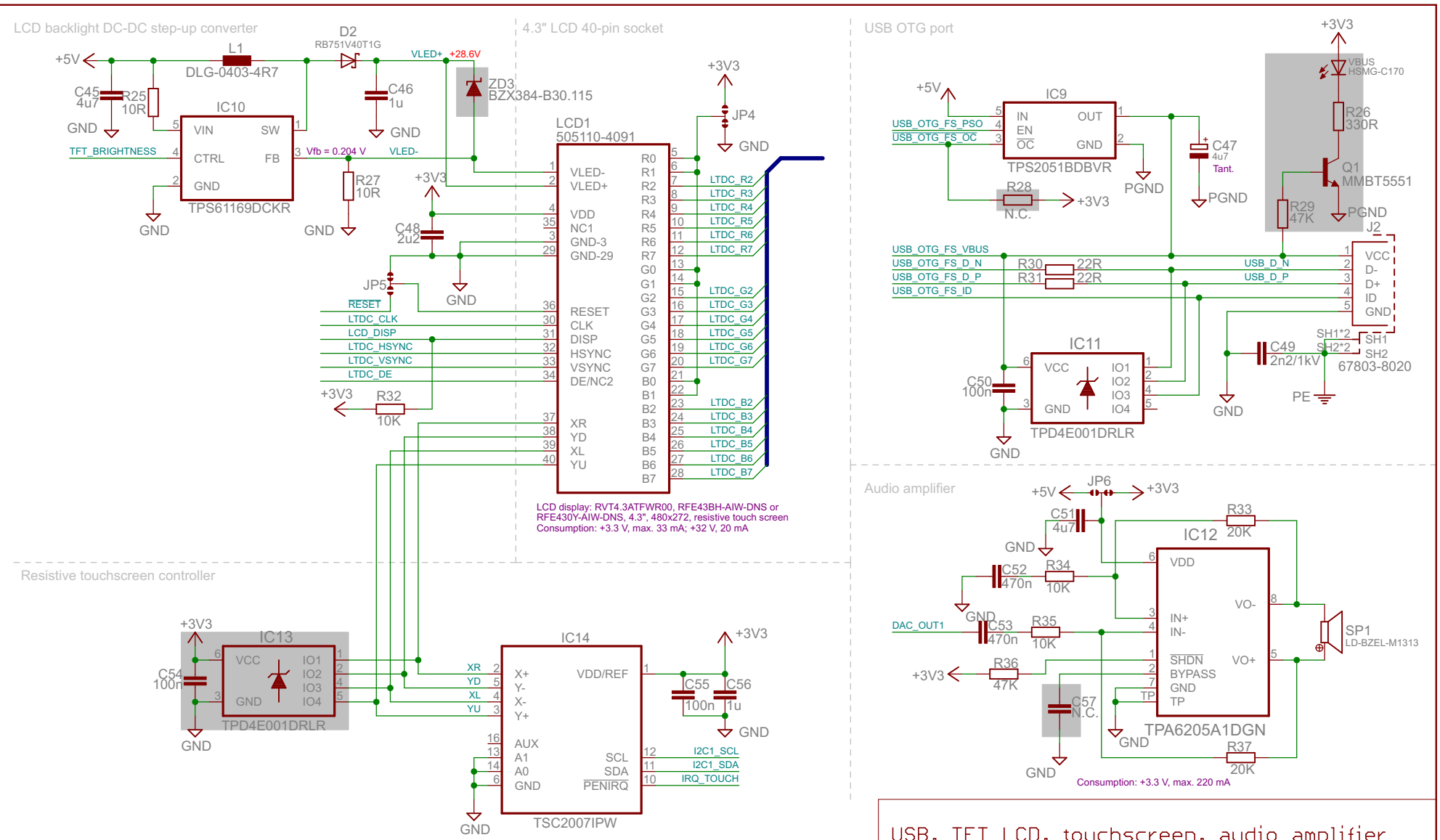

USB, TFT LCD, touchscreen, audio amplifier

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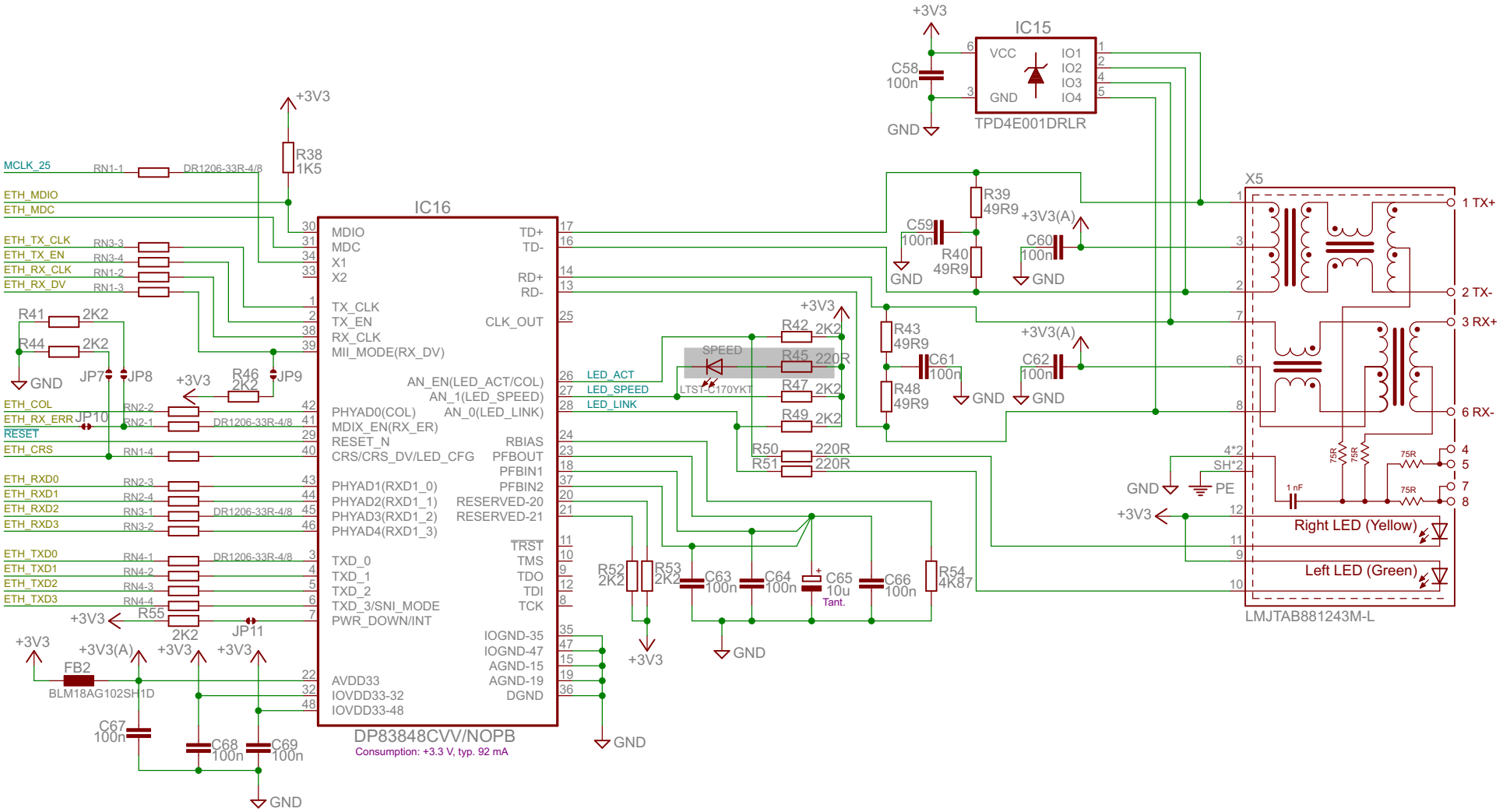

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