

SMPS power pre-regulator with 100% duty cycle

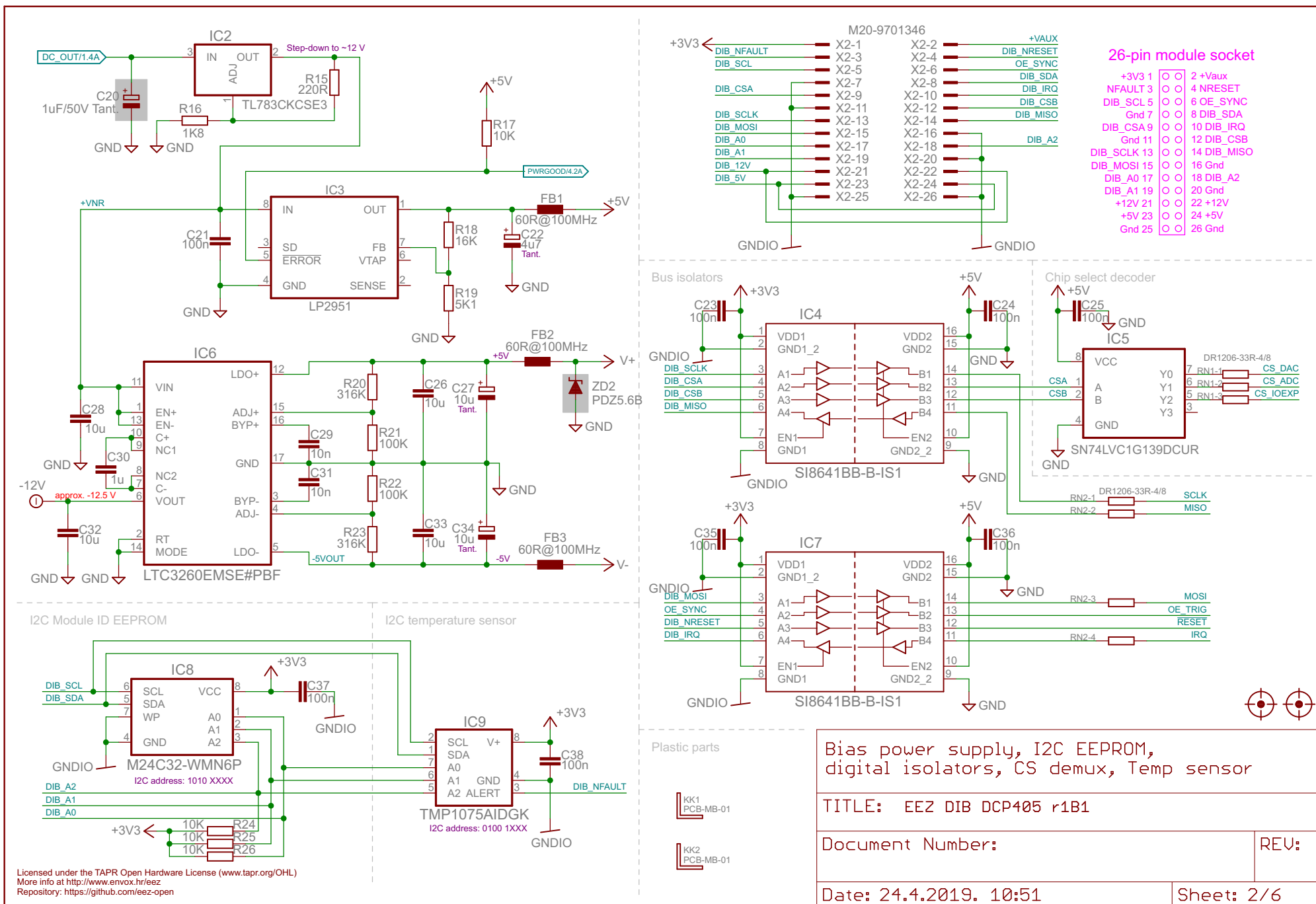
TITLE: EEZ DIB DCP405 r1B1

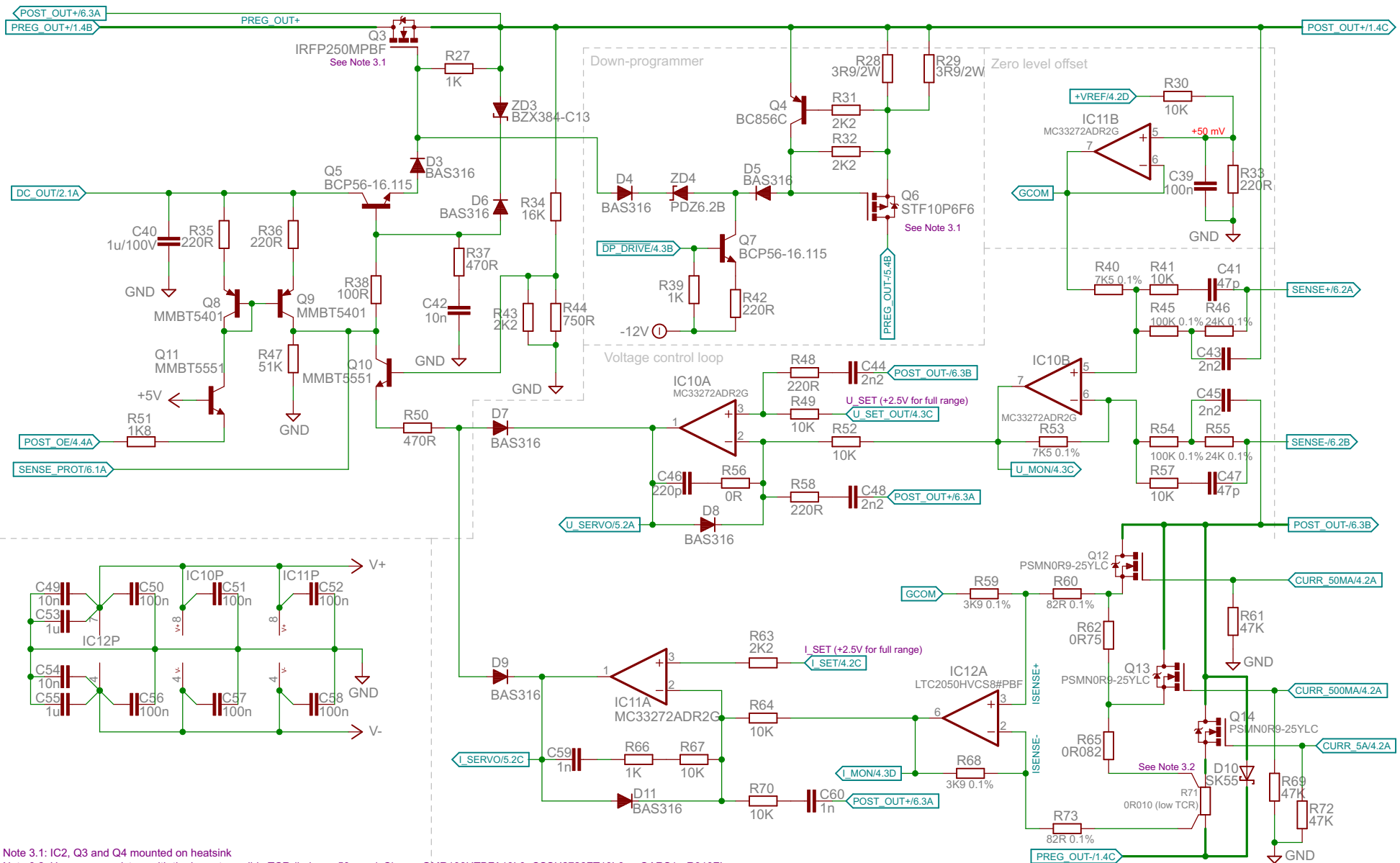
Document Number:

REV:

Date: 24.4.2019. 10:51

Sheet: 1/6



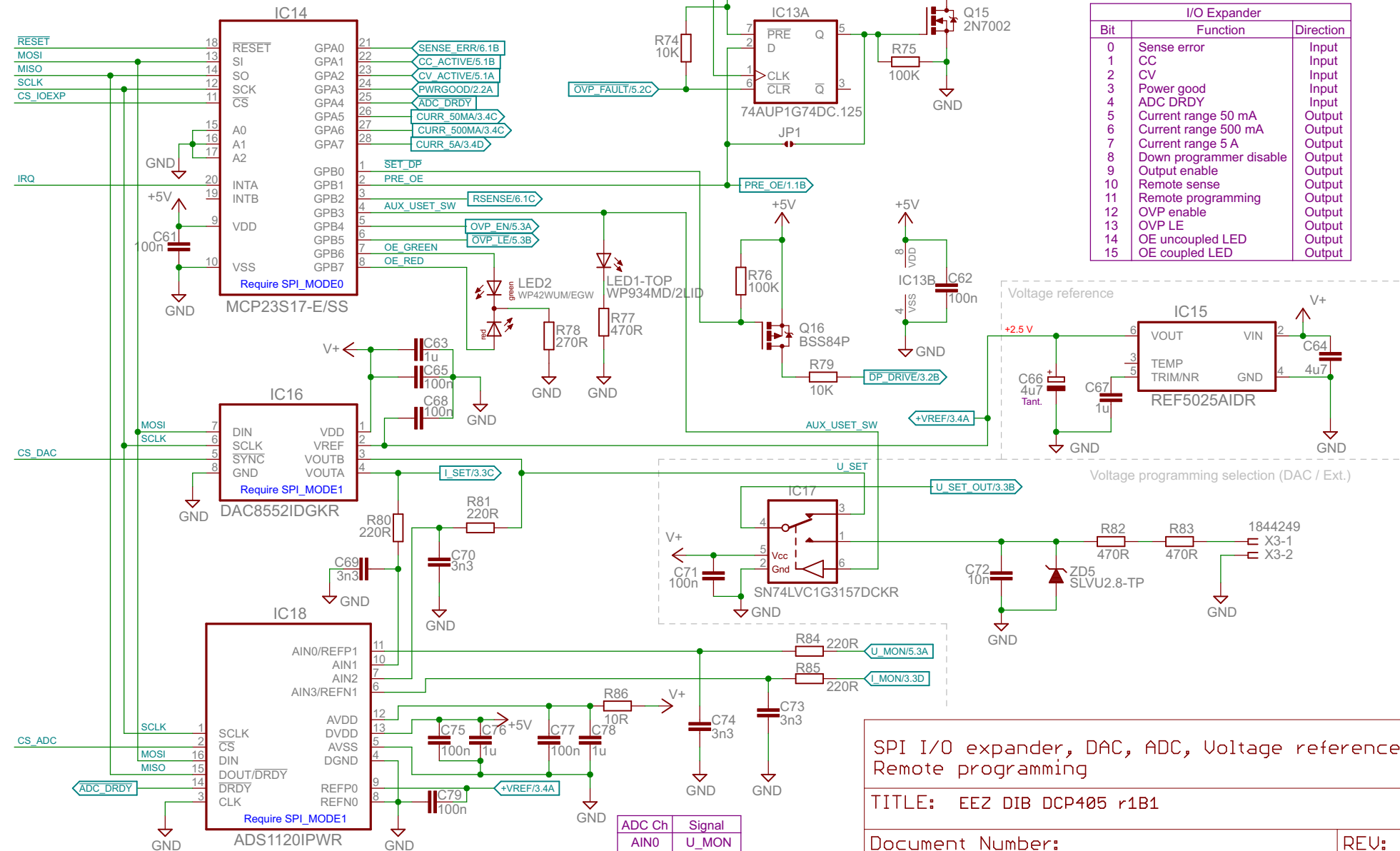


Note 3.1: IC2, Q3 and Q4 mounted on heatsink

Note 3.2: Use power resistors with the lowest possible TCR (below  $\pm 50$  ppm/oC), e.g. GMR100HTBFA10L0, CSSH2728FT10L0 or OARS1 - R010FI

# Digital control (SPI)

OE\_TRIG



SPI I/O expander, DAC, ADC, Voltage reference  
 Remote programming

TITLE: EEZ DIB DCP405 r1B1

Document Number:

REV:

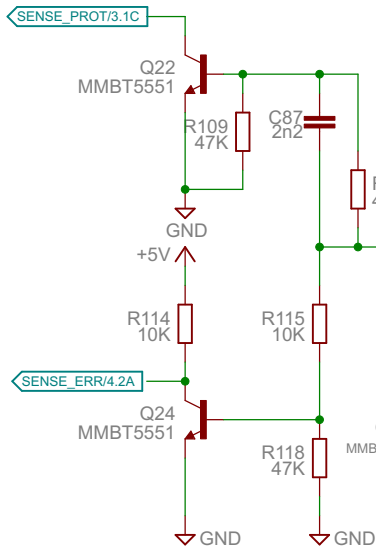
Date: 24.4.2019. 10:51

Sheet: 4/6

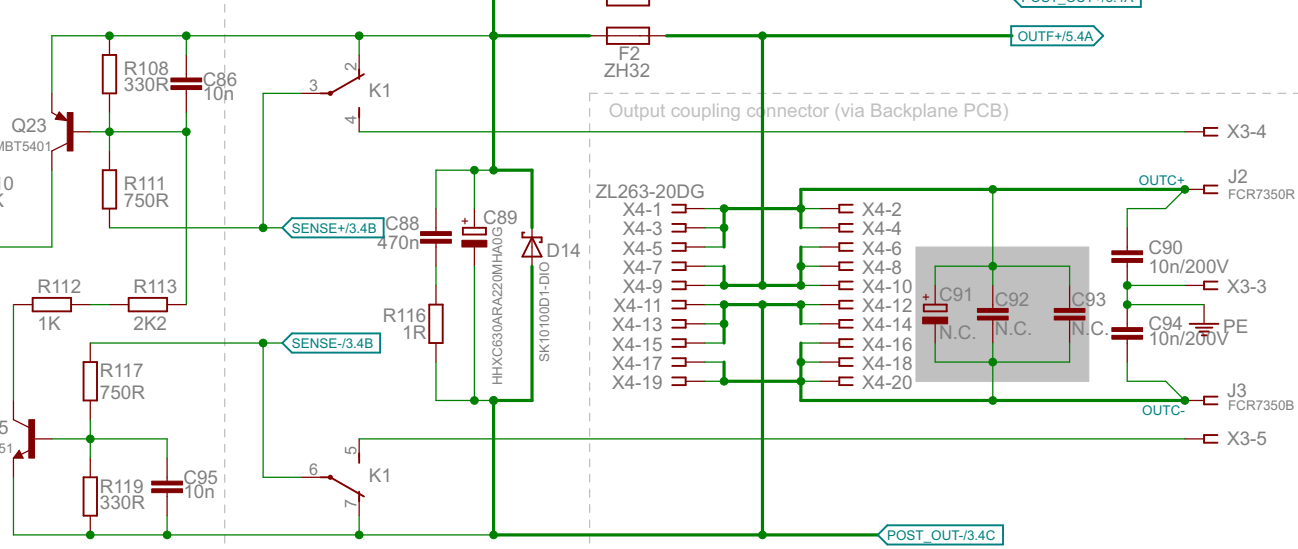
[illegible]

Sheet: 5/6

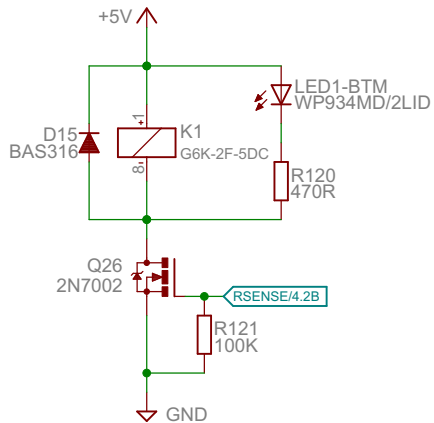
# Remote sense reverse polarity detection



# Power output section



# Remote sense selection



# 20-pin power socket

OUTC+ 1	2 OUTC+
OUTC+ 3	4 OUTC+
OUTC+ 5	6 POST_OUT+ (fused)
POST_OUT+ (fused) 7	8 POST_OUT+ (fused)
POST_OUT+ (fused) 9	10 POST_OUT+ (fused)
POST_OUT- 11	12 POST_OUT-
POST_OUT- 13	14 POST_OUT-
POST_OUT- 15	16 OUTC-
OUTC- 17	18 OUTC-
OUTC- 19	20 OUTC-

Rsense polarity protection  
Output coupling, Rsense relay

TITLE: EEZ DIB DCP405 r1B1

Document Number:

REV:

Date: 24.4.2019. 10:51

Sheet: 6/6