			MCU module (2 x 20-	·pın)			Status: Mandatory
	A	Dir			В	Dir	
1	GND			2	GND		
3	UART_RX	I	Shared UART RX	4	UART_TX	0	Shared UART TX
5	+VAUX	I/O	Backup DC power	6	NRESET	0	Master reset (active low)
7	+3V3	0	DC power	8	NFAULT	I	Fault (active low)
9	CH3_IRQ	I	#3 IRQ	10	SYNC	0	Sync output
11	CH3_CSA	0	#3 Chip select A	12	SSCL	0	Shared I ² C SCL
13	CH3_CSB	0	#3 Chip select B	14	SSDA	I/O	Shared I ² C SDA
15	GND			16	GND		
17	CH3_SCLK	0	#3 SPI CLK	18	CH3_MISO	I	#3 MISO
19	CH2_IRQ	ı	#2 IRQ	20	CH3_MOSI	0	#3 MOSI
21	CH2_CSB	0	#2 Chip select B	22	CH2_CSA	0	#2 Chip select A
23	CH2_SCLK	0	#2 SPI CLK	24	CH2_MISO	I	#2 MISO
25	CH1_IRQ	ı	#1 IRQ	26	CH2_MOSI	0	#2 MOSI
27	CH1_CSB	0	#1 Chip select B	28	CH1_CSA	0	#1 Chip select A
29	GND			30	GND		
31	CH1_MISO	I	#1 MISO	32	CH1_SCLK	0	#1 SPI CLK
33	CH1_MOSI	0	#1 MOSI	34	GND		
35	+5V	- 1	DC power	36	+5V	I	DC power
37	+12V	- 1	DC power	38	+12V	I	DC power
39	GND			40	GND		
	Α		Peripheral modules (2 x	14-p		D:-	Status: Mandatory
	A	Dir	DC nower		В	Dir	Doolup DO nove:
1	+3V3	1/0	DC power	2	+VAUX	l I	Backup DC power
3	NFAULT	1/0	Fault (active low)	4	NRESET	l	Master reset (active low)
5	SSCL	1	Shared I ² C SCL	6	SYNC		Sync input
7	GND		Maril In Old and India	8	SSDA	1/0	Shared I ² C SDA
9	CSA	ı	Module Chip select A	10	IRQ	0	Module IRQ
11	GND			12	CSB		Module Chip select B
13	SCLK		Module SPI CLK	14	MISO	0	Module MISO
15	MOSI		Module MOSI	16	GND		
17	A0		I ² C Address 0	18	A2	ı	I ² C Address 2
19	A1		I ² C Address 1	20	GND		
21	+12V	l l	DC power	22	+12V	- 1	DC power
23	+5V	ı	DC power	24	+5V	- 1	DC power
25	GND			26	ВООТ	ı	Module bootloader select
			OL				Status: Optional*
27	UART_TX	0	Shared UART TX	28	UART RX	ı	Shared UART RX
ALIX PS module (2 x 8-nin) Status: Recommended							
			AUX PS module (2 x 8	3-pin)			Status: Recommended
	A	Dir	AUX PS module (2 x 8	3-pin)		Dir	Status: Recommended
<u> </u>	A PE	Dir	AUX PS module (2 x 8	3-pin) 2		Dir	Status: Recommended N.C.
1 3		Dir O	AUX PS module (2 x 8		В	_	
	PE	_	-	2	B N.C.	0	N.C.
3	PE +12V	0	DC power	2 4	B N.C. +12V	0	N.C. DC power
3 5	PE +12V +5V	0	DC power	2 4 6	B N.C. +12V +5V	0	N.C. DC power
3 5 7	PE +12V +5V GND	0	DC power	2 4 6 8	B N.C. +12V +5V GND	0 0	N.C. DC power DC power
3 5 7 9	PE +12V +5V GND GND	0	DC power DC power	2 4 6 8 10	B N.C. +12V +5V GND +VAUX	0 0 0	N.C. DC power DC power Backup DC power
3 5 7 9 11	PE +12V +5V GND GND PWR_SSTART	0 0	DC power DC power AC soft-start	2 4 6 8 10 12	B N.C. +12V +5V GND +VAUX PWR_DIRECT	0 0 0 1/0	N.C. DC power DC power Backup DC power AC power on
3 5 7 9 11 13	PE +12V +5V GND GND PWR_SSTART SSCL	0 0 1 1 1/0	DC power DC power AC soft-start Shared I ² C SCL Fault (active low)	2 4 6 8 10 12 14 16	B N.C. +12V +5V GND +VAUX PWR_DIRECT SSDA +3V3	0 0 0 1/0 1 1/0	N.C. DC power DC power Backup DC power AC power on Shared I ² C SDA DC power
3 5 7 9 11 13	PE +12V +5V GND GND PWR_SSTART SSCL NFAULT	0 0 1 1 1/0	DC power DC power AC soft-start Shared I ² C SCL	2 4 6 8 10 12 14 16	B N.C. +12V +5V GND +VAUX PWR_DIRECT SSDA +3V3	0 0 0 1/0 1 1/0	N.C. DC power DC power Backup DC power AC power on Shared I ² C SDA
3 5 7 9 11 13 15	PE +12V +5V GND GND PWR_SSTART SSCL NFAULT	0 0 1 1 1/0	DC power DC power AC soft-start Shared I ² C SCL Fault (active low) ower source module (2 :	2 4 6 8 10 12 14 16 x 10-	B N.C. +12V +5V GND +VAUX PWR_DIRECT SSDA +3V3 pin) B	0 0 0 1/0 1 1/0	N.C. DC power DC power Backup DC power AC power on Shared I ² C SDA DC power Status: Optional
3 5 7 9 11 13 15	PE +12V +5V GND GND PWR_SSTART SSCL NFAULT	0 0 1 1 1/0	DC power DC power AC soft-start Shared I ² C SCL Fault (active low) ower source module (2 in power positive input	2 4 6 8 10 12 14 16 x 10-	B N.C. +12V +5V GND +VAUX PWR_DIRECT SSDA +3V3 pin) B	0 0 0 1/0 1 1/0 1	N.C. DC power DC power Backup DC power AC power on Shared I ² C SDA DC power Status: Optional
3 5 7 9 11 13 15	PE +12V +5V GND GND PWR_SSTART SSCL NFAULT A	0 0 1 1 1/0	DC power DC power AC soft-start Shared I ² C SCL Fault (active low) ower source module (2 in the content of t	2 4 6 8 10 12 14 16 x 10-	B N.C. +12V +5V GND +VAUX PWR_DIRECT SSDA +3V3 pin) B IN+ IN+	O	N.C. DC power DC power Backup DC power AC power on Shared I ² C SDA DC power Status: Optional Power positive input Power positive input
3 5 7 9 11 13 15	PE +12V +5V GND GND PWR_SSTART SSCL NFAULT A IN+ IN+ IN+	0 0 1 1 1/0 Po Dir	DC power DC power AC soft-start Shared I ² C SCL Fault (active low) Ower source module (2 to 1) Power positive input Power positive input Power positive input	2 4 6 8 10 12 14 16 × 10-	B N.C. +12V +5V GND +VAUX PWR_DIRECT SSDA +3V3 pin) B IN+ IN+ OUT+	O	N.C. DC power DC power Backup DC power AC power on Shared I ² C SDA DC power Status: Optional Power positive input Power positive output
3 5 7 9 11 13 15	PE +12V +5V GND GND PWR_SSTART SSCL NFAULT A IN+ IN+ IN+ OUT+	0 0 1 1/0 1/0 Dir	DC power DC power AC soft-start Shared I ² C SCL Fault (active low) Ower source module (2 to 1) Power positive input Power positive input Power positive input Power positive input Power positive output	2 4 6 8 10 12 14 16 × 10-	B N.C. +12V +5V GND +VAUX PWR_DIRECT SSDA +3V3 pin) B IN+ IN+ OUT+ OUT+	O O I/O I I/O I I O O O O O O O	N.C. DC power DC power Backup DC power AC power on Shared I ² C SDA DC power Status: Optional Power positive input Power positive output Power positive output
3 5 7 9 11 13 15 15 1 3 5 7	PE +12V +5V GND GND PWR_SSTART SSCL NFAULT A IN+ IN+ OUT+ OUT+	0 0 1 1/0 Po Dir	DC power DC power AC soft-start Shared I ² C SCL Fault (active low) Ower source module (2 : Power positive input Power positive input Power positive input Power positive output Power positive output Power positive output	2 4 6 8 10 12 14 16 × 10-	B N.C. +12V +5V GND +VAUX PWR_DIRECT SSDA +3V3 pin) B IN+ IN+ OUT+ OUT+ OUT+	0 0 1/0 1 1/0 1 1 0 0	N.C. DC power DC power Backup DC power AC power on Shared I ² C SDA DC power Status: Optional Power positive input Power positive output Power positive output Power positive output Power positive output
3 5 7 9 11 13 15 1 1 3 5 7 9 11	PE +12V +5V GND GND PWR_SSTART SSCL NFAULT A IN+ IN+ OUT+ OUT+ OUT-	0 0 1 1/0 Po Dir 1 1 0 0	DC power DC power AC soft-start Shared I ² C SCL Fault (active low) Dwer source module (2 x Power positive input Power positive input Power positive input Power positive output Power positive output Power positive output Power positive output Power negative output	2 4 6 8 10 12 14 16 × 10-	B N.C. +12V +5V GND +VAUX PWR_DIRECT SSDA +3V3 pin) B IN+ IN+ OUT+ OUT+ OUT+ OUT-	O	N.C. DC power DC power Backup DC power AC power on Shared I ² C SDA DC power Status: Optional Power positive input Power positive output
3 5 7 9 11 13 15 15 1 3 5 7 9 11 13	PE +12V +5V GND GND PWR_SSTART SSCL NFAULT A IN+ IN+ OUT+ OUT+ OUT- OUT- OUT-		DC power DC power AC soft-start Shared I ² C SCL Fault (active low) Dever source module (2 of the content of t	2 4 6 8 10 12 14 16 x 10-	B N.C. +12V +5V GND +VAUX PWR_DIRECT SSDA +3V3 pin) B IN+ IN+ OUT+ OUT+ OUT+ OUT- OUT- OUT-	O	N.C. DC power DC power Backup DC power AC power on Shared I ² C SDA DC power Status: Optional Power positive input Power positive output Power positive output Power positive output Power positive output Power negative output Power negative output
3 5 7 9 11 13 15 1 1 3 5 7 9 11 13 15	PE +12V +5V GND GND PWR_SSTART SSCL NFAULT A IN+ IN+ OUT+ OUT+ OUT- OUT- OUT- OUT-	0 0 1 1/0 Po Dir 1 1 0 0	DC power DC power AC soft-start Shared I ² C SCL Fault (active low) Dever source module (2 of the power positive input Power positive input Power positive input Power positive output Power positive output Power negative output	2 4 6 8 10 12 14 16 × 10-	B N.C. +12V +5V GND +VAUX PWR_DIRECT SSDA +3V3 pin) B IN+ IN+ OUT+ OUT+ OUT+ OUT- OUT- IN-	O	N.C. DC power DC power Backup DC power AC power on Shared I ² C SDA DC power Status: Optional Power positive input Power positive output Power positive output Power positive output Power positive output Power negative output Power negative output Power negative input
3 5 7 9 11 13 15 15 1 3 5 7 9 11 13	PE +12V +5V GND GND PWR_SSTART SSCL NFAULT A IN+ IN+ OUT+ OUT+ OUT- OUT- OUT-		DC power DC power AC soft-start Shared I ² C SCL Fault (active low) Dever source module (2 of the content of t	2 4 6 8 10 12 14 16 x 10-	B N.C. +12V +5V GND +VAUX PWR_DIRECT SSDA +3V3 pin) B IN+ IN+ OUT+ OUT+ OUT+ OUT- OUT- OUT-	O	N.C. DC power DC power Backup DC power AC power on Shared I ² C SDA DC power Status: Optional Power positive input Power positive output Power positive output Power positive output Power positive output Power negative output Power negative output

*) The first 26-pin of peripheral module connector is mandatory and last two pin are optional. New versions of DIB specification could introduce even more features but that will require also introduction of larger MCU connector or additional connector for the MCU