		MCU module (2 x 20-	pin)			Status: Mandatory
	Α	Dir		В	Dir	
1	GND		2	GND		
3	UART_RX	□ Shared UART RX	4	UART_TX	0	Shared UART TX
5	+VAUX	Backup DC power	6	NRESET	0	Master reset (active low)
7	+3V3	DC power	8	NFAULT	Ī	Fault (active low)
9	CH3_IRQ	#3 IRQ	10	SYNC	0	Sync output
11	CH3_CSA	O #3 Chip select A	12	SSCL	0	Shared I <sup>2</sup> C SCL
13	CH3_CSB	O #3 Chip select B	14	SSDA	I/O	Shared I <sup>2</sup> C SDA
15	GND		16	GND		
17	CH3_SCLK	O #3 SPI CLK	18	CH3_MISO	I	#3 MISO
19	CH2_IRQ	│ #2 IRQ	20	CH3_MOSI	0	#3 MOSI
21	CH2_CSB	O #2 Chip select B	22	CH2_CSA	0	#2 Chip select A
23	CH2_SCLK	O #2 SPI CLK	24	CH2_MISO		#2 MISO
25	CH1_IRQ	#1 IRQ	26	CH2 MOSI	0	#2 MOSI
27	CH1_CSB	O #1 Chip select B	28	CH1_CSA	0	#1 Chip select A
		θ #1 Chip Sciect B			O	#1 Only Scient A
29	GND	1 //4 NAICO	30	GND	_	//1 CDI CLI/
31	CH1_MISO	#1 MISO	32	CH1_SCLK	0	#1 SPI CLK
33	CH1_MOSI	O #1 MOSI	34	GND		
35	+5V	DC power	36	+5V		DC power
37	+12V	DC power	38	+12V		DC power
39	GND		40	GND		
		•			-	
Peripheral modules (2 x 14-pin) Status: Mandatory						
	Α	Dir		В	Dir	•
1	UART_TX	O Shared UART TX	2	UART_RX	I	Shared UART RX
3	GND		4	GND		
5	+5V	DC power	6	+5V		DC power
7	+12V	DC power	8	+12V		DC power
9	GND	De power	10	GND		DC power
		L Madula CDI CLI/				Master reset (setime leve)
11	SCLK	Module SPI CLK	12	NRESET		Master reset (active low)
13	MISO	Module MISO	14	NFAULT		Fault (active low)
15	GND		16	SSCL	ı	Shared I <sup>2</sup> C SCL
17	MOSI	│ Module MOSI	18	SSDA	I/O	Shared I <sup>2</sup> C SDA
19	CSA	I Module Chip select A	20	+3V3		DC power
21	CSB	Module Chip select B	22	+VAUX		Backup DC power
23	IRQ	O Module IRQ	24	SYNC	ı	Sync input
25	A1	∣ I²C A1	26	A0	i	I <sup>2</sup> C A0
27	A2	I <sup>2</sup> C A2	28	BOOT		Bootloader select
21	AZ	1 TC AZ	20	ВООТ	1	Bootioader Select
AUX PS module (2 x 8-pin) Status: Recommended						
	Α	Dir	J-Pill	В	Dir	Status. Necommended
1	PE	Dii	2	MBOOT	0	Master bootloader select
3	+12V	DC power	4	+12V		DC power
5	+5V	DC power	6	+5V		DC power
7		DC power		GND		DC power
	GND		8			B I BO
9	GND		10	+VAUX		Backup DC power
11	PWR_SSTART	│ AC soft-start	12	PWR_DIRECT	I	AC power on
13	SSCL	│ Shared I <sup>2</sup> C SCL	14	SSDA	I/O	
15	NFAULT	I/O Fault (active low)	16	+3V3		DC power
		Power source module (2	x 10-	·pin)		Status: Optional
	Α	Dir		В	Dir	
1	IN+	<ul> <li>Power positive input</li> </ul>	2	IN+	ı	Power positive input
3	IN+	Power positive input	4	IN+	ı	Power positive input
5	IN+	Power positive input	6	OUT+	0	Power positive output
7	OUT+	O Power positive output	8	OUT+	0	Power positive output
9	OUT+	<ul><li>Power positive output</li></ul>	10	OUT+	0	Power positive output
11	OUT-	O Power negative output	12	OUT-	0	Power negative output
13	OUT-	O Power negative output	14	OUT-	0	Power negative output
15	OUT-	O Power negative output		IN-	!	Power negative input
17	IN-	Power negative input	18	IN-	!	Power negative input
19	IN-	Power negative input	20	IN-		Power negative input

Notes:

If two or more modules have to be galvanically isolated (e.g. like in case of power modules with floating outputs) use appropriate isolators for control and data lines (e.g. Silabs Si86xx, Maxim MAX14850).

EEZ DIB (DIY Instrumentation Bus) pin mappings v1.0 (DRAFT2 06/12/2019)