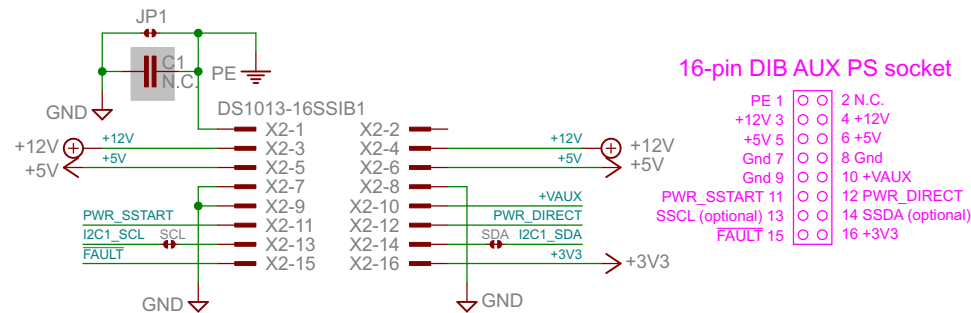
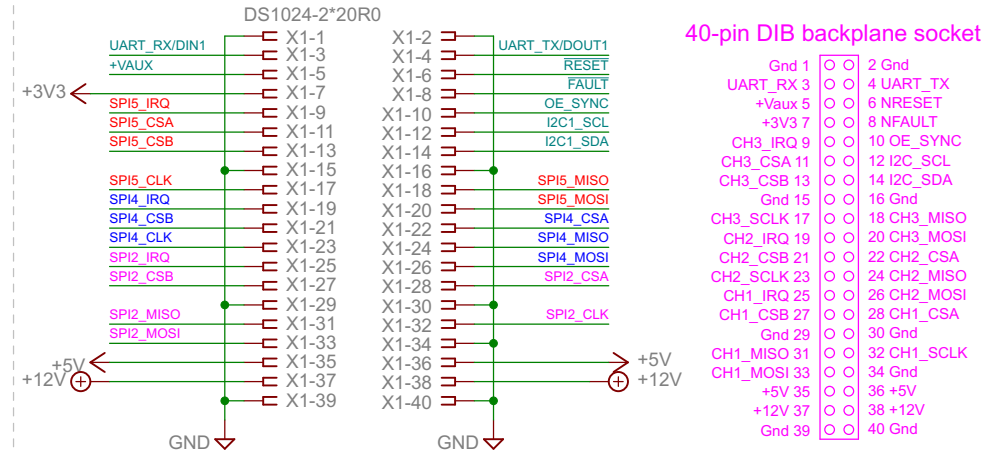


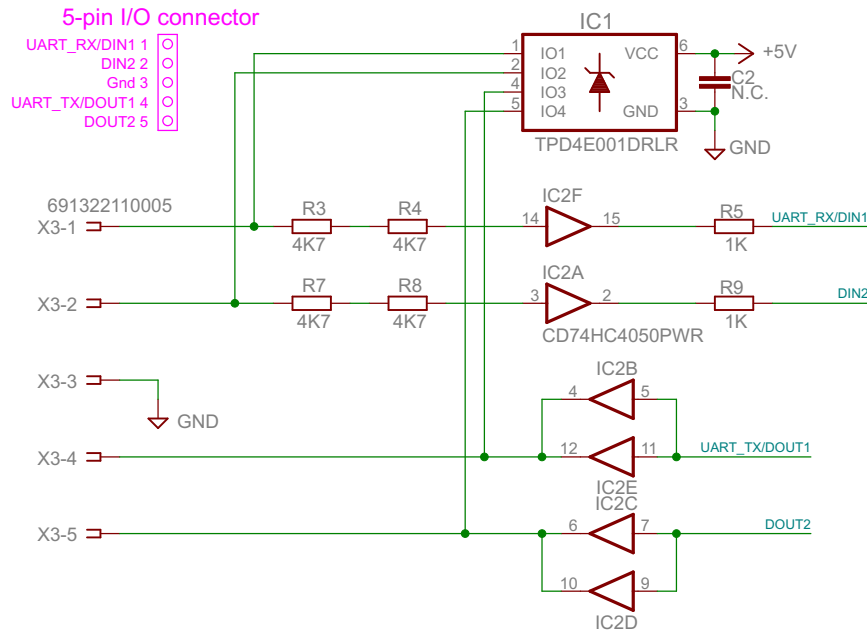
Input connector (power, soft start, fan controller)



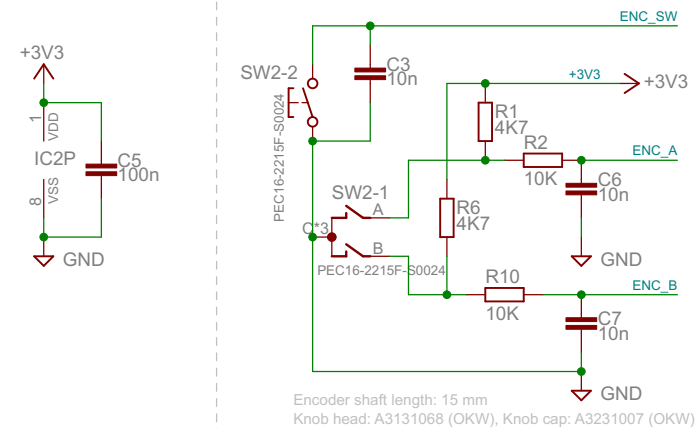
Output connector (3 x SPI modules)



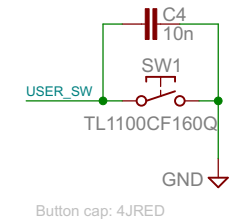
Digital I/O with protections



Encoder with switch



User switch



I/O connectors, User SW, Encoder, Digital I/O

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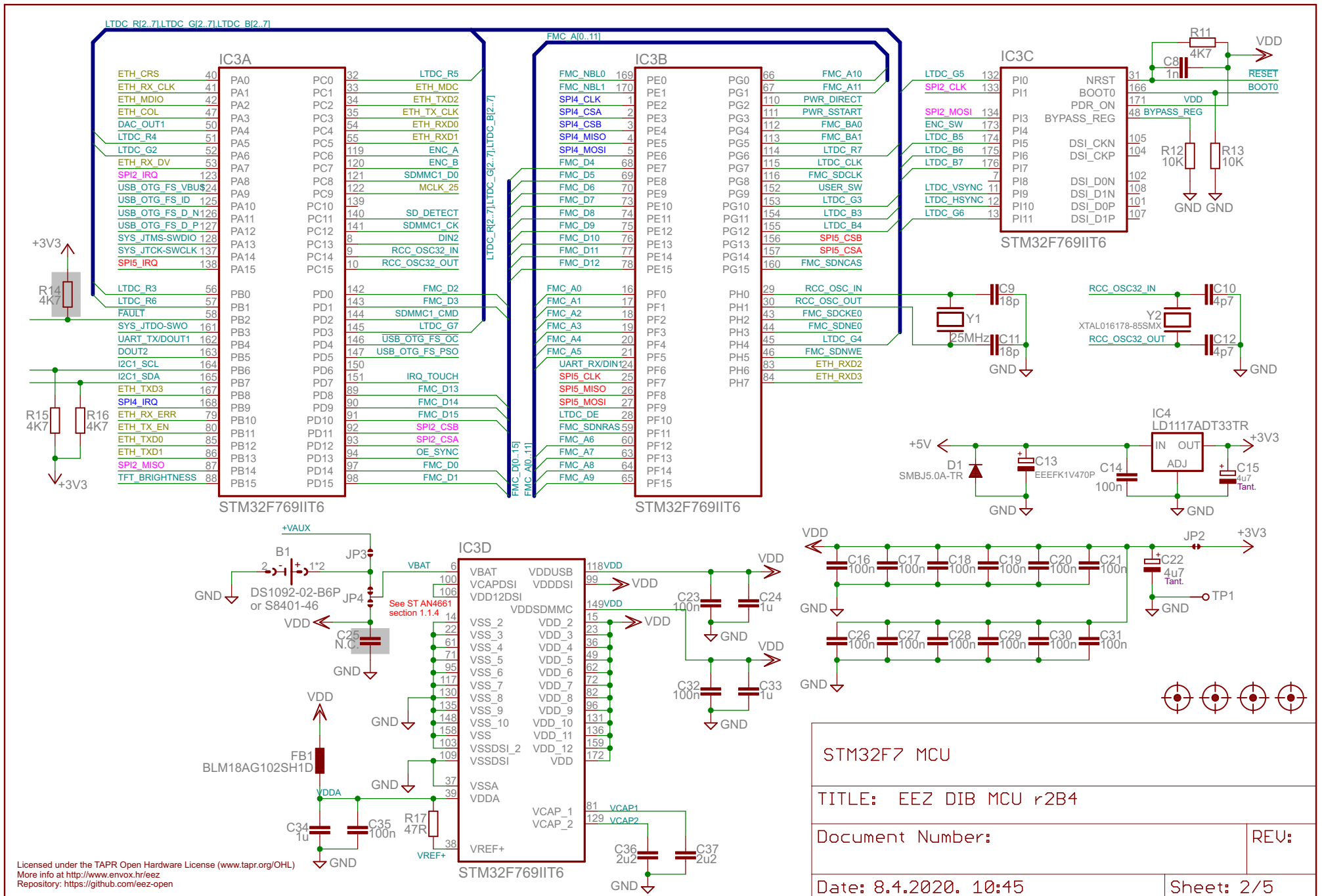


HR000002

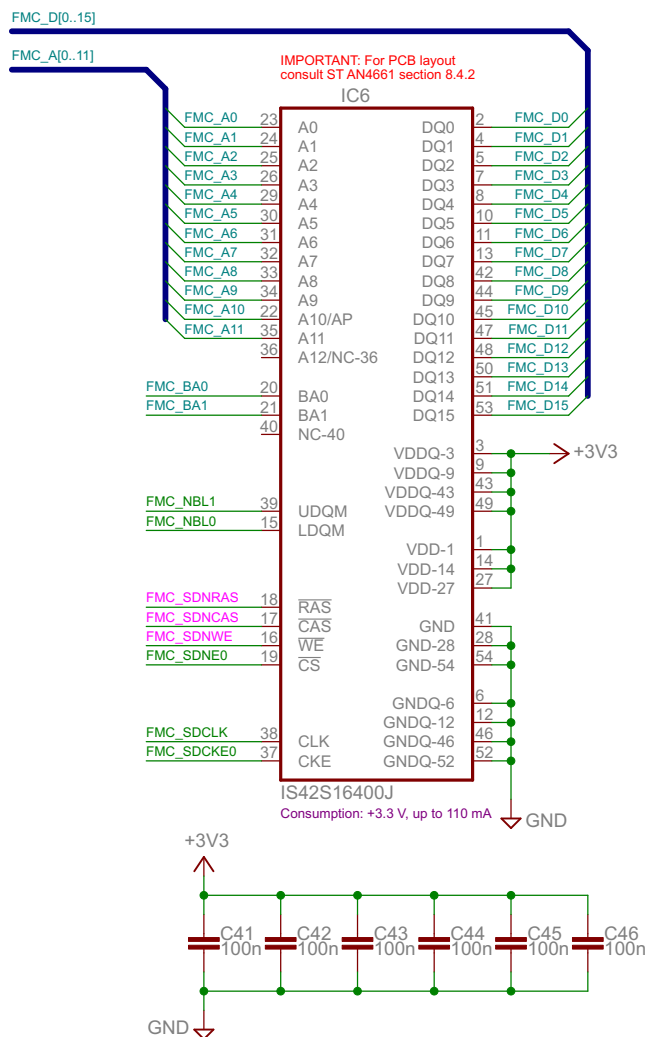
Licensed under the TAPR Open Hardware License (www.tapr.org/OHL)

More info at <http://www.envox.hr/eez>

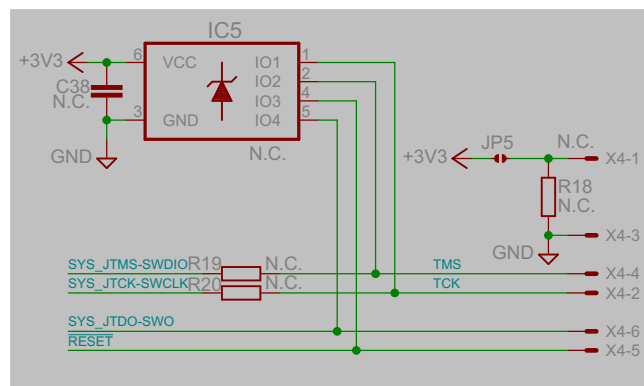
Repository: <https://github.com/eez-open>



SDRAM



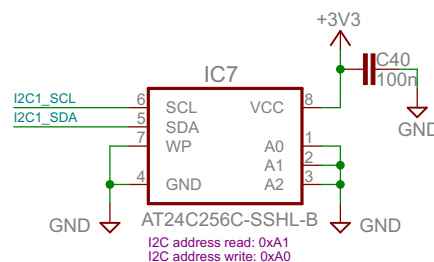
JTAG/SWD (optional)



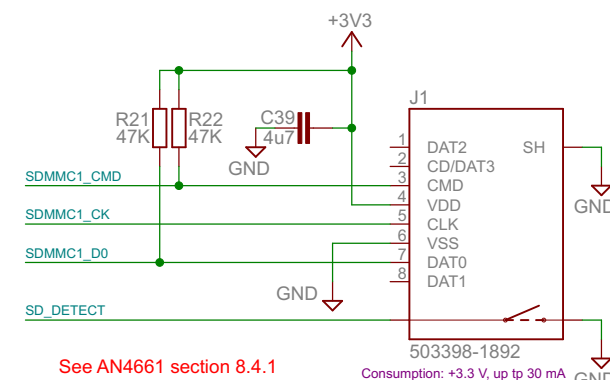
STM-32 board SWD header

Vdd target 1	○
SWCLK 2	○
Gnd 3	○
SWDIO 4	○
NRST 5	○
SWO 6	○

I2C EEPROM



Micro SD card socket



SDRAM, JTAG, I2C EEPROM, SD Card

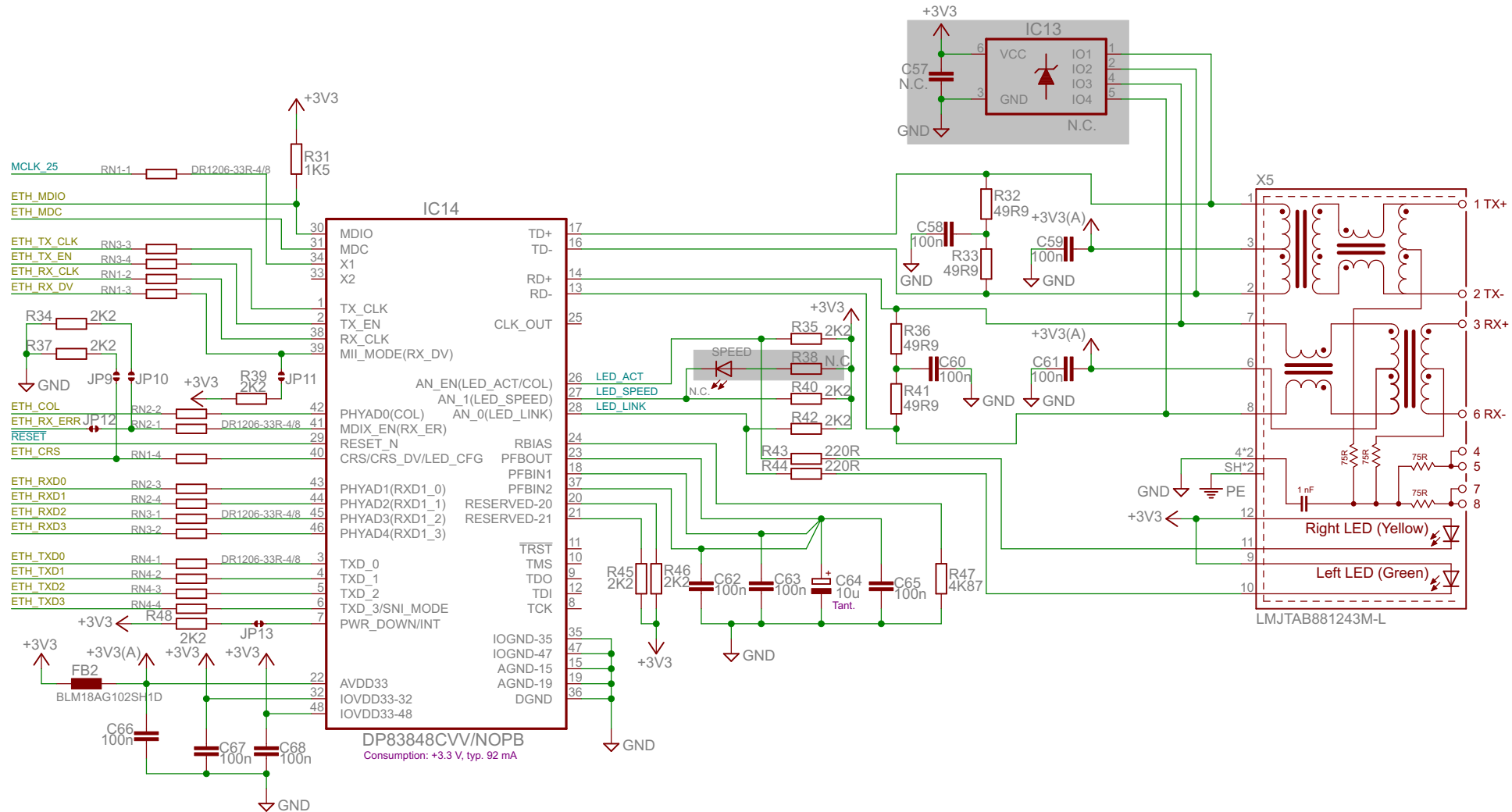
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Ethernet PHY

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