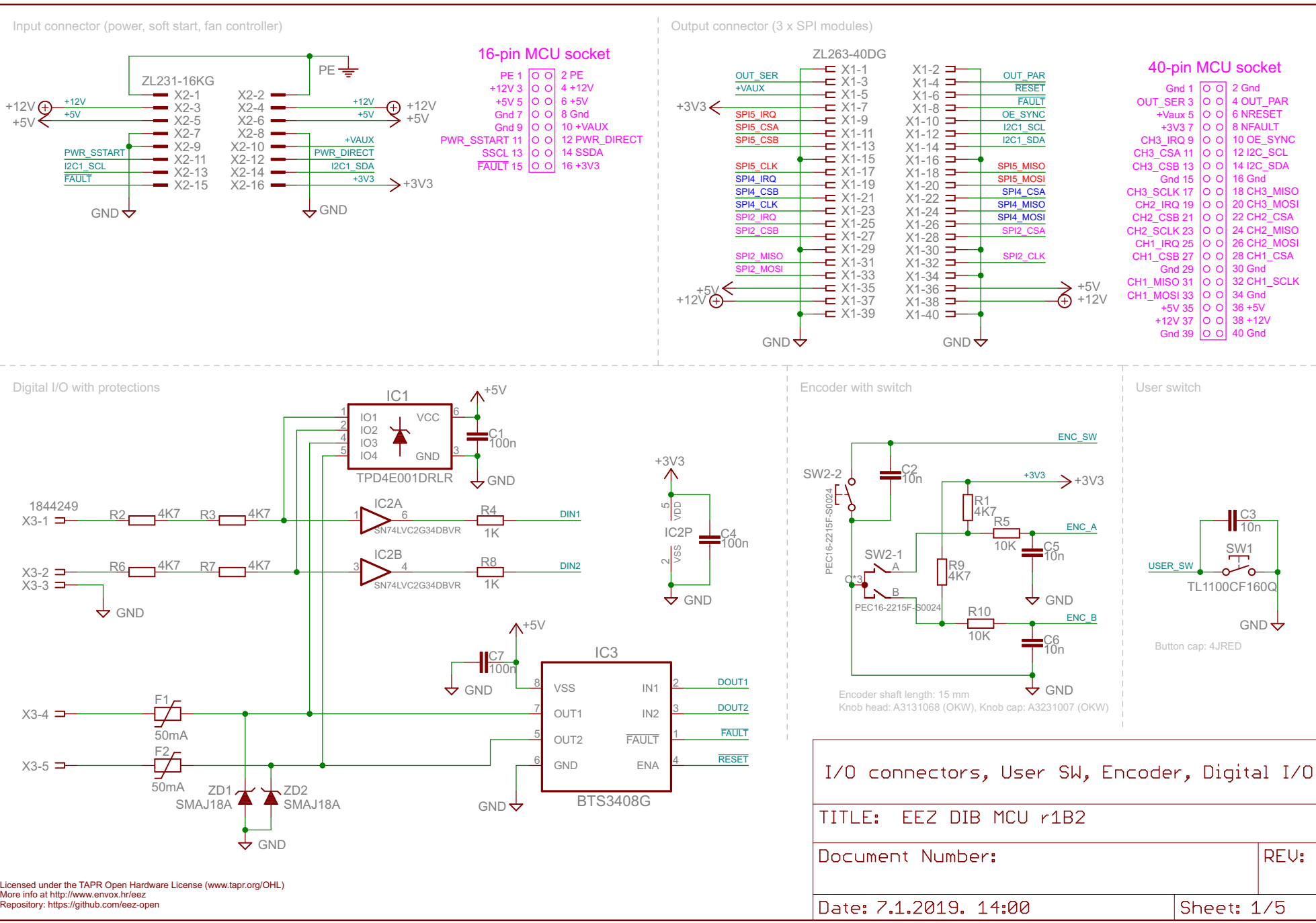

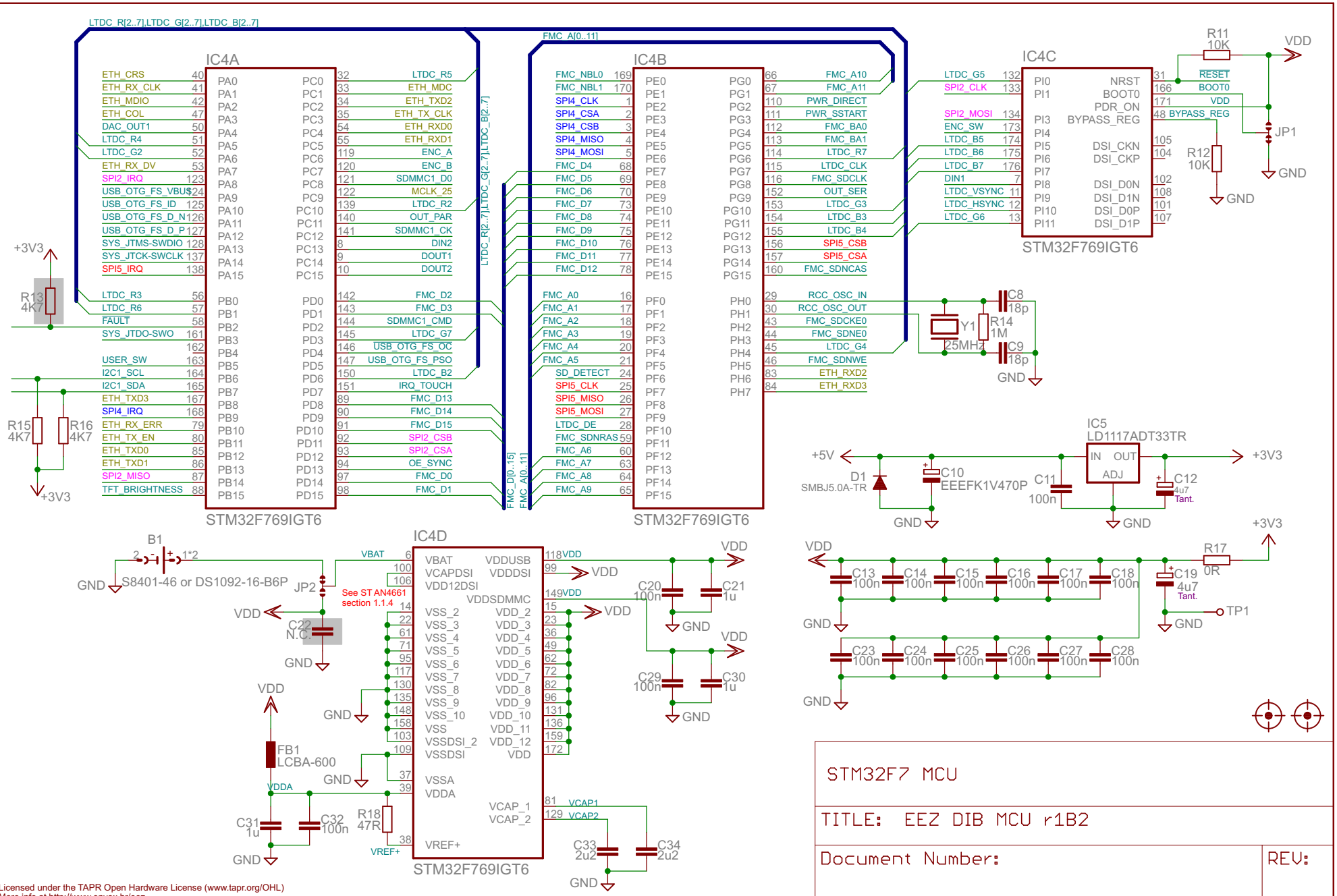
LTDC_R[2..7],LTDC_G[2..7],LTDC_B[2..7]
FMC_A[0..11]
IC4A
IC4B
IC4C
STM32F769IGT6
ETH_CRS
ETH_RX_CLK
ETH_MDIO
ETH_COL
DAC_OUT1
LTDC_R4
LTDC_G2
ETH_RX_DV
SPI2_IRQ
USB_OTG_FS_VBUS
USB_OTG_FS_ID
USB_OTG_FS_D_N
USB_OTG_FS_D_P
SYS_JTMS-SWDIO
SYS_JTCK-SWCLK
SPI5_IRQ
LTDC_R3
LTDC_R6
FAULT
SYS_JTDO-SWO
USER_SW
I2C1_SCL
I2C1_SDA
ETH_TXD3
SPI4_IRQ
ETH_RX_ERR
ETH_TX_EN
ETH_TXD0
ETH_TXD1
SPI2_MISO
TFT_BRIGHTNESS
LTDC_R5
ETH_MDC
ETH_TXD2
ETH_TX_CLK
ETH_RXD0
ETH_RXD1
ENC_A
ENC_B
SDMMC1_D0
MCLK_25
LTDC_R2
OUT_PAR
SDMMC1_CK
DIN2
DOUT1
DOUT2
FMC_D2
FMC_D3
SDMMC1_CMD
LTDC_G7
USB_OTG_FS_OC
USB_OTG_FS_PSO
LTDC_B2
IRQ_TOUCH
FMC_D13
FMC_D14
FMC_D15
SPI2_CSB
SPI2_CSA
OE_SYNC
FMC_D0
FMC_D1
FMC_NBL0
FMC_NBL1
SPI4_CLK
SPI4_CSA
SPI4_CSB
SPI4_MISO
SPI4_MOSI
FMC_D4
FMC_D5
FMC_D6
FMC_D7
FMC_D8
FMC_D9
FMC_D10
FMC_D11
FMC_D12
FMC_A0
FMC_A1
FMC_A2
FMC_A3
FMC_A4
FMC_A5
SD_DETECT
SPI5_CLK
SPI5_MISO
SPI5_MOSI
LTDC_DE
FMC_SDNRAS
FMC_A6
FMC_A7
FMC_A8
FMC_A9
FMC_A10
FMC_A11
PWR_DIRECT
PWR_SSTART
FMC_BA0
FMC_BA1
LTDC_R7
LTDC_CLK
FMC_SDCLK
OUT_SER
LTDC_G3
LTDC_B3
LTDC_B4
SPI5_CSB
SPI5_CSA
FMC_SDNCAS
RCC_OSC_IN
RCC_OSC_OUT
FMC_SDCKE0
FMC_SDNE0
LTDC_G4
FMC_SDNWE
ETH_RXD2
ETH_RXD3
LTDC_G5
SPI2_CLK
SPI2_MOSI
ENC_SW
LTDC_B5
LTDC_B6
LTDC_B7
DIN1
LTDC_VSYNC
LTDC_HSYNC
LTDC_G6
NRST
BOOT0
PDR_ON
BYPASS_REG
DSI_CKN
DSI_CKP
DSI_D0N
DSI_D1N
DSI_D0P
DSI_D1P
RESET
VDD
R11 10K
R12 10K
JP1
GND
+3V3
R13 4K7
R14 1M
R15 4K7
R16 4K7
Y1 25MHz
C8 18p
C9 18p
IC5 LD1117ADT33TR
IN
OUT
ADJ
+5V
D1 SMBJ5.0A-TR
C10 EEEFK1V470P
C11 100n
C12 4u7 Tant.
B1
S8401-46 or DS1092-16-B6P
JP2
C22 N.C.
See ST AN4661 section 1.1.4
IC4D
VBAT
VCAPDSI
VDD12DSI
VSS_2
VSS_3
VSS_4
VSS_5
VSS_6
VSS_7
VSS_8
VSS_9
VSS_10
VSS
VSSDSI_2
VSSDSI
VSSA
VDDA
VREF+
VDDUSB
VDDDSI
VDDSDMMC
VDD_2
VDD_3
VDD_4
VDD_5
VDD_6
VDD_7
VDD_8
VDD_9
VDD_10
VDD_11
VDD_12
VDD
VCAP_1
VCAP_2
FB1 LCBA-600
C31 1u
C32 100n
R18 47R
C33 2u2
C34 2u2
C20 100n
C21 1u
C29 100n
C30 1u
C13 100n
C14 100n
C15 100n
C16 100n
C17 100n
C18 100n
C19 4u7 Tant.
R17 0R
TP1
C23 100n
C24 100n
C25 100n
C26 100n
C27 100n
C28 100n
STM32F7 MCU
TITLE: EEZ DIB MCU r1B2
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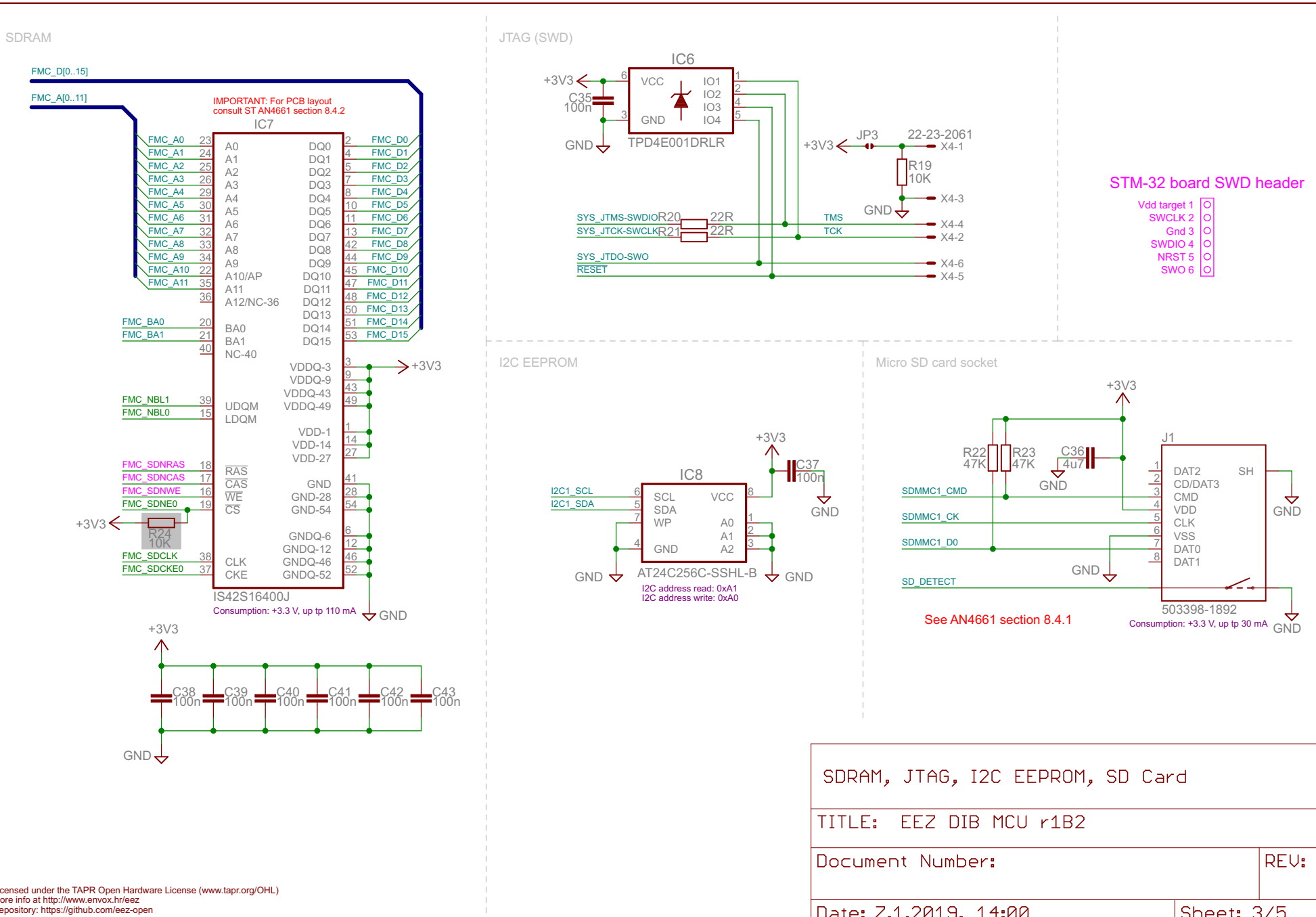
Repository: https://github.com/eez-open


SDRAM
FMC_D[0..15]
FMC_A[0..11]
IMPORTANT: For PCB layout
consult ST AN4661 section 8.4.2
IC7
FMC_A0 23
FMC_A1 24
FMC_A2 25
FMC_A3 26
FMC_A4 29
FMC_A5 30
FMC_A6 31
FMC_A7 32
FMC_A8 33
FMC_A9 34
FMC_A10 22
FMC_A11 35
36
A0
A1
A2
A3
A4
A5
A6
A7
A8
A9
A10/AP
A11
A12/NC-36
FMC_BA0 20
FMC_BA1 21
40
BA0
BA1
NC-40
FMC_NBL1 39
FMC_NBL0 15
UDQM
LDQM
FMC_SDNRAS 18
FMC_SDNCAS 17
FMC_SDNWE 16
FMC_SDNE0 19
RAS
CAS
WE
CS
+3V3
R24
10K
FMC_SDCLK 38
FMC_SDCKE0 37
CLK
CKE
DQ0 2 FMC_D0
DQ1 4 FMC_D1
DQ2 5 FMC_D2
DQ3 7 FMC_D3
DQ4 8 FMC_D4
DQ5 10 FMC_D5
DQ6 11 FMC_D6
DQ7 13 FMC_D7
DQ8 42 FMC_D8
DQ9 44 FMC_D9
DQ10 45 FMC_D10
DQ11 47 FMC_D11
DQ12 48 FMC_D12
DQ13 50 FMC_D13
DQ14 51 FMC_D14
DQ15 53 FMC_D15
VDDQ-3 3
VDDQ-9 9
VDDQ-43 43
VDDQ-49 49
+3V3
VDD-1 1
VDD-14 14
VDD-27 27
GND 41
GND-28 28
GND-54 54
GNDQ-6 6
GNDQ-12 12
GNDQ-46 46
GNDQ-52 52
GND
IS42S16400J
Consumption: +3.3 V, up tp 110 mA
+3V3
C38 100n
C39 100n
C40 100n
C41 100n
C42 100n
C43 100n
GND
JTAG (SWD)
IC6
+3V3
C35
100n
GND
VCC
GND
IO1
IO2
IO3
IO4
TPD4E001DRLR
+3V3
JP3
22-23-2061
X4-1
R19
10K
X4-3
GND
SYS_JTMS-SWDIO R20 22R
SYS_JTCK-SWCLK R21 22R
TMS
TCK
X4-4
X4-2
SYS_JTDO-SWO
RESET
X4-6
X4-5
STM-32 board SWD header
Vdd target 1
SWCLK 2
Gnd 3
SWDIO 4
NRST 5
SWO 6
I2C EEPROM
+3V3
C37
100n
GND
IC8
I2C1_SCL 6
I2C1_SDA 5
SCL
SDA
WP
GND 4
VCC 8
A0 1
A1 2
A2 3
AT24C256C-SSHL-B
GND
GND
I2C address read: 0xA1
I2C address write: 0xA0
Micro SD card socket
+3V3
R22
47K
R23
47K
C36
4u7
GND
J1
SDMMC1_CMD
SDMMC1_CK
SDMMC1_D0
SD_DETECT
1 DAT2
2 CD/DAT3
3 CMD
4 VDD
5 CLK
6 VSS
7 DAT0
8 DAT1
SH
GND
GND
GND
503398-1892
Consumption: +3.3 V, up tp 30 mA
See AN4661 section 8.4.1
SDRAM, JTAG, I2C EEPROM, SD Card
TITLE: EEZ DIB MCU r1B2
Document Number:
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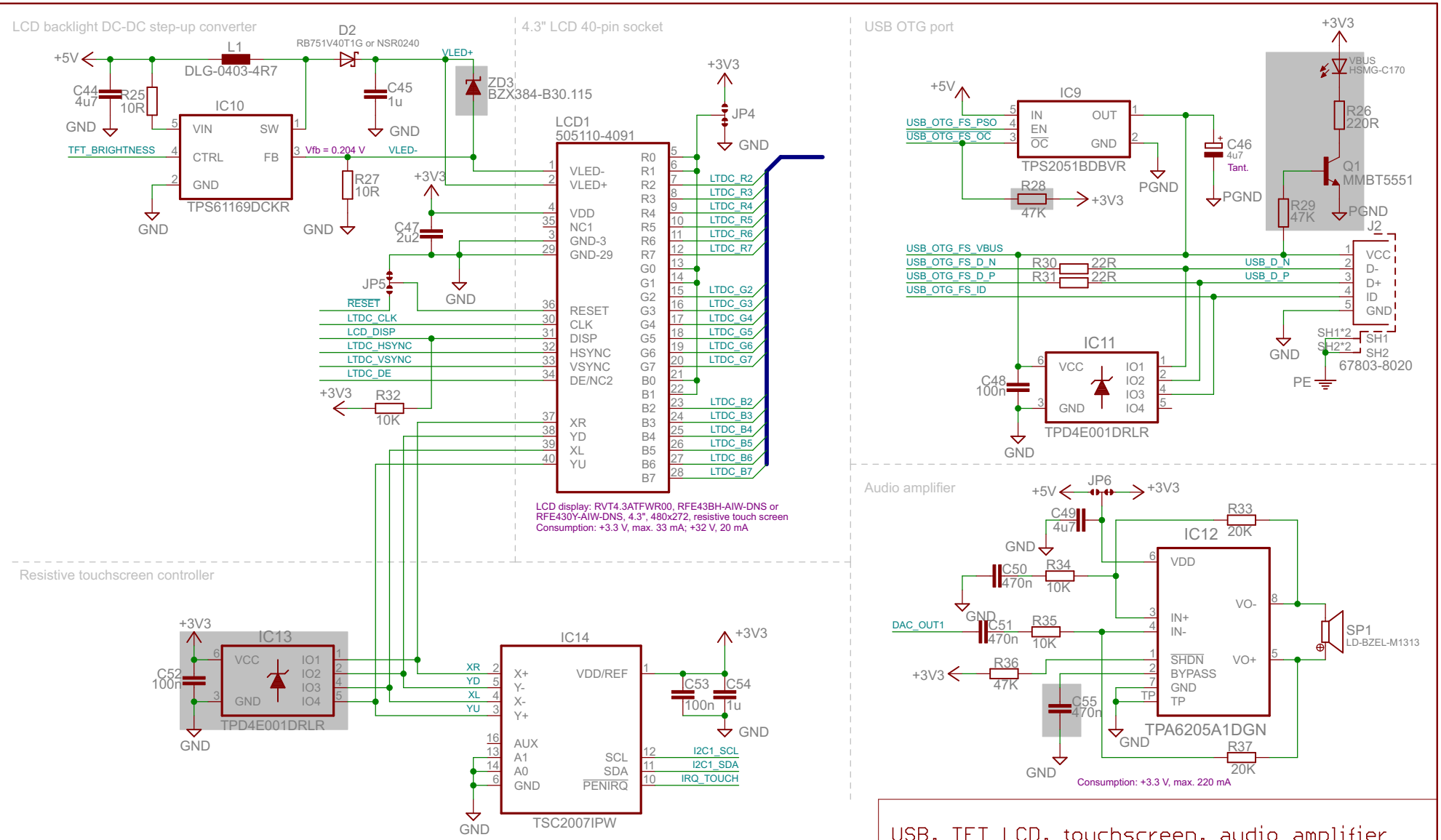

USB, TFT LCD, touchscreen, audio amplifier

TITLE: EEZ DIB MCU r1B2

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