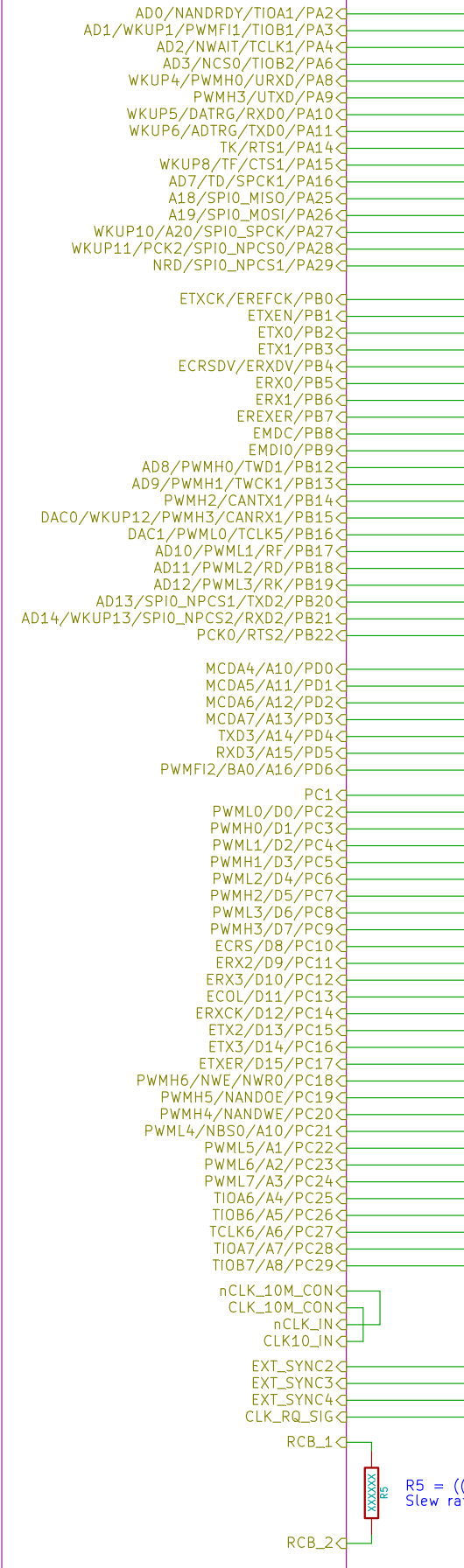
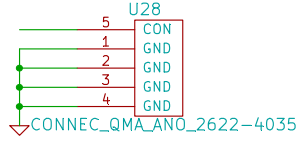
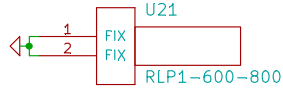
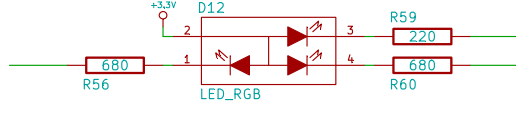
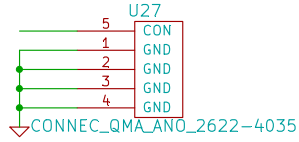
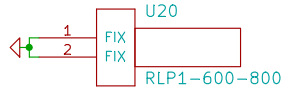
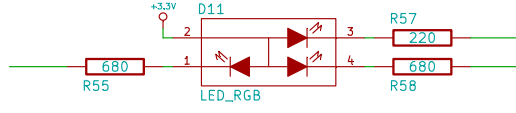
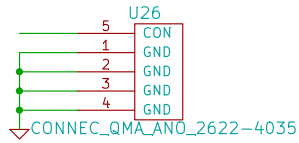
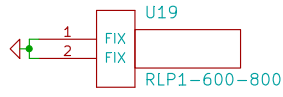
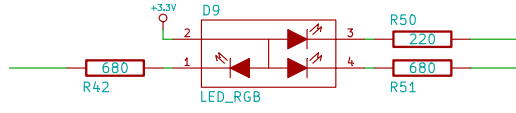
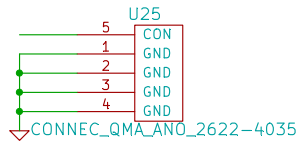
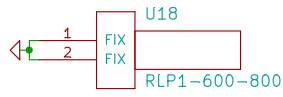
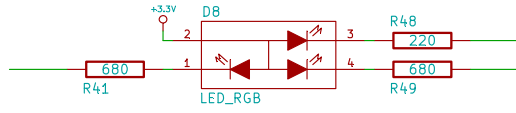
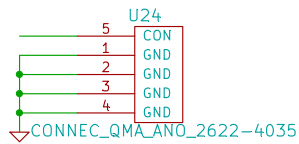
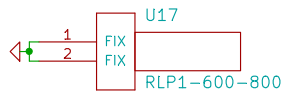
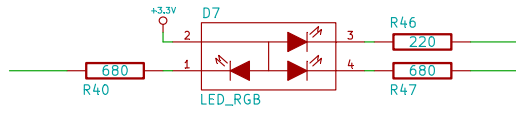
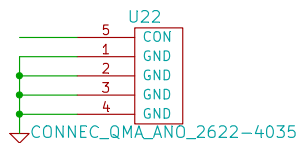
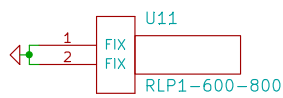
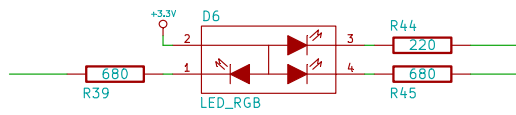


Sheet: template_core_easyphi



$R5 = ((I_{triplow} * 0.03) + 4.7mV) / 37\mu A$
Slew rate set to max 3.3V/ms



ABOUT THE POWER SUPPLY SYMBOLS

+12V



Is the 12V filtered coming from the backplane (MAX 4A)
set R5 according to the max current you'll have

+5V



Is the 5V filtered coming from the backplane (MAX 0.5A)

+3.3V



Is a 3.3V generated from this 5V by a stepdown
used by template_core chips

+3.3VADC



Is a 3.3V generated from the 5V by a LDO
used by the uC ADC

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Sheet: /

Title: Easy-phi Template

Size: A3 Date: 22 oct 2013

Rev: 1.0

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