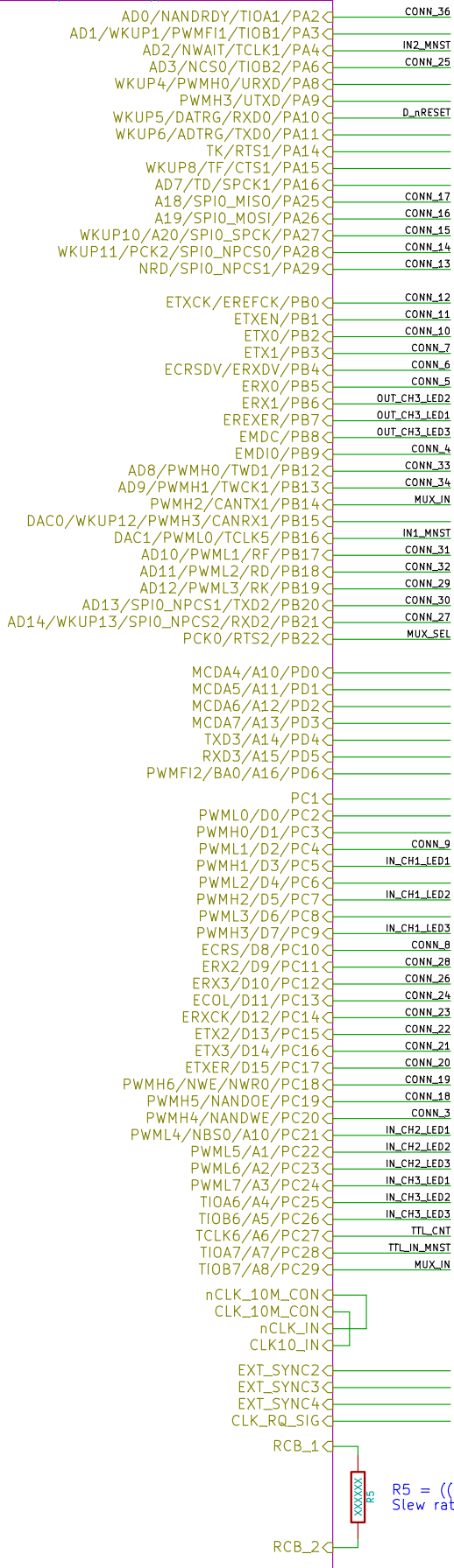


Sheet: template\_core\_easyphi

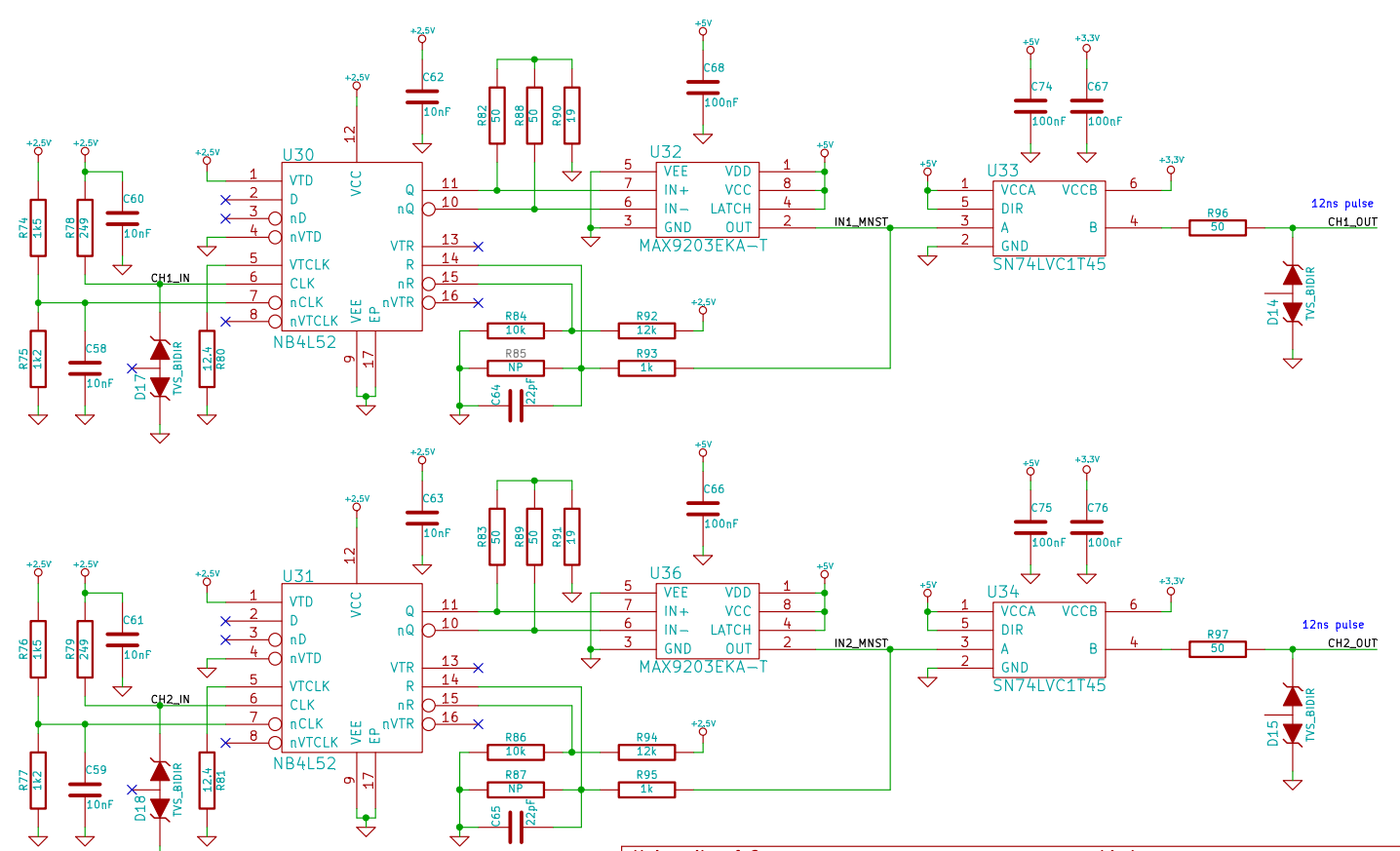
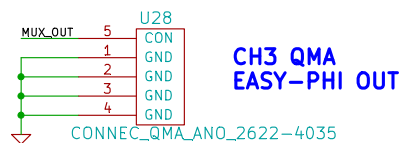
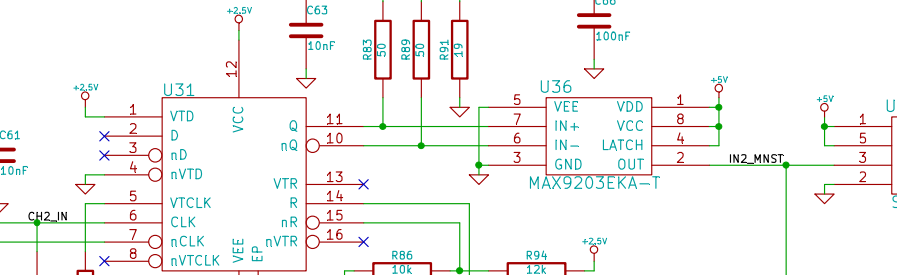
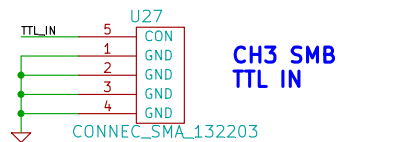
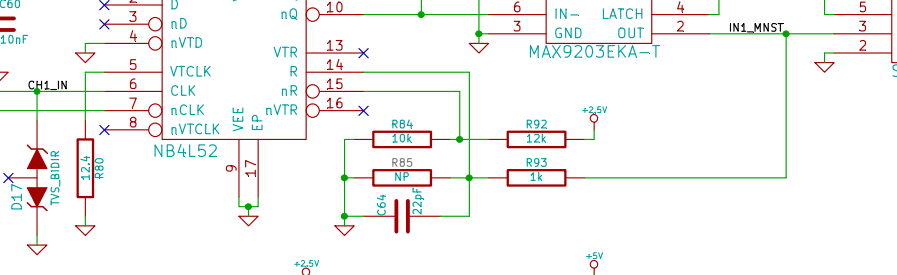
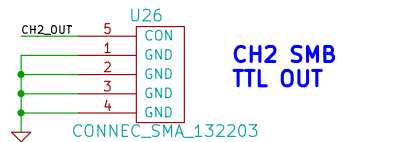
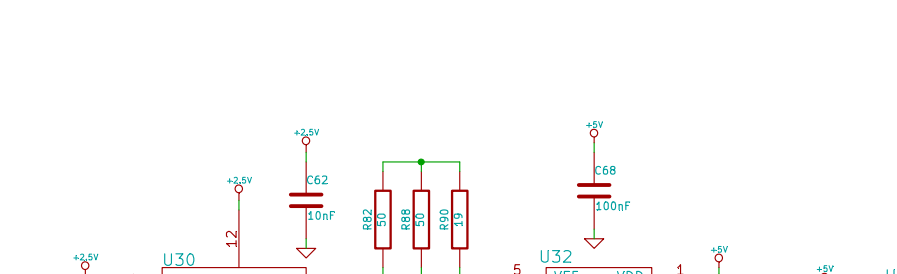
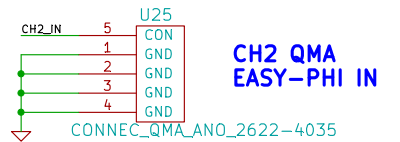
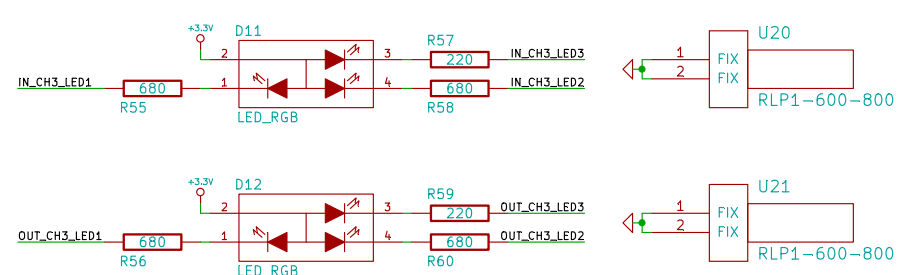
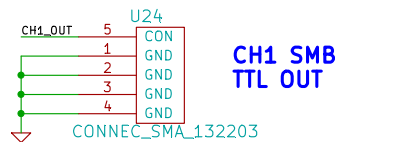
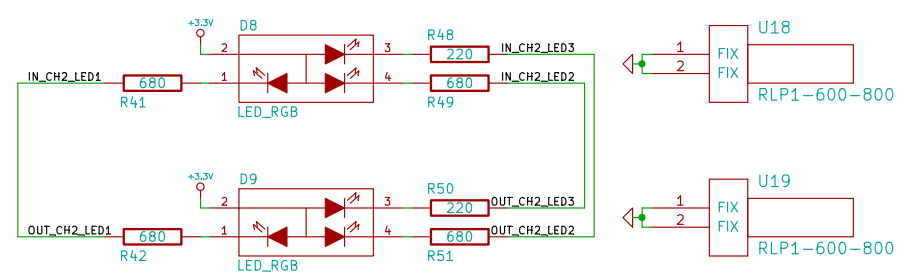
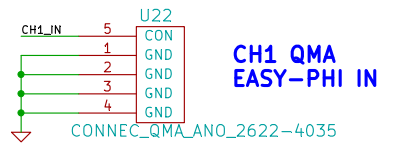
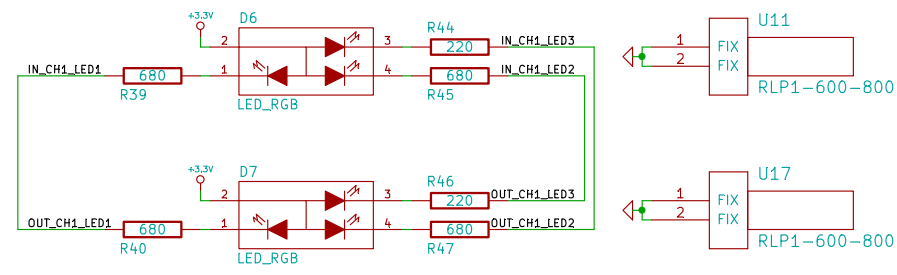
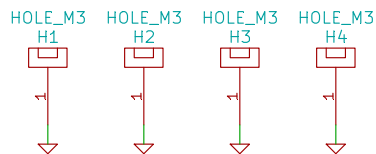
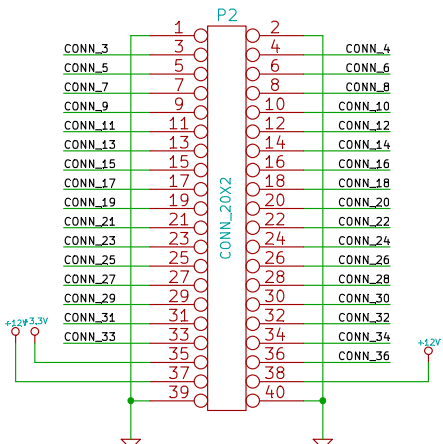
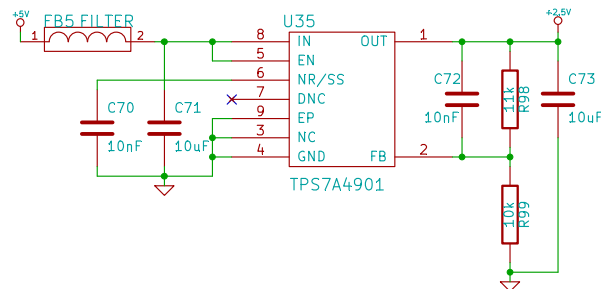
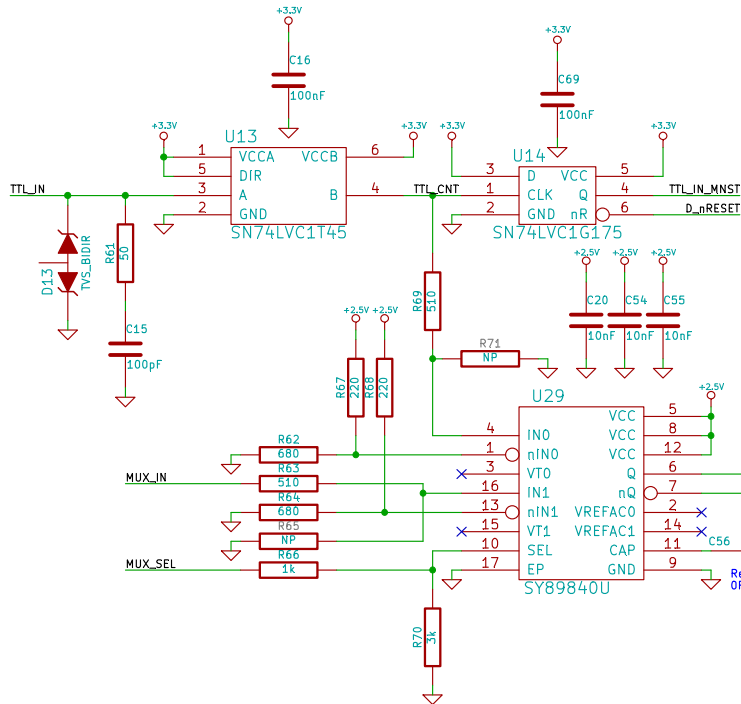


$R5 = ((I_{triplo} * 0.03) + 4.7mV) / 37\mu A$   
Slew rate set to max 3.3V/ms

### ABOUT THE POWER SUPPLY SYMBOLS

	is the 12V filtered coming from the backplane (MAX 4A) set R5 according to the max current you'll have
	is the 5V filtered coming from the backplane (MAX 0.5A)
	is a 3.3V generated from this 5V by a stepdown used by template_core chips
	is a 3.3V generated from the 5V by a LDO used by the uC ADC

change for v1  
- TTL\_CNT on PC27 (D\_nRESET)  
- D\_nRESET on PA10



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File: top_level_easyphi.sch			
Sheet: /			
Title: Low cost dual 10MHz Counters			
Size: A3		Date: 17 apr 2014	Rev: 1.0
KiCad E.D.A. eeschema (2013-07-07 BZR 4022)-stable			Id: 1/2

