

+3.3VADC is a 3.3V generated from the 5V by a LDO used by the uC ADC

RCB_2<

File: template_core_easyphi.sch

This documentation describes Open Hardware and is licensed under the CERN OHL V1.2

You may redistribute and modify this documentation under the terms of the CERN OHL V1.2 (http://ohwr.org/cernohl). This documentation is distributed WiTHOUT ANY EXPRESS OR IMPLIED WARRANTY, INCLUDING OF MERCHANTABILITY, SATISFACTORY QUALITY AND FITNESS FOR A PARTICULAR PURPOSE. Please see the CERN OHL V1.2 for applicable conditions

University of Geneva www.easy-phi.ch
File: top_level_easyphi.sch

Sheet: /
Title: Easy-phi Module Template

 Size: A3
 Date: 23 oct 2013
 Rev: 1.0

 KiCad E.D.A. eeschema (2013-07-07 BZR 4022)-stable
 Id: 1/2

