

Something about the CPEN 311 Lab 4

Something about the SOF file

It is located here:

[./rtl/output_files/rc4.sof](#)

Something about the status

All parts complete, including:

1. Task 1 - memory initialization
2. Task 2a/2b - Pseudorandom sequence creation and decryption with secret key
3. Task 3 - FSM to attempt to brute force all possible keys
4. Bonus - Multi-core initialization

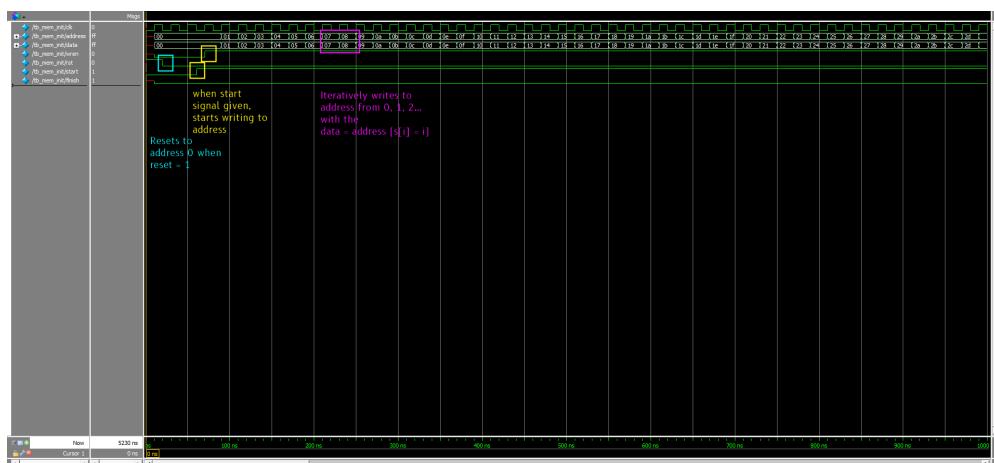
Complete with simulations and SignalTaps

Annotated Simulation screenshots

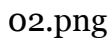
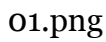
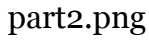
[./docs](#)

They are partitioned by task. Inside, you will find simulation screenshots and SignalTap screenshots.

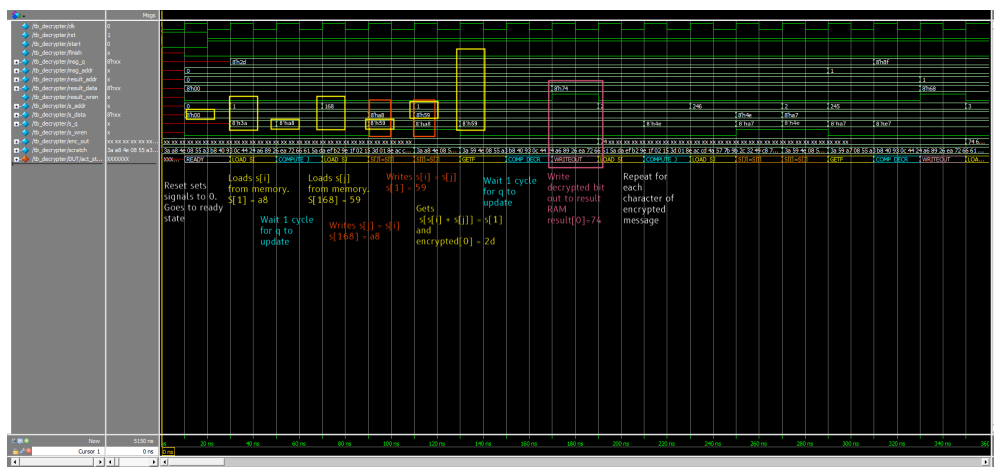
Task 1 Simulations



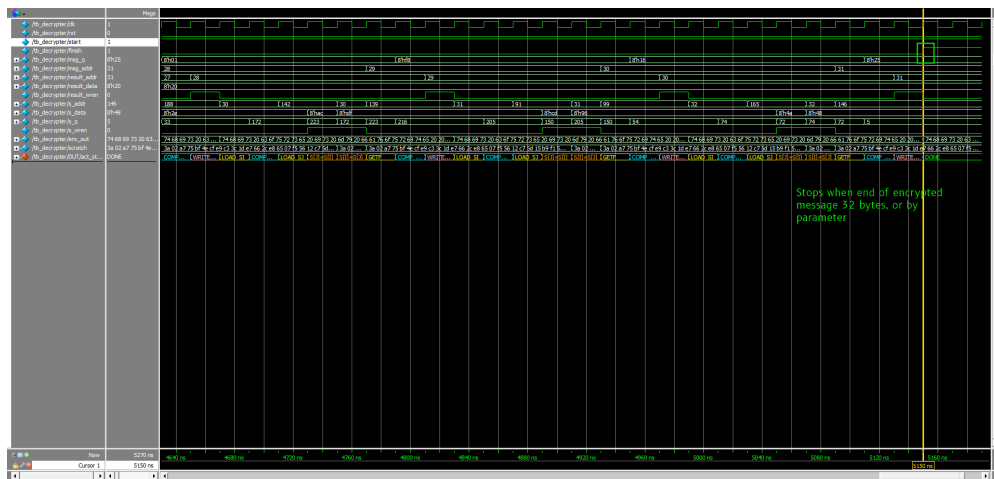
part1.png



Task 2b Simulations

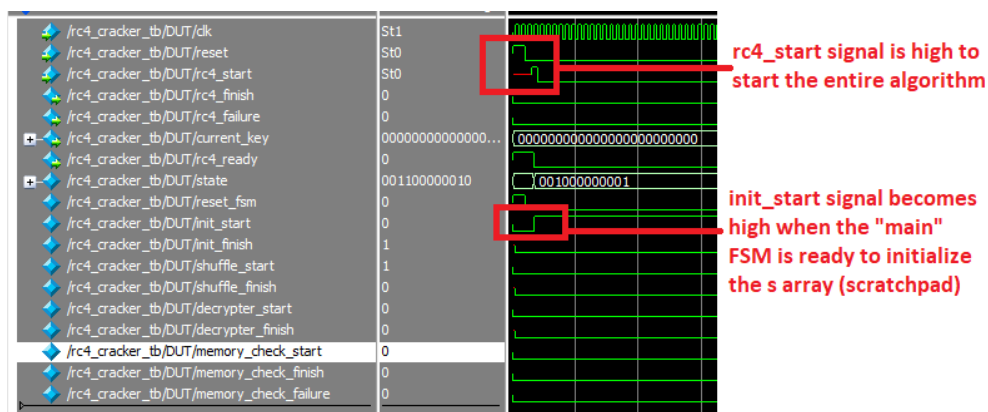


01.png

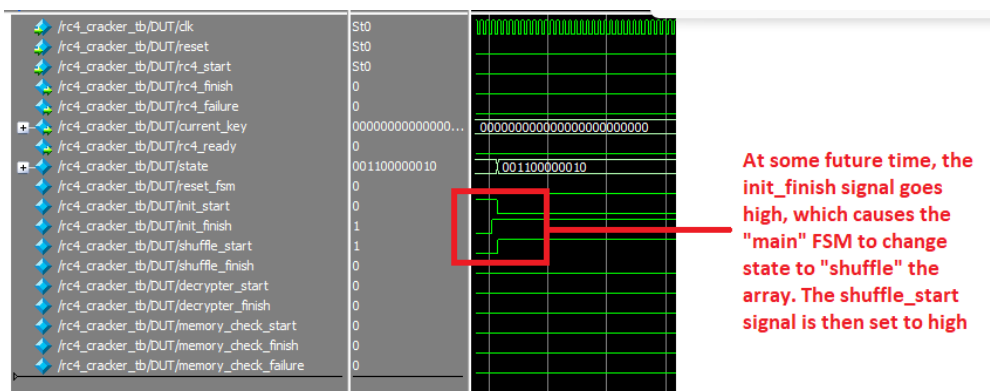


02.png

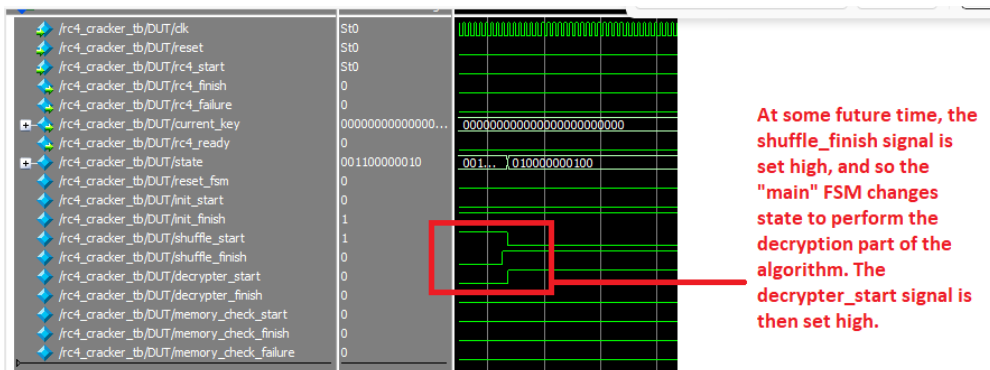
Task 3 Simulations



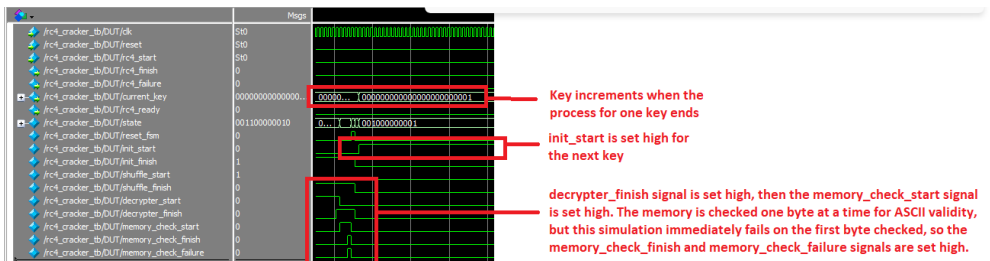
01.png



02.png



03.png



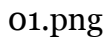
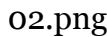
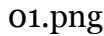
04.png

Annotated SignalTap screenshots

[./docs](#)

They are partitioned by task. Inside, you will find simulation screenshots and SignalTap screenshots.

Task 1 SignalTap



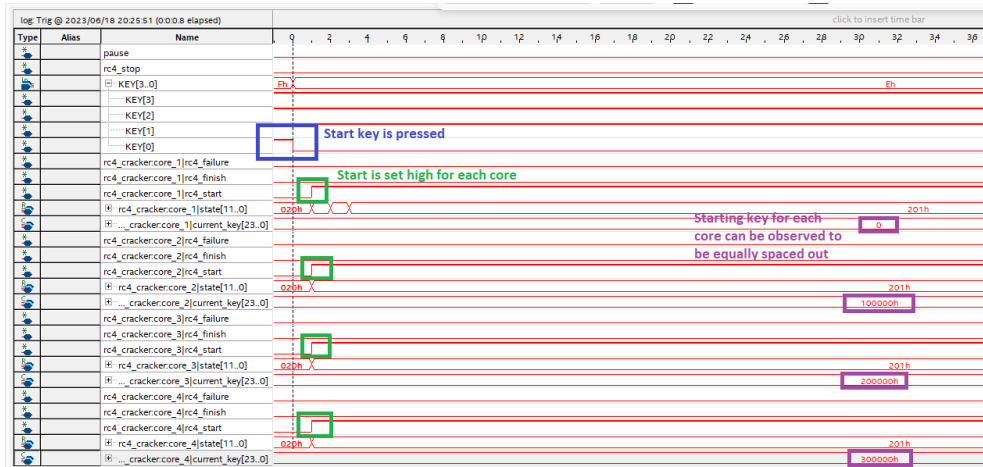
02.png

Task 2b SignalTap

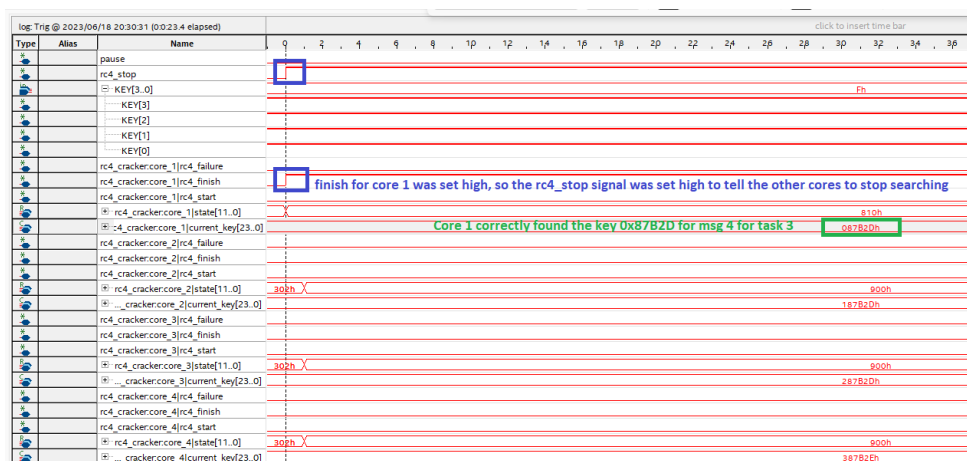
01.png

02.png

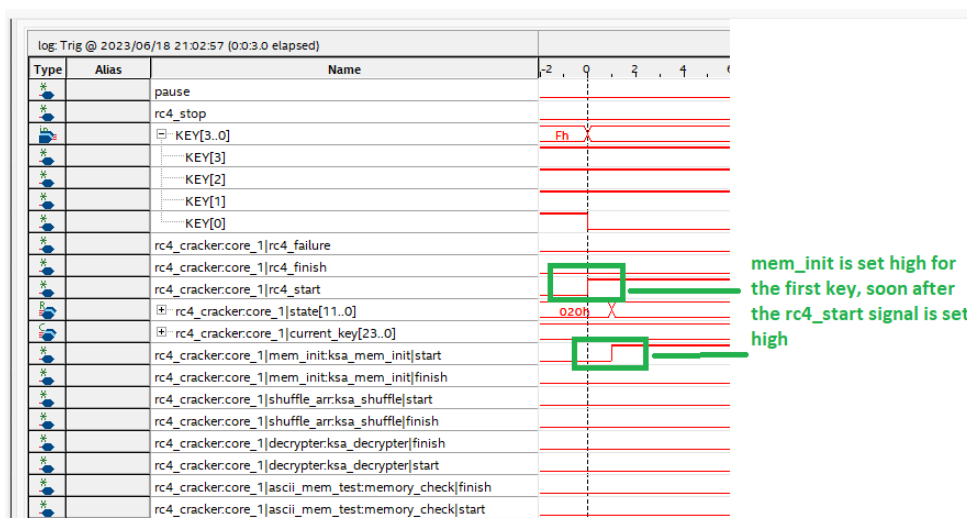
Task 3 SignalTap



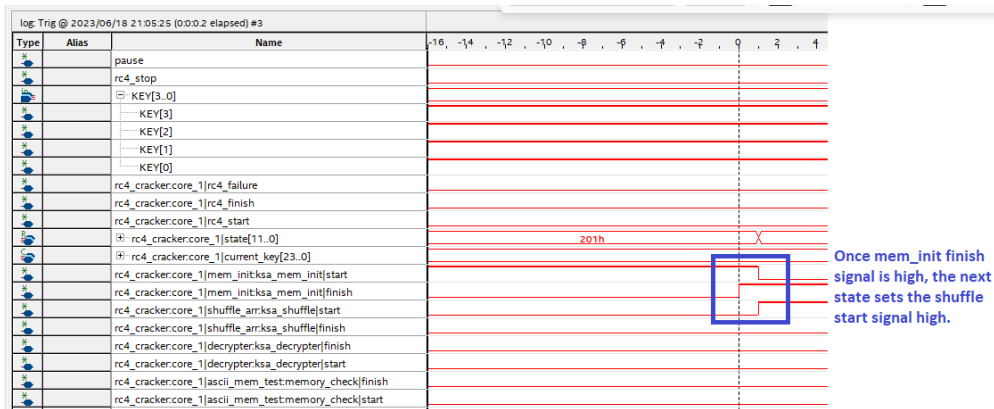
01.png



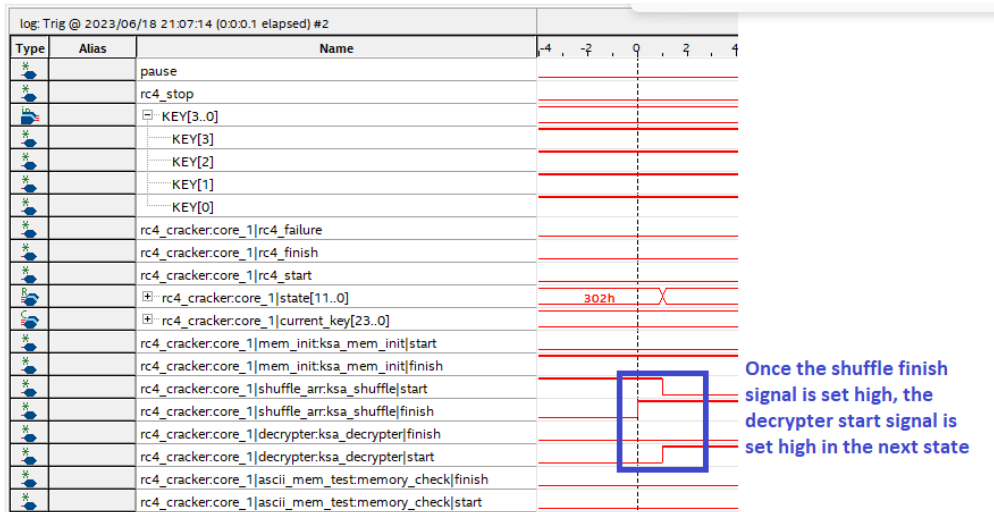
02.png



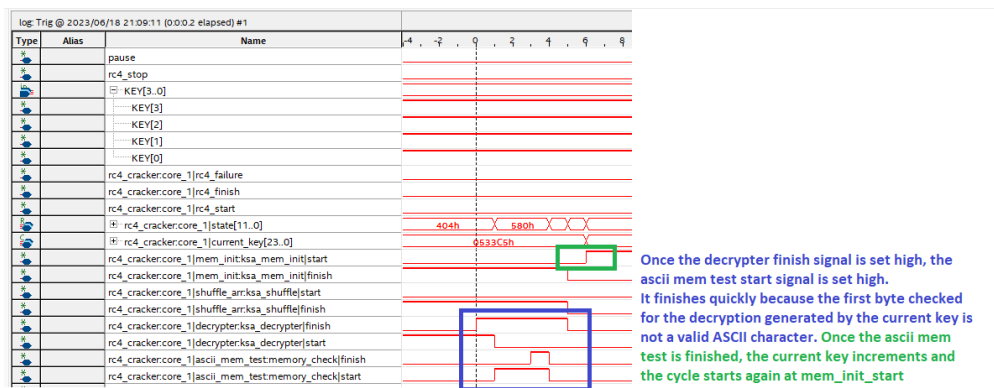
03.png



04.png



05.png



06.png

Additional Information

None