;<16 BIT MULTIPLICATION> **LHLD** 0003 SPHL **LHLD** 0005 XCHG **MVI A,** 00 **LXI H,** 0000 **LXI B,** 0000 LOOP1:DAD SP JNC LOOP2 INX B LOOP2:DCX D CMP E JNC SKIP CMP D SKIP:JNZ LOOP1 **SHLD** 0008 MOV H,B MOV L, C **SHLD** 0010 STOP:HLT