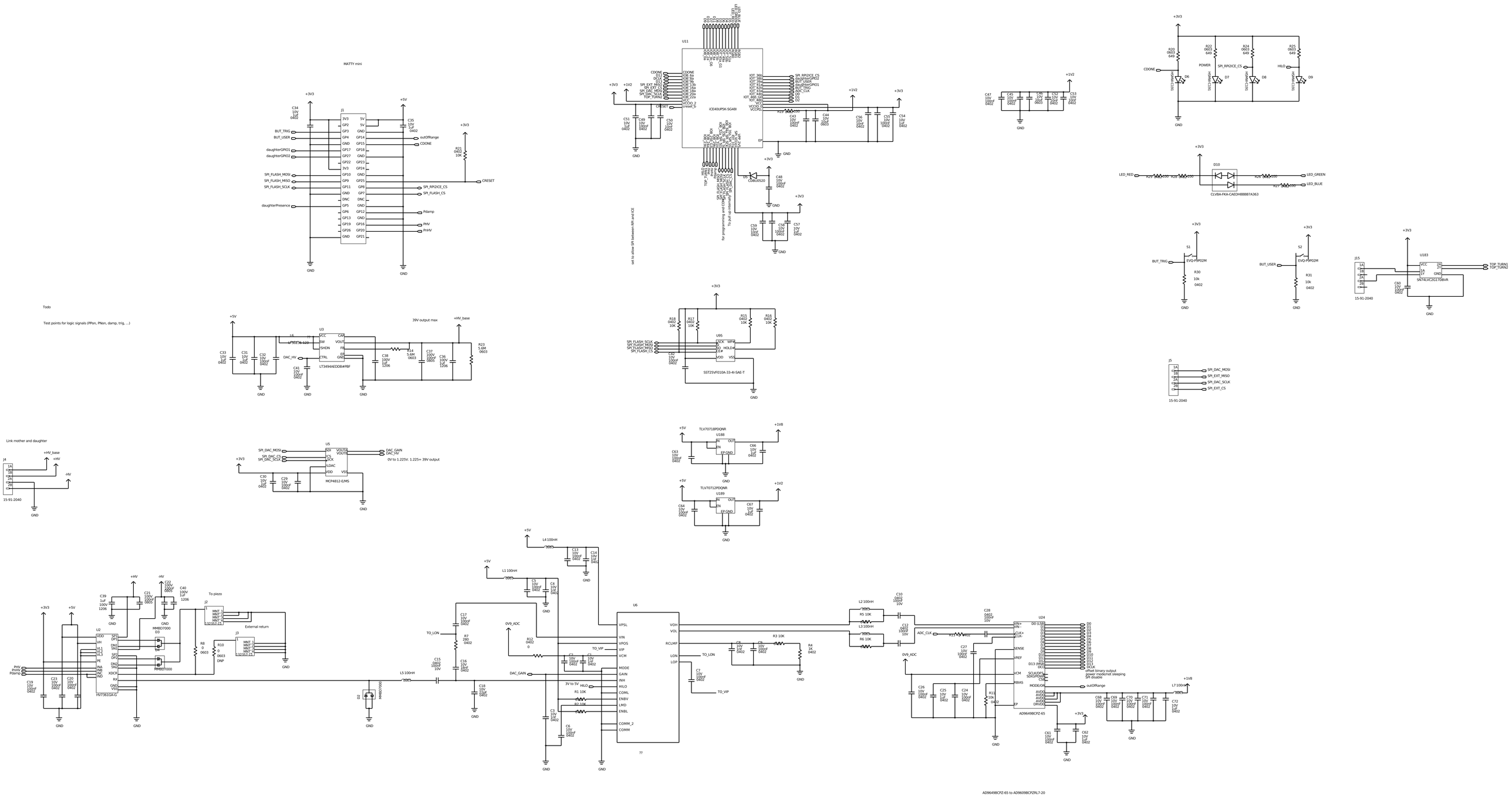
MATTY mini
J1
BUT_TRIG
BUT_USER
daughterGPIO1
daughterGPIO2
SPI_FLASH_MOSI
SPI_FLASH_MISO
SPI_FLASH_SCLK
daughterPresence
outOfRange
CDONE
CRESET
SPI_RP2ICE_CS
SPI_FLASH_CS
Pdamp
PHV
PnHV
U11
ICE40UP5K-SG48I
set to allow SPI between RPi and ICE
for programming and CRESET
To pull up internally
Todo
Test points for logic signals (PRon, PNon, damp, trig, ...)
U3
LT3494AEDDB#PBF
39V output max
+HV_base
U5
MCP4812-E/MS
DAC_GAIN
DAC_HV
0V to 1.225V. 1.225= 39V output
U95
S5T25VF010A-33-4I-SAE-T
TLV70718PDQNR
U188
TLV707129DQNR
U189
Link mother and daughter
J4
15-91-2040
J5
SPI_DAC_MOSI
SPI_EXT_MISO
SPI_DAC_SCLK
SPI_EXT_CS
U183
SN74LVC2G17DBVR
J15
TOP_TURN1
TOP_TURN2
U2
HV7361GA-G
To piezo
External return
J2
J3
MMBD7000
D3
D4
D2
U6
??
L4 100nH
L1 100nH
L5 100nH
0V9_ADC
TO_LON
TO_VIP
U24
AD96498CPZ-65
ADC_CLK
outOfRange
L7 100nH
+1V8
+1V2
+3V3
+5V
+HV
-HV
GND
AD96498CPZ-65 to AD96098CPZRL7-20