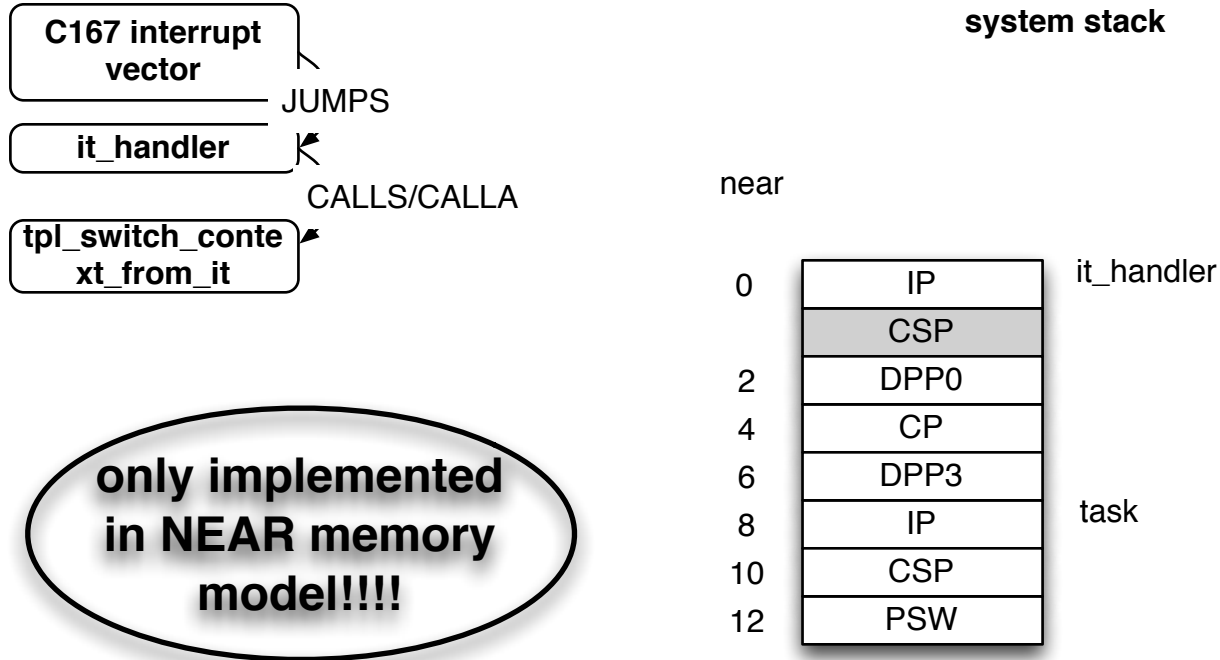# interrupt management for the C167

To process an interrupt, the CPU suspends its program execution and branches to the interrupt vector and push IP, PSW and CSP in segmented mode.

C167 interrupt vector

JUMPS

it_handler

CALLS/CALLA

tpl_switch_conte xt_from_it

only implemented in NEAR memory model!!!!

system stack

near

0	IP	it_handler
	CSP	
2	DPP0	
4	CP	
6	DPP3	
8	IP	task
10	CSP	
12	PSW	

only if FAR mode (32 bit pointers)

always on the stack