# The Trampoline Handbook

release 2.0

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Ι	Th	The Real-Time Operating System				
1	Оре	erating System Execution	11			
	1.1	Configuration Options	11			
	1.2	System Services	11			
		1.2.1 StartOS	11			
		1.2.2 ShutdownOS	12			
	1.3	Application Modes Declarations	12			
	1.4	Application Modes Services	13			
		1.4.1 DeclareApplicationMode	13			
		1.4.2 GetActiveApplicationMode	13			
	1.5	Implementation	14			
2	Tas	ks	15			
	2.1	States a task	15			
	2.2	The scheduling	15			
	2.3	Writing the code of a task	18			
	2.4	Tasks services	18			
		2.4.1 DeclareTask	18			
		2.4.2 ActivateTask	18			
		2.4.3 ChainTask	19			
		2.4.4 TerminateTask	20			
		2.4.5 Schedule	21			

		2.4.6	GetTaskID	21	
		2.4.7	GetTaskState	22	
	2.5	5 Inside Task management			
		2.5.1	Static attributes	23	
		2.5.2	Dynamic attributes	23	
		2.5.3	Additional task states	24	
	2.6	The ia	lle task	25	
3	Rese	esources			
	3.1	OSEK	Priority Ceiling Protocol	27	
	3.2	The R	ES_SCHEDULER resource	27	
	3.3	Standa	ard and Internal Resources	30	
	3.4	Nested	l resources accesses	30	
	3.5	OIL de	escription	31	
	3.6	Resour	rces services	31	
		3.6.1	DeclareResource	31	
		3.6.2	GetResource	32	
		3.6.3	ReleaseResource	32	
4	Eve	Events			
	4.1	OIL de	escription	33	
	4.2	Events	services	34	
		4.2.1	SetEvent	34	
		4.2.2	WaitEvent	35	
		4.2.3	GetEvent	35	
5	os .	Applic	ations	37	
	5.1		tion of the OS Applications startup and shutdown hooks	37	
6	Tim	ming Protection Implementation 3			
	6.1	Low L	evel Functions	39	
		6.1.1	FRT related functions	39	
		6.1.2	TPT related functions	40	
7	Schedule Table Implementation 4				

	7.1	The States of a Schedule Table	42
	7.2	Processing a Schedule Table	44
8	The	communication library	<b>47</b>
	8.1	Implementation	47
		8.1.1 Sending Message Objects	47
		8.1.2 Receiving Message Objects	48
9	The	Inter OS-application Communication Library	51
	9.1	IOC declaration in OIL	51
	9.2	Implementation	53
10	Men	mory mapping	55
11	Syst	em generation and compilation	57
	11.1	The generated files	57
	11.2	The Configuration Macros	58
		11.2.1 Number of objects macros	60
		11.2.2 Error Handling Macros	60
		11.2.3 Protection Macros	61
		11.2.4 Hook call macros	62
		11.2.5 Miscellaneous macros	62
	11.3	Application configuration	64
		11.3.1 Counter related constants declaration	64
		11.3.2 Events definition	64
		11.3.3 Standard resources definition	65
		11.3.4 Tasks definition	66
<b>12</b>	Imp	lementation details	69
	12.1	The tpl_kern structure	69
13	Port	ting Trampoline	<b>7</b> 1
	13.1	Adding files to the directory structure	71
	13.2	Using a target with goil	72
	13.3	Target specific code	72

		13.3.1 Functions called by Trampoline	72
		13.3.2 Service call	73
		13.3.3 Interrupt management	74
	13.4	Target specific structures	74
	13.5	Code templates	75
	13.6	Structures initialization templates	76
	13.7	The memory mapping and the link script templates	77
14	Port	ts details	<b>7</b> 9
	14.1	PowerPC	79
		14.1.1 System services	79
		14.1.2 Dispatching the service call	80
		14.1.3 Interrupt handler	85
		14.1.4 The CallTrustedFunction service	85
		14.1.5 The ExitTrustedFunction service	87
		14.1.6 Execution of the OS Applications startup and shutdown hooks	90
		14.1.7 The MPC5510 Memory Protection Unit	90
	14.2	ARM – Common conventions	92
		14.2.1 File hierarchy	92
		14.2.2 Common definitions	92
		14.2.3 Bootstraping	92
		14.2.4 Stacks	92
		14.2.5 Interrupt management	92
	14.3	ARM – ARM926 chip support	93
		14.3.1 Memory protection	93
		14.3.2 CPU cache support	94
	14.4	ARM – Armadeus APF27 board	94
		14.4.1 Debugging with Abatron BDI2000 or BDI3000 JTAG probe	94
		14.4.2 Configuration	94
		14.4.3 Memory mapping	94
		14.4.4 Memory protection	95
	14.5	ARM – Simtec EB675001 board	95
		14.5.1 Memory map and hardware resources	95

		14.5.2 Booting	95
		14.5.3 Internal kernel drivers	96
		14.5.4 Hardware interrupts handling	96
		14.5.5 Idle task	96
		14.5.6 Exceptions handling	96
		14.5.7 Kernel sleep service	96
	14.6	AVR8	96
		14.6.1 Context	96
		14.6.2 Context switch	96
		14.6.3 Context init	97
		14.6.4 Interrupts	98
II	$\mathbf{T}$	he Goil system generator	101
<b>15</b>	The	Goil templates	103
	15.1	The configuration data	104
		$15.1.1 \ \ \text{The PROCESSES}, \ \textit{TASKS}, \ \textit{BASICTASKS}, \ \textit{EXTENDEDTASKS}, \ \textit{ISRS1} \ \text{and} \ \textit{ISRS2} \ \text{list}$	s104
		15.1.2 The COUNTERS, HARDWARECOUNTERS and SOFTWARECOUNTERS lists	105
		15.1.3 The EVENTS list	106
		15.1.4 The ALARMS list	106
		15.1.5 The REGULARRESOURCES and INTERNALRESOURCES lists	106
		15.1.6 The MESSAGES, SENDMESSAGES and RECEIVEMESSAGES lists	107
		15.1.7 The SCHEDULETABLES list	108
		15.1.8 The OSAPPLICATIONS list	109
		15.1.9 The TRUSTEDFUNCTIONS list	110
		15.1.10 The READYLIST list	111
		15.1.11 The SOURCEFILES, CFLAGS, CPPFLAGS, ASFLAGS, LDFLAGS and TRAMPOLINESOURCEFILES lists	111
		15.1.12 The INTERRUPTSOURCES list	112
		15.1.13 Scalar data	112
	15.2	The Goil template language (or GTL)	114
	15.3	GTL types	114
		15.3.1 string readers	114
		<u> </u>	

	15.3.2	boolean readers	115
	15.3.3	integer readers $\dots$	115
	15.3.4	list readers $\dots$	116
15.4	GTL o	perators	116
	15.4.1	Unary operators	116
	15.4.2	Binary operators	116
	15.4.3	Constants	117
15.5	GTL in	nstructions	117
	15.5.1	The $let$ instruction	117
	15.5.2	The $if$ instruction	117
	15.5.3	The $foreach$ instruction	118
	15.5.4	The $for$ instruction	119
	15.5.5	The $loop$ instruction	119
	15.5.6	The $\!\!\!/$ instruction	119
	15.5.7	The $?$ instruction	119
	15.5.8	The $template$ instruction	120
	15.5.9	The $\textit{write}$ instruction	120
	15.5.10	The error and warning instructions	120
15.6	Examp	les	121
	15.6.1	Computing the list of process ids	121
	15.6.2	Computing an interrupt table	122
	15.6.3	Generation of all the files	123

# $$\operatorname{Part} \ I$$ The Real-Time Operating System

**CHAPTER** 

ONE

# OPERATING SYSTEM EXECUTION

This chapter presents how to start and shutdown the operating system as well as the configuration options and the Application Modes. Application Modes are used to start the operating system in different configurations. Usually, the configuration is read from hardware switches. The current Application Mode is passed to the StartOS service and cannot be changed once the operating system is started.

#### 1.1 Configuration Options

### 1.2 System Services

#### 1.2.1 StartOS

StartOS starts the OS in the AppModeID Application Mode. First the OS does some initializations, then the Startup Hook, if configured, is called. At last the scheduling is started and the highest priority task runs.



When called from outside a task or an ISR, typically from the main(), StartOS does not returns. When called from a task or an ISR, a case which is forbidden, StartOS returns and the Error Hook (if configured) is called.



If AppModeID does not correspond to any Application Mode, no error occurs but none of the AUTOSTART objects is started.

#### Prototype of StartOS:

void StartOS(AppModeType AppModeID);

#### **Arguments of StartOS:**

AppModeID The Application Mode.

#### 1.2.2 ShutdownOS

ShutdownOS shuts down the OS and notify the Error error code. If it is configured, the Shutdown Hook is called with Error as argument. The behavior may depends on the target platform. On embedded platforms interrupts are disabled and an infinite loop or a halt is executed. On POSIX the application exits.

#### Prototype of ShutdownOS:

```
void ShutdownOS(StatusType Error);
```

#### Arguments of ShutdownOS:

Error The error that occurred.

#### 1.3 Application Modes Declarations

Application Mode are used to specify which AUTOSTART objects (tasks, alarms or schedule tables) are started when StartOS is called. Application Modes are declared in OIL using the APPMODE object. *goil* accepts the DEFAULT boolean attribute. When TRUE, this attributes specifies the default Application Mode. DEFAULT is implicitly FALSE.

When only one Application Mode is defined, the constant OSDEFAULTAPPMODE is set to this Application Mode. When more than one Application Mode are defined, one and only one of the Application Modes DEFAULT attribute must be set to TRUE and the constant OSDEFAULTAPPMODE is set to this one.

At most 32 application modes may be declared in the current implementation. We believe it is far enough.

In the following example, 2 Application Modes are declared:

```
APPMODE normal { DEFAULT = TRUE; };

APPMODE diag { };
```

Let's consider 2 tasks and one alarm. The first task, *command*, is AUTOSTART in any case, the second one, *logging* is not AUTOSTART and the alarm, *trigger\_logging*, is AUTOSTART in Application Mode diag only. The goal is to have a periodic task doing some logging when the OS is started in Application Mode diag:

```
TASK command {
   AUTOSTART = TRUE {
     APPMODE = normal;
     APPMODE = diag;
   };
   ...
};
```

```
TASK logging {
  AUTOSTART = FALSE;
  ...
};

ALARM trigger_logging {
  AUTOSTART = TRUE {
    APPMODE = diag;
    ALARMTIME = 10;
    CYCLETIME = 10;
  };

  ACTION = ACTIVATETASK {
    TASK = logging;
  };
  ...
};
```

If StartOS is called with argument normal or OSDEFAULTAPPMODE, the alarm trigger\_logging is not started by StartOS and task logging does not run. If StartOS is called with argument diag, the alarm is started and task logging runs. In both cases task command is started.

#### 1.4 Application Modes Services

#### 1.4.1 DeclareApplicationMode

On the C side, each declared Application Mode is available as a constant of type AppModeType. However, before using one of the constants, you have to put it in the current scope with the DeclareApplicationMode service <sup>1</sup> as follow:

```
DeclareApplicationMode(normal);
DeclareApplicationMode(diag);
```

An exception is the constant OSDEFAULTAPPMODE which is in the scope as long as file 'tpl\_os.h' is included.



DeclareApplicationMode is a C macro

#### Prototype of DeclareApplicationMode:

DeclareApplicationMode(AppModeType AppModeID);

#### Arguments of DeclareApplicationMode:

AppModeID The Application Mode.

#### 1.4.2 GetActiveApplicationMode

GetActiveApplicationMode returns the Application Mode that was used to start the OS.

<sup>&</sup>lt;sup>1</sup>This macro is not part of [?] but has been added for convenience purpose

```
AppModeType currentAppMode;
currentAppMode = GetActiveApplicationMode();
```

If GetActiveApplicationMode is called before the OS is started, OSNOAPPMODE is returned.

#### Prototype of GetActiveApplicationMode:

AppModeType GetActiveApplicationMode(void);

#### 1.5 Implementation

At system generation time, an identifier AppModeID of type AppModeType is attributed to each Application Mode. Identifiers range from 0 to number of application modes - 1 and are attributed by goil in their order of appearance in the OIL file.

For each AppModeID, goil computes a mask: AppModeMask = 1 << AppModeID. For each task, alarm and schedule table, a table indexed by the object id is computed by goil. Each element of these tables is the bitwise or of the AppModeMask in which the object is AUTOSTART. If there is no task, alarm or schedule table defined, the corresponding table is not generated.

StartOS iterates over the tasks, alarms and schedule tables Application Mode mask tables. It does a bitwise and with the mask stored in the table and the mask computed from the Application Mode. If the result is not 0 then the corresponding object is AUTOSTART in this Application Mode and is started.

Using the example of section 1.3 we have

```
CONST(tpl_application_mode, OS_CONST) diag = 0; /* mask = 1 */
CONST(tpl_application_mode, OS_CONST) normal = 1; /* mask = 2 */
AppModeType is an alias of tpl_application_mode.

CONST(tpl_appmode_mask, OS_CONST) tpl_task_app_mode[TASK_COUNT] = {
    3 /* task command : normal / diag */,
    0 /* task logging : */
};

CONST(tpl_appmode_mask, OS_CONST) tpl_alarm_app_mode[ALARM_COUNT] = {
    1 /* alarm trigger_logging : diag */
};
```

The tpl\_appmode\_mask type is computed according to the number of Application Modes.

Table 1.1: Size of tpl\_appmode\_mask type.

Number of Application Modes	$ ext{tpl_appmode_mask type}$
[1,8]	u8
[9, 16]	u16
[17, 32]	u32

**CHAPTER** 

**TWO** 

# **TASKS**

A task is an execution framework for the functions of the application <sup>1</sup>. A task is a kind of process. Tasks are executed concurrently and asynchronously, see 2.2. 2 kinds of task exist: basic tasks and extended tasks. A basic task cannot block (i.e. it cannot use a service that may block) while an extended task can. The tasks and their properties are declared in the OIL file, see ??. Their functions are defined in a C file.

#### 2.1 States a task

A task may be in different states. A basic task may be currently executing (in the RUNNING state), ready to execute (in the READY state) or not active at all (in the SUSPENDED state). Figure 2.1 shows the states of a basic task. An extended task has an additional WAITING state. Figure 2.2 shows the states of an extended task. See section 2.5.3 for additional informations about the states of a task.

A task goes from one state to the other according to various conditions as shown in table 2.1.



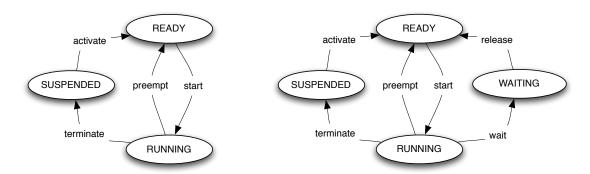
A system service may do more than one transition at a time. For instance, if a task is activated by calling ActivateTask and its priority is higher than the priority of the current running task, the new task will go from SUSPENDED to RUNNING and the intermediate state READY will not be observable.

#### 2.2 The scheduling

Trampoline schedules the tasks dynamically during the execution of the application. A task is scheduled according to its priority and whether it is preemptable or not. The priority of a task

<sup>&</sup>lt;sup>1</sup>The term *Application* is also used in AUTOSAR to designate a set of object, this manual uses OS Application to name the AUTOSAR applications and Application to name the user level software.

16 2.2. The scheduling



 $\textbf{Figure 2.1:} \ \textit{States of a BASIC task}.$ 

Figure 2.2: States of an EXTENDED task.

Table 2.1: Transition from state to state of a task.

${f transition}$	former state	new state	description
activate	SUSPENDED	READY	the task is set in the READY state on one of the following occurrences: services ActivateTask
			or ChainTask, activation notification coming from an alarm, a schedule table or a message.
start	READY	RUNNING	the task is set to the running state and begin to execute because it has the highest prior- ity in the system and has been elected by the scheduler.
terminate	RUNNING	SUSPENDED	the task is set to the SUSPENDED state when it calls the TerminateTask or ChainTask service.
preempt	RUNNING	READY	the task is set to the READY state when the scheduler starts a higher priority task.
wait	RUNNING	WAITING	the task may be set to the WAITING state when it calls the service WaitEvent.
release	WAITING	READY	the task is set to the READY state when it gets one of the events it is waiting for.

2.2. The scheduling 17

is given at design stage, and indicated in the OIL file using the PRIORITY attribute, see ??, and may change during execution when the task gets or release a resource. The preemptability of a task may be set too. It is also indicated in the OIL file using the SCHEDULE attribute, see ??.

A tasks continues to run until it is preempted because a task having a higher priority is put in the READY state, or it blocks because it is waiting for an event. Only extended tasks may block. If more than one task have the same priority, tasks are run one after the other because a task may not preempt an other task having the same priority. So there is no round robin among tasks of the same priority level.

A non-preemptable task runs until it calls **Schedule** and a higher priority task is in the **READY** state or until it blocks. More informations about priority and preemptability may be found in chapter 3.

In the following examples, the horizontal axis is the time. The state of the task is indicated in a rectangle that spans a period of time. When the task is running the rectangle is grayed. An up arrow  $\uparrow$  indicates a task activation and a down arrow  $\dagger$  a task termination.



Figure 2.3: Scheduling of preemptable tasks. During A period,  $T_1$  is RUNNING and  $T_2$  is SUSPENDED. Then  $T_2$  is activated. Since  $Prio(T_2) > Prio(T_1)$ ,  $T_1$  is preempted and  $T_2$  runs (B period).  $T_2$  terminates and  $T_1$  becomes RUNNING again (C period) until it terminates.



Figure 2.4: Scheduling of non-preemptable tasks. During A period,  $T_1$  is RUNNING and  $T_2$  is SUSPENDED. Then  $T_2$  is activated. Even if  $Prio(T_2) > Prio(T_1)$ ,  $T_1$  is non-preemptable and continues to run until it terminates (B period). In the meantime,  $T_2$  is READY.  $T_1$  terminates and  $T_2$  runs (C period) until it terminates.

#### 2.3 Writing the code of a task

Trampoline provides a TASK macro to define a task in a C source file. The macro takes one argument which is the identifier of the task:

```
TASK(MyTask)
{
   /* code of the task */
   TerminateTask();
}
```

The code of the task is plain C.

The task should always end with a call to the TerminateTask service. See 2.4.4.

#### 2.4 Tasks services

#### 2.4.1 DeclareTask

Each task has an identifier of type TaskType. This identifier is declared in the OIL file and is used in system calls to refer to a particular task. Before using such an identifier in your program, you have to declare it:

```
DeclareTask(MyTask);
```

This makes the MyTask identifier available in the current scope.



DeclareTask is a C macro. When the task has been define above using the macro TASK, the identifier of the task is already in the scope and DeclareTask is not needed.

#### Prototype of DeclareTask:

DeclareTask(TaskType TaskID);

#### Arguments of DeclareTask:

TaskID The id of the task to declare.

#### 2.4.2 ActivateTask



This service does a rescheduling

Activates a new instance of a task. If activation counter has reached the maximum activation count or the task cannot be activated for timing protection purpose, the service fails. Otherwise if an instance is already active (RUNNING or READY), the state does not change and the activation is recorded to be done later. If no instance is active, the state of the task is changed to READY.

Figures 2.5, 2.6 and 2.8 show 2 examples of task activation.

2.4. Tasks services 19

#### Prototype of ActivateTask:

StatusType ActivateTask(TaskType TaskID);

#### Arguments of ActivateTask:

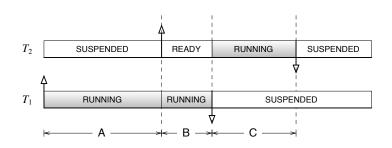
TaskID The id of the task to activate.

#### Status codes returned by ActivateTask:

E\_OK No error, the task has been successfully activated (extended and standard).

E\_OS\_ID Invalid TaskID. No task with such an id exists (extended only).

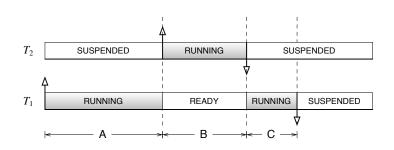
E\_OS\_LIMIT Too many activations of the task (extended and standard).



```
TASK(T2) {
    ... /* C period */
    TerminateTask();
}

TASK(T1) {
    ... /* A period */
    ActivateTask(T2);
    ... /* B period */
    TerminateTask();
}
```

Figure 2.5: Activation of a lower priority task.  $Prio(T_1) \ge Prio(T_2)$ . During A period,  $T_1$  is RUNNING and  $T_2$  is SUSPENDED. Then  $T_1$  calls ActivateTask(T2);. Since  $T_2$  does not have a higher priority, it becomes READY (B period).  $T_1$  terminates and  $T_2$  runs (C period) until it terminates.



```
TASK(T2) {
    ... /* B period */
    TerminateTask();
}

TASK(T1) {
    ... /* A period */
    ActivateTask(T2);
    ... /* C period */
    TerminateTask();
}
```

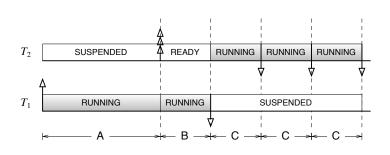
Figure 2.6: Activation of a higher priority task.  $Prio(T_1) < Prio(T_2)$ . During A period,  $T_1$  is RUNNING and  $T_2$  is SUSPENDED. Then  $T_1$  calls ActivateTask(T2);. Since  $T_2$  has a higher priority, it becomes RUNNING (B period).  $T_2$  terminates and  $T_1$  resumes (C period) until it terminates.

#### 2.4.3 ChainTask



This service does a rescheduling

20 2.4. Tasks services



```
TASK(T2) {
    ... /* C period */
    TerminateTask();
}

TASK(T1) {
    ... /* A period */
    ActivateTask(T2);
    ActivateTask(T2);
    ActivateTask(T2);
    ... /* B period */
    TerminateTask();
}
```

Figure 2.7: Multiple activations of a lower priority task.  $Prio(T_1) \ge Prio(T_2)$ . During A period,  $T_1$  is RUNNING and  $T_2$  is SUSPENDED. Then  $T_1$  calls ActivateTask(T2); 3 times. Since  $T_1$  has a higher priority,  $T_2$  does not run immediately and the 3 activations are recorded provided the ACTIVATION attribute in the OIL description of the task is a least 3 (B period). When  $T_1$  terminates, the scheduler executes  $T_2$  3 times (C periods).

This service puts task TaskID in READY state, and the calling task in the SUSPENDED state. It acts as the TerminateTask service for the calling task.

#### Prototype of ChainTask:

StatusType ChainTask(TaskType TaskID);

#### Arguments of ChainTask:

TaskID The id of the task to activate.

#### Status codes returned by ChainTask:

E\_OK No error, the task TaskID has been successfully activated and the calling task has been successfully terminated. Note in this case ChainTask does not return so actually E\_OK is never returned (extended and standard).

E\_OS\_ID Invalid TaskID. No task with such an id exists (extended only).

E\_OS\_LIMIT Too many activations of the task (extended and standard).

E\_OS\_RESOURCE The calling task still held a resource (extended only).

E\_OS\_CALLEVEL Called outside of a task (extended only).

#### 2.4.4 TerminateTask



This service does a rescheduling

This service stops the calling task and puts it in SUSPENDED state.

Chapter 2. Tasks

2.4. Tasks services 21



Figure 2.8: Chaining of tasks. During A period,  $T_1$  is RUNNING and  $T_2$  is SUSPENDED. Then  $T_1$  calls ChainTask(T2);  $T_1$  terminates and  $T_2$  is activated. Then  $T_2$  runs (B periods).

#### Prototype of TerminateTask:

StatusType TerminateTask(void);

#### Status codes returned by TerminateTask:

E\_OK No error, the calling task has been successfully terminated. Note in this case TerminateTask does not return so actually E\_OK is never returned (extended and standard).

E\_OS\_RESOURCE The calling task still held a resource (extended only).

E\_OS\_CALLEVEL Called outside of a task (extended only).

#### 2.4.5 Schedule



This service does a rescheduling. Schedule does not deal directly with tasks but since it is a call to the scheduler, it is presented here.

If called from a preemptable task that does not use an internal resource, Schedule has not effect. If called from a preemptable or a task that uses an internal resource, the priority of the task revert to its base priority and a rescheduling occurs.

Schedule allows to implement cooperative multitasking to insure synchronous rescheduling.

#### Prototype of Schedule:

StatusType Schedule(void);

#### Status codes returned by Schedule:

E\_OK No error. (extended and standard).

E\_OS\_RESOURCE The calling task still held a resource (extended only).

E\_OS\_CALLEVEL Called outside of a task (extended only).

#### 2.4.6 GetTaskID

GetTaskID writes in the *TaskID* variable passed as reference the identifier of the task currently RUNNING. If no task is currently RUNNING because GetTaskID was called from an ISR of before

22 2.4. Tasks services

Trampoline is started, INVALID\_TASK is got.



The argument is a pointer. Do not pass an uninitialized pointer. Proper use of this service supposes a TaskType variable is instantiated, then its address is passed to GetTaskID as shown in the example below:

```
TaskType runningTaskID;
GetTaskID(&runningTaskID);
```

#### Prototype of GetTaskID:

StatusType GetTaskID(TaskRefType TaskID);

#### Arguments of GetTaskID:

TaskID Reference to the task.

#### Status codes returned by GetTaskID:

E\_OK No error. (extended and standard).

E\_OS\_PROTECTION\_MEMORY The caller does not have access to the addresses of *TaskID* reference (extended + AUTOSAR scalability class 3 and scalability class 4 only).

#### 2.4.7 GetTaskState

GetTaskState writes in the variable passed as reference in State the state of the task given in TaskID.



The *State* argument is a pointer. Do not pass an uninitialized pointer. Proper use of this service supposes a TaskState variable is instantiated, then its address is passed to GetTaskState as shown in the example below:

```
TaskStateType T1State;
GetTaskState(T1, &T1State);
```

#### Prototype of GetTaskState:

StatusType GetTaskState(TaskType TaskID, TaskStateRefType State);

#### Arguments of GetTaskState:

TaskID The id of the task...

State Reference to the state..

#### Status codes returned by GetTaskState:

E\_OK No error. (extended and standard).

E\_OS\_ID Invalid TaskID. No task with such an id exists (extended only).

E\_OS\_PROTECTION\_MEMORY The caller does not have access to the addresses of *State* reference (extended + AUTOSAR scalability class 3 and scalability class 4 only).

#### 2.5 Inside Task management

#### 2.5.1 Static attributes

A task has the following static attributes:

The entry point of the task. A pointer to the code of the task. When the scheduler start a task instance the first time, it uses this pointer to begin the execution.

The internal resource the task uses if any. An internal resource is automatically taken when a task enters the RUNNING state and automatically released when the task leaves the RUNNING state. See ?? for more informations.

**The base priority** of the task as specified in the OIL file. This priority is used to reset the current priority when the task is activated.

The maximum activation count of the task as specified in the OIL file.

The kind of task, BASIC or EXTENDED.

The task id. Used for internal checking.

The id of the OS Application the tasks belong to (only available in AUTOSAR scalability class 3 and scalability class 4).

The timing protection configuration if any (only available in AUTOSAR scalability class 2 and scalability class 4).

#### 2.5.2 Dynamic attributes

A task has also the following dynamic attributes:

The context. This is the chunk of RAM where the current execution context of a task is stored when the task is in the READY or WAITING state. The execution context is the value of the microprocessor's registers (program counter, stack pointer, other working registers). So the context depends on the target on which Trampoline runs.

The stack(s). This is the chunk of RAM where registers are pushed for function call. This attributes depends on the target architecture. For instance, the C166 micro-controller uses 2 stacks.

The current activation count. When a task is activated while not in SUSPENDED state, the activation is recorded and is actually done when the task returns to the SUSPENDED state. Many activation may be recorded according to the value given to the ACTIVATION task OIL attribute. When a task is activated, the current activation count is compared to the maximum activation count and if  $\geq$ , the activation fails.

The list of resources the task currently owns.

**The current priority** of the task. This priority starts equal to the basic priority and may increase when the task get a resource.

The state of the task as defined in sections 2.1 and 2.5.3.

The trusted counter. If = 0, the task is non-trusted. If > 0 the task is trusted. See chapter ?? for more informations. This counter is available if Trampoline is compiled with memory protection support.

The activation allowed flag. If true, the task may be activated. If false, it cannot be activated. This flag is set by the timing protection facility. It is available if Trampoline is compiled with timing protection support. See chapter ??.

#### 2.5.3 Additional task states

In addition to states presented in section 2.1, 2 extra states are used for internal management:

AUTOSTART This state is used to indicate what task should be started automatically when StartOS is called. An AUTOSTART task is in this initial state but no task is in this state once the application code is running. StartOS iterates through the tasks and activates those that are in the AUTOSTART state.

READY\_AND\_NEW This state is used to flag a task that is ready but has its context uninitialized. This happens when the task has just been activated. The kernel initializes the context of the task the first time it goes to the RUNNING state.

Figure 2.9 show a complete task state automaton for both basic and extended tasks with these states added.

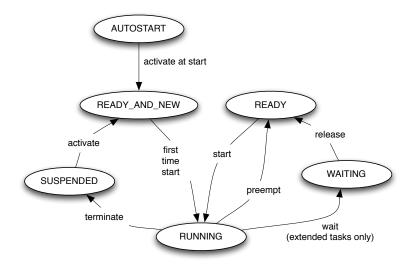


Figure 2.9: States of a task in Trampoline. AUTOSTART is the initial state of autostart tasks. SUSPENDED is the initial state of both non autostart tasks.

2.6. The idle task 25

#### 2.6 The idle task

The *idle* task is activated by StartOS. It is a BASIC task with a priority of 0 (i.e. the lowest priority in the system, the lowest priority of tasks defined in the application is 1). So when no other task is currently running, the *idle* task run.

To be able to use specific platform capabilities (to put the micro-controller in stand by mode for example), this task calls repetitively a hardware specific function called tpl\_sleep (defined in machines/). The tasks is then able to quantify the microprocessor occupation.

GOIL doesn't produce anything about this idle task (unlike application(s) task(s)). The idle task descriptor is defined in 'tpl\_os\_kernel.c'.

26 2.6. The *idle* task

**CHAPTER** 

THREE

# **RESOURCES**

A Resource is an object used to protect a critical section in a task or in an ISR and to insure mutual exclusion. By using a resource to protect the use of a shared piece of data or a shared hardware device, the programmer avoids race conditions. Figure 3.1 shows an example of race condition.

#### 3.1 OSEK Priority Ceiling Protocol

OSEK uses a modified version of the Priority Ceiling Protocol [?]. A priority is assigned to each resource. This priority is computed to be at least equal to the highest priority of the tasks and ISRs that use the resource. So let  $T_1, T_2, \ldots, T_n$  a set of tasks sharing the same resource R and  $P_1, P_2, \ldots, P_n$  their priorities so that  $P_i = P(T_i)$ . We have  $P(R) = \max_{i=1,n}(P_i)$ .

When a task gets a resource, its priority is raised to the priority of the resource. That way, the task will run with the priority of the highest priority task and will insure the release of the resource is not delayed by a lower priority task. In addition, since every other tasks that use the same resource have now a priority  $\leq$ , they cannot preempt the running task and mutual exclusion is insured. Figure 3.2 show an example of resource use.

The priority of a resource is computed by *goil* according to the priorities of the tasks and ISRs that use the resource.

#### 3.2 The Res\_scheduler resource

Trampoline provides a predefined standard resource called RES\_SCHEDULER. This resource has a priority  $\geq$  to the maximum priority of the tasks but < to the minimum priority of the ISR. When a task gets RES\_SCHEDULER, it becomes non preemptable. To make RES\_SCHEDULER available to the application, the USERESCHEDULER attribute must be set to TRUE within the OS object in the

```
int val = 0;
int actCount = 0;
                                                      val=2 count=10
                                                      val=3 count=20
TASK (bgTask)
                                                      val=4 count=30
                                                      val=5 count=40
  while (1) {
                                                      val=2 count=50
    val++;
                                                      val=2 count=60
    val--;
                                                      val=0 count=70
                                                      val=-2 count=80
}
                                                      val = -1 count = 90
                                                      val=-1 count=100
TASK (periodicTask)
                                                      val=-2 count=110
                                                      val=0 count=120
                                                      val=0 count=130
  activationCount++;
                                                      val=0 count=140
  if ((actCount % 2) == 1) {
                                                      val=0 count=150
    val++;
                                                      val=-2 count=160
                                                      val = -1 count = 170
  else {
                                                      val = -2 count = 180
    val--;
                                                      val=-4 count=190
                                                      val=-4 count=200
                                                      val = -6 count = 210
  TerminateTask();
                                                      val=-4 count=220
}
                                                      val = -5 count = 230
                                                      val=-6 count=240
TASK(displayTask)
                                                      val = -7 count = 250
                                                      val = -6 count = 260
  printf("val=%d_{\square}count=%d_{\square},
                                                      val = -3 count = 270
          val,
                                                      val=-3 count=280
          activationCount);
                                                      val = -5 count = 290
                                                      val = -5 count = 300
  TerminateTask();
}
```

Figure 3.1: Shared data access. In this example 3 preemptable tasks are used. bgTask increments and decrements the global integer variable shared in an infinite loop. periodicTask runs every 100ms and increments the global integer variable activateCount. If activateCount is odd, periodicTask increments shared otherwise it is decremented. A third task, displayTask runs every second and displays both variables. On the left, the corresponding program, on the right one of the possible outputs

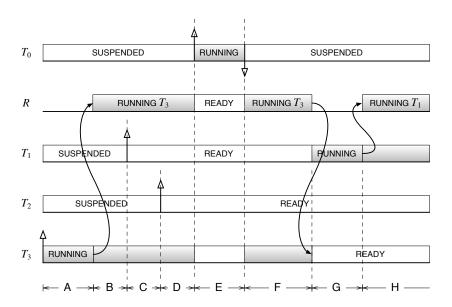


Figure 3.2: Scheduling with a resource used by 3 tasks and a fourth task having a higher priority.  $P(T_0) > P(T_1) > P(T_2) > P(T_3)$ . R is used by  $T_1$ ,  $T_2$  and  $T_3$  so  $P(T_0) > P(R) \ge P(T_1)$ . During A period,  $T_3$  is Running and other tasks are suspended. Then  $T_3$  gets R and  $P(T_3) \leftarrow P(R)$  (B to F periods).  $T_1$  is activated and becomes Ready; since  $P(T_3) \ge P(T_1)$ ,  $T_1$  does not run (C to F periods).  $T_2$  is activated and becomes reason it does not run (D to H periods).  $T_0$  is activated and because  $P(T_0) > P(R)$  it runs (E period).  $T_0$  terminates and  $T_3$  continues its execution (F period). Then  $T_3$  releases R and  $P(T_3)$  reverts to its base priority; so since  $P(T_1) > P(T_2) > P(T_3)$ ,  $T_1$  runs (G period).  $T_1$  gets R and  $P(T_1) \leftarrow P(R)$  (H period).

OIL file. Unlike resources defined by the application, there is no need to declare RES\_SCHEDULER is used by a task in the OIL file.

#### 3.3 Standard and Internal Resources

Standard resources are got and released explicitly by tasks and ISRs using the ad-hoc services. Internal resources are got implicitly when the task enters the RUNNING state and released implicitly when the task calls Schedule or blocks when using WaitEvent.



At most one internal resource may be used by a task.

Standard resources are dedicated to the protection of critical sections around the access to a shared data or to a device. Internal resources are used to implement non preemptable tasks within a task group. A task group is a set of task that are non preemptable by each other but remain preemptable by higher priority tasks in the application. A task group priority is the priority of its internal resource.

Trampoline provides a predefined internal RES\_SCHEDULER resource with the same priority. This internal resource is used to implement non preemptable tasks in the whole application as if all the non preemptable tasks belong to an implicit task group. When a task is non preemptable by setting the SCHEDULE attribute to NON in its OIL description, the task is assigned the internal RES\_SCHEDULER resource.

#### 3.4 Nested resources accesses

Resources may be accessed in a nested way. That is once a resource is got, another one may be got before releasing the first one and so on. However resources must be released in the reverse order they have been got as if they were pushed on a stack. The following example shows the good usage of resources:

```
TASK(MyTask)
{
   GetResource(rez1);
   ...
   /* critical section protected by rez1 */
   ...
   GetResource(rez2);
   ...
   /* critical section protected by rez2 and rez1 */
   ...
   ReleseResource(rez2);
   ...
   /* more critical section protected by rez1 */
   ...
   ReleaseResource(rez1);
   TerminateTask();
}
```

#### 3.5 OIL description

A resource is described using a RESOURCE object. RESOURCEPROPERTY is the single attribute of this object. A standard resource is defined with the following code:

```
RESOURCE res {
  RESOURCEPROPERTY = STANDARD;
};
```

And an internal resource is defined with the following code:

```
RESOURCE other_res {
   RESOURCEPROPERTY = INTERNAL;
};
```

A third kind of declaration exists for LINKED resources. A linked resource may be linked to a linked resource or a standard resource but a link tree of resources must have a standard resource at the root. A linked resource has the same priority as the standard resource it is linked to and is a kind of reference. Linked resources are provided to replace nested access to the same resource (which is prohibited) and are rarely used.

```
RESOURCE 1_res {
   RESOURCEPROPERTY = LINKED { LINKEDRESOURCE = res };
}:
```



Every task and ISR that uses a resource in the C code must declare it in the OIL file. Otherwise *goil* will compute a wrong priority for the resource and the scheduling of tasks and the execution of ISR will not be as expected.

#### 3.6 Resources services

#### 3.6.1 DeclareResource

Each resource has an identifier of type ResourceType. This identifier is declared in the OIL file and is used in system calls to refer to a particular resource. DeclareResource declares a resource exists. The result is to make the id of the resource available and allows to use it in services' calls.



DeclareResource is a C macro

#### Prototype of DeclareResource:

DeclareResource(ResourceType ResourceID);

#### Arguments of DeclareResource:

ResourceID The id of the resource.

31

32 3.6. Resources services

#### 3.6.2 GetResource

GetResource enters the critical section protected by the resource. For each call to GetResource, a corresponding call to ReleaseResource must be made in the control flow of the task or ISR. Nested calls are allowed, see 3.4 for nested resource accesses.

#### Prototype of GetResource:

StatusType GetResource(ResourceType ResourceID);

#### Arguments of GetResource:

ResourceID The id of the resource to get.

#### Status codes returned by GetResource:

E\_OK No error (extended and standard).

E\_OS\_ID Invalide resource id. No resource with such an id exists (extended and standard).

E\_OS\_ACCESS The resource is already taken by a task or an ISR or has a priority lower than the base priority of the calling task or ISR. This should not happen if the application is configured correctly except if the same task or ISR try to get the same resource twice (extended only).

#### 3.6.3 ReleaseResource

ReleaseResource leaves the critical section protected by the resource. For each call to ReleaseResource, a corresponding call to GetResource must have been made in the control flow of the task or ISR. Nested calls are allowed, see 3.4 for nested resource accesses.



This service does a rescheduling

#### Prototype of ReleaseResource:

StatusType ReleaseResource(ResourceType ResourceID);

#### Arguments of ReleaseResource:

ResourceID the id of the resource.

#### Status codes returned by ReleaseResource:

E\_OK No error (extended and standard).

E\_OS\_ID Invalide resource id. No resource with such an id exists (extended and standard).

**CHAPTER** 

**FOUR** 

# **EVENTS**

Events are used to synchronize an extended task to a condition external to the task. Each extended task has a private set of events (it owns the event) and an event is explicitly sent to a task. Having the same event attributed to many tasks does not mean the tasks share the event. They share only the value (or mask) associated to the event.

Events may be set by any other task, by an ISR2, by an alarm, by a schedule table or by the arrival of a message. Any task or ISR may read the events of a task but only the extended task owning the event is able to wait for it or to clear it.



If you use AUTOSAR OS Applications, involved objects must belong to the same OS Application or must have an access right to the OS Application of the target task.

A RUNNING task that wait for an event is put in the WAITING state if the event has not occured or stay in the RUNNING state if it has already occured.

A WAITING task is put in the READY state if one of the events it is waiting for occurs. See chapter 2 for more informations.



Events must be explicitly cleared once read. If a tasks does not clear the previous occurrence of an event, it will be seen as "already occurred" the next time the task will wait for it.

### 4.1 OIL description

An event is described using a EVENT object. MASK is the single attribute of this object. MASK may be set to a literal value:

```
EVENT ev {
   MASK = 0x1;
};
```

34 4.2. Events services

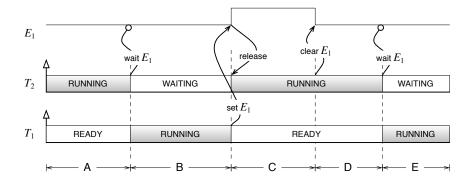


Figure 4.1: Scheduling with an event.  $T_2$  is an extended task. During A period,  $T_2$  is RUNNING and  $T_1$  is READY. Then  $T_2$  wait for  $E_1$  and blocks.  $T_2$  runs (B period) and sets  $E_1$ .  $T_2$  is released and since  $P(T_2) > P(T_1)$ ,  $T_2$  runs (C period), clears  $E_1$  and continues to run (D period). Then  $T_2$  wait for  $E_1$  again and blocks,  $T_1$  runs (E period).



The literal value should have only 1 bit set. Goil emits a warning when this is not the case.

Or MASK may be set to AUTO. In this case, the system generation tool computes the event mask:

```
EVENT ev {
   MASK = AUTO;
};
```

#### 4.2 Events services

#### 4.2.1 SetEvent

Events of task TaskID are set according to the Mask passed as  $2^{nd}$  argument. This service is non blocking and may be called from a task or an ISR2.



SetEvent may do a rescheduling if the target task is unblocked and goes to the READY state.

#### Prototype of SetEvent:

StatusType SetEvent(TaskType TaskID, EventMaskType Mask);

#### **Arguments of SetEvent:**

TaskID the id of the task.

Mask the event mask.

Chapter 4. Events

4.2. Events services 35

#### Status codes returned by SetEvent:

E\_OK No error (extended and standard).

E\_OS\_ID Invalid TaskID (extended only).

E\_OS\_ACCESS TaskID is not an extended task (not able to manage events) (extended only).

E\_OS\_STATE Events cannot be set because the target task is in the SUSPENDED state (extended only).

#### 4.2.2 WaitEvent

The calling task waits for event(s) *Mask*. If one the events are already set, the task continues its execution. If none of the events are set, the task is put in the WAITING state and blocks.



WaitEvent may do a rescheduling if the calling task blocks.

#### Prototype of WaitEvent:

StatusType WaitEvent(EventMaskType Mask);

#### Arguments of WaitEvent:

Mask The event(s) to wait for.

#### Status codes returned by WaitEvent:

E\_OK No error (extended and standard).

E\_OS\_ACCESS The calling task is not an extended task (not able to manage events) (extended only).

E\_OS\_RESOURCE The calling task holds a resource (extended only).

E\_OS\_CALLEVEL The caller is not a task (extended only).

#### 4.2.3 GetEvent

Events of task TaskID are copied in Mask argument passed as reference.



GetEvent does not reset the event mask. ClearEvent should be used to clear, in the event mask, the events that have been processed.



The <code>Mask</code> argument is a pointer. Do not pass an uninitialized pointer. Proper use of this service supposes a <code>EventMask</code> variable is instantiated, then its address is passed to <code>GetEvent</code> as shown in the example below:

```
EventMaskType myEventMask;
GetEvent(aTask, &myEventMask);
```

#### Prototype of GetEvent:

StatusType GetEvent(TaskType TaskID, EventMaskRefType Mask);

36 4.2. Events services

#### Arguments of GetEvent:

TaskID the id of the task.

 ${\tt Mask}$  the reference of the event mask where the  ${\tt TaskID}$  event mask is copied.

#### Status codes returned by GetEvent:

E\_OK No error (extended and standard).

E\_OS\_ID Invalid TaskID (extended only).

**E\_OS\_ACCESS** The task identified by TaskID is not an extended task (not able to manage events) or, in AUTOSAR, the caller cannot access the task (extended only).

E\_OS\_STATE The task identified by TaskID is in SUSPENDED state (extended only).

**FIVE** 

# OS APPLICATIONS

OS Applications are a set of objects managed by Trampoline and sharing common data and access rights.

## 5.1 Execution of the OS Applications startup and shutdown hooks

These hooks are executed from the kernel but with the access right of a task belonging to the OS Application. The system generation tool should choose one of the tasks of the OS Application to be used as context to execute the OS Application startup and shutdown hooks. Execution of an OS Application startup hook is done by the tpl\_call\_startup\_hook\_and\_resume function. The argument of this function is a function pointer to the hook. Similarly execution of an OS Application shutdown hook is done by the tpl\_call\_shutdown\_hook\_and\_resume function. These functions end by a call to NextStartupHook and NextShutdownHook services respectively to cycle through the hooks.

38	5.1. Execution of the OS Applications startup and shutdown hooks
Chapter 5. OS Applica	ations
1	

SIX

# TIMING PROTECTION IMPLEMENTATION

The Timing Protection Implementation uses 2 timers. The first one is a Free Running Timer (FRT) which is used for Time Frame. The second one is a classical timer called Timing Protection Timer (TPT) which is used for Execution Time Budget, Resource Locking Budget and Interrupt Disabling Budget.

## 6.1 Low Level Functions

These functions are provided by the *Board Support Package* and are used to manage the timers needed by the Timing Protection.

## 6.1.1 FRT related functions

tpl\_status tpl\_start\_frt(void) starts the FRT. On a microcontroller having a FRT that starts automatically when the system is powered on, this function does nothing but must be present since it is called by Trampoline in initialization stage. An error code is returned:  $E_{-}OK$  means no error,  $E_{-}OS_{-}NOFUNC$  means the FRT could not be started.

tpl\_status tpl\_read\_frt(tpl\_tp\_tick \*out\_value) write the current value of the FRT in  $out_value$ . An error code is returned:  $E_{-}OK$  means no error,  $E_{-}OS_{-}NOFUNC$  means the FRT could not be read.

tpl\_status tpl\_elapsed\_frt(tpl\_tp\_tick last\_tick, tpl\_tp\_tick \*out\_value) write the number of ticks elapsed since last\_tick in out\_value. If the FRT has overflown/underflown

between the time  $last\_tick$  was get and the time tpl\_elapsed\_frt is called, tpl\_elapsed\_frt gives a correct value. An error code is returned:  $E\_OK$  means no error,  $E\_OS\_NOFUNC$  means the FRT could not be read.

### 6.1.2 TPT related functions

tpl\_status tpl\_init\_tpt(???) initializes the TPT. An error code is returned:  $E\_OK$  means no error,  $E\_OS\_NOFUNC$  means the TPT could not be initialized.

tpl\_status tpl\_deinit\_tpt(void) deinitializes the TPT. An error code is returned:  $E\_OK$  means no error,  $E\_OS\_NOFUNC$  means the TPT could not be deinitialized.

 $tpl\_status$   $tpl\_start\_tpt(tpl\_tp\_tick$  delay) starts the TPT with an expiration delay equal to delay ticks. At that time, the  $tpl\_tpt\_handler$  function is called. An error code is returned:  $E\_OK$  means no error,  $E\_OS\_NOFUNC$  means the TPT could not be started because it is not initialized.

tpl\_status tpl\_read\_tpt(tpl\_tp\_tick \*out\_value) write the current value of the TPT in *out\_value*. An error code is returned: *E\_OK* means no error, *E\_OS\_NOFUNC* means the TPT could not be read.

tpl\_status tpl\_elapsed\_tpt(tpl\_tp\_tick last\_tick, tpl\_tp\_tick \*out\_value) write the number of ticks elapsed since  $last_tick$  in  $out_value$ . An error code is returned:  $E_-OK$  means no error,  $E_-OS_-NOFUNC$  means the TPT could not be read.

**SEVEN** 

# SCHEDULE TABLE IMPLEMENTATION

Here is the files list:

- 'tpl\_as\_schedtable.c' contains the API services.
- 'tpl\_as\_st\_kernel.c' contains the kernel API services, tpl\_process\_schedtable() and tpl\_adjust\_next\_expiry\_point()
- 'tpl\_as\_action.c' contains tpl\_action\_finalize\_schedule\_table()
- 'tpl\_as\_definitions.h' contains the schedule table's states (SCHEDULETABLE\_STOPPED, SCHEDULETABLE\_BOOTSTRAP, SCHEDULETABLE\_AUTOSTART\_ABSOLUTE...)
- 'tpl\_os\_timeobj\_kernel.c' contains tpl\_remove\_time\_obj() which has been modified for the schedule table object.

The schedule table class diagram is shown in Figure 7.1 below.

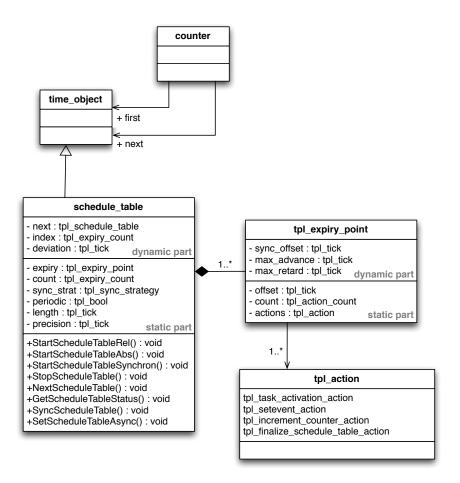


Figure 7.1: Schedule table class diagram

## 7.1 The States of a Schedule Table

A schedule table always has a defined state. States include those found at page 42 of the AUTOSAR specifications 3.1 and others states used for internal management.

Indeed, bit 1 is the "autostart" bit. It's used when autostarted schedule tables have been declared in the OIL file. Goil generates schedule tables with SCHEDULETABLE\_AUTOSTART\_X (X can be RELATIVE, ABSOLUTE or SYNCHRON) state. At startup (in tpl\_init\_os()), the system starts autostarted schedule tables and resets the bit 1.

bit 4 is the "bootstrap" bit. It's used when the first expiry point of a schedule table is dated in more than OsCounterMaxAllowedValue ticks from the current date<sup>1</sup>. It can happen when:

• the schedule table start (<tick\_val>) is after the current date and the first expiry point

<sup>&</sup>lt;sup>1</sup>As the <offset> parameter of StartScheduleTableRel() cannot be greater than **OsCounterMaxAllowed-Value** minus the **InitialOffset** of the schedule table (OS276), the first expiry point cannot be in more than **OsCounterMaxAllowedValue** ticks from the current date. Thus the "bootstrap" bit can set by StartScheduleTableAbs() only.

comes between the current date and <tick\_val>

• <tick\_val> is before the current date and the first expiry point comes after the current date

Figure 7.2 below shows a bootstrap example for the first item.

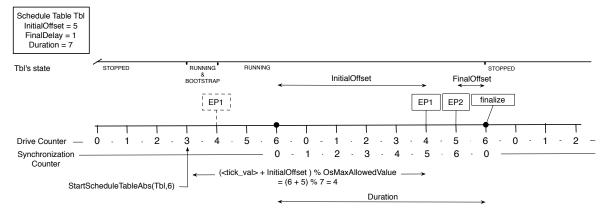


Figure 7.2: Bootstrap example

 ${f bit}$  5 is the "asynchronous" bit. It tells the system that the schedule table is in asynchronous mode.

Thus, the different states of a schedule table are described in Table?? below.

State code Binary code Associated constant 0 000000 SCHEDULETABLE\_STOPPED SCHEDULETABLE\_RUNNING 000001 1 5 000101 SCHEDULETABLE\_NEXT 9 001001 SCHEDULETABLE\_WAITING 13 001101 SCHEDULETABLE\_RUNNING\_AND\_SYNCHRONOUS SCHEDULETABLE\_AUTOSTART\_ABSOLUTE 6 000110 10 001010 SCHEDULETABLE\_AUTOSTART\_RELATIVE 14 001110 SCHEDULETABLE\_AUTOSTART\_SYNCHRON SCHEDULETABLE\_BOOTSTRAP 16 010000 32 100000 SCHEDULETABLE\_ASYNC

Table 7.1: States of a schedule table

Figure 7.3 shows how a schedule table goes from state to state.



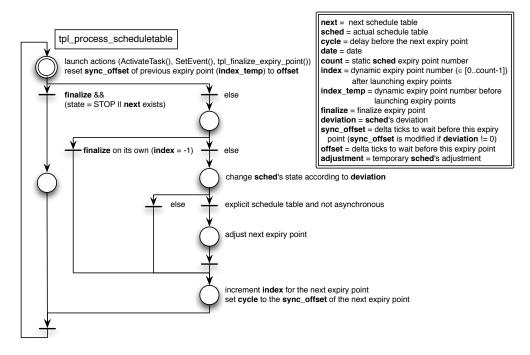
Figure 7.3: States of a schedule table in Trampoline.

## 7.2 Processing a Schedule Table

In the same time of producing the schedule tables expiry points, GOIL adds one expiry point more than the number of expiry point delared in the OIL file: the "finalize" expiry point (see Figure 7.2). Indeed, the RUNNING state of a "nexted" schedule table should be set at the finalize expiry point, thus, this expiry point has to be inserted. Moreover, for a periodic schedule table, the "finalize" expiry point helps to launch the first expiry point of the next period.

To process a **synchronized** schedule table, the schedule table's state has to be updated each expiry point and the next expiry point has to be adjusted according to the schedule table's deviation each epiry point too.

A schedule table is a time object, like an alarm. tpl\_processing\_scheduletable() is called by each expiry point (before activating a task, setting an event or finalizing a schedule table via tpl\_finalize\_expiry\_point()). The state machine of this function is shown in the Figure 7.4.



 $\textbf{Figure 7.4:} \ \textit{tpl\_process\_scheduletable's state machine}.$ 



Chapter 7. Schedule Table Implementation



tpl\_finalize\_expoiry\_point() state machine is shown in Figure 7.5 below.



 ${\bf Figure~7.5:~} tpl\_finalize\_expiry\_point's~state~machine.$ 

**EIGHT** 

# THE COMMUNICATION LIBRARY

A N OSEK/COM compliant library is part of Trampoline. This chapter presents the communication configuration and API. Implementation details as well as examples of extension are provided at the end of the chapter.

## 8.1 Implementation

## 8.1.1 Sending Message Objects

In the following paragraphs, acronyms are widely used. Here is the meaning of these acronyms:

MO Message Object

SMO Sending Message Object

RMO Receiving Message Object

### Base Sending Message Object

The Base SMO is an abstract *class* that is the common part of all SMOs. Since a SMO may be wired to an IPDU for external communication or a RMO for internal communication, the BSMO type is only a structure with one member: **sender**, a function pointer to a function doing the actual work according to the kind of SMO.



It is easy to extend the communication library by providing a sending function that will manage message sending to a different kind of destination than the standard OSEK/COM one.

The BSMO is declared as follow:

struct TPL\_BASE\_SENDING\_MO {

```
tpl_sending_func sender; /* pointer to the sending function */
};

typedef struct TPL_BASE_SENDING_MO tpl_base_sending_mo;

The sending function has the following prototype:

typedef tpl_status (*tpl_sending_func)(
    P2CONST(void, AUTOMATIC, OS_CODE),
    CONSTP2CONST(tpl_com_data, AUTOMATIC, OS_VAR));
```

The first argument is a pointer to the SMO and the second argument is a pointer to the data to be sent.

### Internal Sending Message Object

The first concrete *subclass* of tpl\_base\_sending\_mo is the tpl\_internal\_sending\_mo structure. This structure adds internal\_target, a pointer to a tpl\_base\_receiving\_mo (see 8.1.2) which is the first RMO of a chained list of RMOs:

## 8.1.2 Receiving Message Objects

## Base Receiving Message Object

The root type is the tpl\_base\_receiving\_mo structure. This structure contains two members, notification and next\_mo:

notification is a pointer to a notification descriptor and is used to perform the notification associated to the receiving message object.  $next\_mo$  is a pointer to another RMO which allows to chain RMOs  $^1$ .

### Data Receiving Message Object

An abstract *subclass* of tpl\_base\_receiving\_mo exists: tpl\_data\_receiving\_mo. This *subclass* extends tpl\_base\_receiving\_mo and adds the following data related members:

<sup>&</sup>lt;sup>1</sup>In OSEK/COM a sending message may have more than one RMO

receiver is a pointer to a receiving function (ie the function that will copy the data from a source to the destination message object). This function has the following prototype:

```
typedef tpl_status (*tpl_receiving_func)(
    void *,
    tpl_com_data *
);
```

The first argument is a pointer to the RMO and the second one is a pointer to the data to copy in the RMO

copier is a pointer to a function that is used to copy the data from the receiving message object to the application. It is called by the ReceiveMessage system service. This function has the following prototype:

```
typedef tpl_status (*tpl_data_copy_func)(
    tpl_com_data *,
    void *
);
```

The first argument is a pointer to the data to copy from the RMO and the second one is a pointer to the RMO.

filter is a pointer to a filter descriptor.

NINE

# THE INTER OS-APPLICATION COMMUNICATION LIBRARY

Inter OS-application Communication library is an API initially dedicated to communications between tasks from different OS-applications in multicore systems. However, it could also be used for communications between tasks from a same OS-Application. In the fallowing, Inter OS-application Communication will be denoted IOC. This chapter presents the IOC configuration and API. Implementation details as well as examples of utilization are provided.

## 9.1 IOC declaration in OIL

The IOC configuration is performed using OIL. Parameters such as IOC name, the type of manipulated data, the kind of communication (queued or last is best) and informations about sender/receiver are mandatory. The syntax is presented below using tow example.

Let us consider the case where a task A (as part of OS-application os-app1) sends a data to a task B (as part of OS-application os-app2). In the first case, we consider a last is best semantic communication where only one data of type u8 is sent. In the second case, we consider a queued semantic communication where a data of type u8 and a data of type mytype (defined by user) are sent. It is worth noting that this type have to be defined by user un the file ioc\_types.h at the root of the project directory.

mytype can be defined like this:

```
struct mytype {
   u8    a;
   u8    b,
}
/* LAST_IS_BEST semantic */
```

```
IOC com_A_to_B_last_is_best {
  DATATYPENAME u8 {
    DATATYPEPROPERTY = DATA;
  SEMANTICS = LAST_IS_BEST {
    INIT_VALUE_SYMBOL = AUTO;
  RECEIVER rcv {
    RCV_OSAPPLICATION = os-app2;
    RECEIVER_PULL_CB = AUTO;
    ACTION = NONE;
 };
 SENDER sender0 {
    SENDER_ID = 0;
    SND_OSAPPLICATION = os-app1;
 };
};
/* QUEUED semantic */
IOC com_A_to_B_queued {
 DATATYPENAME u8 {
   DATATYPEPROPERTY = DATA;
  DATATYPENAME mytype {
    DATATYPEPROPERTY = REFERENCE;
  SEMANTICS = QUEUED {
    BUFFER_LENGTH = 2;
  RECEIVER rcv {
    RCV_OSAPPLICATION = os-app2;
    RECEIVER_PULL_CB = AUTO;
    ACTION = NONE;
 };
  SENDER sender0 {
    SENDER_ID = 0;
    SND_OSAPPLICATION = os-app1;
  };
};
```

The DATATYPENAME parameter defines the name of the data type to be transferred. A file named *ioc\_types.h* should be created by user in order to defined new types, if any. The associated property specifies if the data is passed to sending functions by reference or by value. It is worth noting that it is possible to specify many DATATYPENAME as illustrated with the second example. In that case, the applicative sending function should have as many parameters as the number of DATATYPE specify in the OIL file. In case of a last is best semantic, the INIT\_VALUE\_SUMBOL defines the initial data value. It can be set to AUTO is there are no initial value. Otherwise, the INIT\_VALUE\_SYMBOL is a string type defined by user and the function *IOC\_init()* has to be called at the beginning of application. In case of a queued semantic, only a BUFFER\_LENGTH has to be specified. The receiver configuration requires

the setting of the target OS-application (RCV\_OSAPPLICATION), the king of task notification used when the message has arrived (ACTION = ACTIVATETASK, SETEVENT or NONE) (not functional at the moment) and the callback function to call (not functional at the moment). The sender configuration require the SENDER\_ID, as an integer, and the sender OS-application (SND\_OSAPPLICATION).

## 9.2 Implementation

The IOC is divided in two set of source files. First, the APIs (part of the OS) containing kernel functions are generic. They can be found in *ioc*/ directory. Second, specific files for the IOC configuration are generated. The IOC API is very closed to internal communication library and will not be detailed here. Let us now detailed what is generated in *tpl\_ioc\_api\_config.c*.

In case of the last is best communication (example 1), the sending operation is performed by the call of  $IocWrite\_IocName()$  function and the receiving operation, by the call of the function  $IocRead\_IocName()$ . These functions have to be called directly by user in applicative functions. The generated part of the API transmit the request to the kernel. Let us now illustrated the generated code for the first example.

```
FUNC(Std_ReturnType, OS_CODE) IocWrite_com_A_to_B_last_is_best(
  VAR(u8, AUTOMATIC) INO /* one data is send */
{
  /* only one data implies only one element in the message table */
 VAR(tpl_ioc_message, AUTOMATIC) message[1];
  VAR(Std_ReturnType, AUTOMATIC) result;
  /* Fill in the message structure with the data address and its size */
 message[0].data=(tpl_ioc_data *)&INO;
 message[0].length=sizeof(u8);
  /* Call the kernel function */
  result = IOC_Write(0, message);
  return result;
FUNC(Std_ReturnType, OS_CODE) IocRead_com_A_to_B_last_is_best(
  P2VAR(u8, AUTOMATIC, OS_APPL_DATA) INO
{
  VAR(tpl_ioc_message, AUTOMATIC) message[1];
  VAR(Std_ReturnType, AUTOMATIC) result;
 message[0].data=(tpl_ioc_data *)IN0;
  message[0].length=sizeof(u8);
  /* Call the kernel function */
  result = IOC_Read(0, message);
```

```
return result;
}
```

In the case of a queued communication, the sending and receiving operations are performed by the call of  $IocSend\_IocName()$  and  $IocReceive\_IocName()$  respectively. Generated functions would be of the same form that in last is best case.

Finally, it is possible that several senders send a same data. In that case, many senders can be defined during the OIL configuration. In the applicative functions, user have to call API functions of type  $IocWrite\_IocName\_SenderName()$  or  $IocSend\_IocName\_SenderName()$  when sending a message.

**TEN** 

# **MEMORY MAPPING**

 $\mathbf{T}^{\mathrm{Rampoline}}$  uses the memory mapping scheme defined by the AUTOSAR consortium.

**ELEVEN** 

# SYSTEM GENERATION AND COMPILATION

Trampoline is a static operating system. This means all the objects (tasks, ISR, ...) are known at compile time. This way, an application is made of tasks' code and ISRs' code, application data, and statically initialized descriptor for each object the operating system manages. A system generation tool, like goil, generates these descriptors in C files from an application configuration described in OIL or in XML. After that the Trampoline source code, the generated files and the application source code are compiled and linked together to produce an executable file as shown in figure 11.1.

## 11.1 The generated files

The following files are generated by *goil* from the OIL file or should be generated if you use a different system configuration tool. More information may be found in part ??.

File name	Usage			
tpl_app_define.h	This file contains all the configuration macros (see section 11.2)			
	and is included in all the Trampoline files to trigger conditional			
	compilation. goil generates this file using the 'tpl_app_define			
	h.goilTemplate' template file.			
tpl_app_config.h	This file contains the declarations of the constants and functions required by the OSEK and Autosar standard (like OSMAXAL-LOWEDVALUE_x, OSTICKSPERBASE_x or OSMINCYCLE_x constants for counter x). goil generates this file using the 'tplapp_config_h.goilTemplate' template file.			

tpl_app_config.c	This file contains the definitions of the constants and functions required by the OSEK and Autosar standard and the definitions of object descriptors used by Trampoline (see section ??) goil generates this file using the 'tpl_app_config_c.goilTemplate' template file.
tpl_app_custom_types.h	Some data types used by Trampoline are not statically defined. They are generated to fit size or performance criterions. For instance, the type used for a TaskType may be a byte if there is less than 256 tasks in the system and a word otherwise. This file defined these data types.
tpl_service_ids.h	This file is generated only if Trampoline is compiled with service calls implemented using a system call. It contains all the identifiers of the services used by the application according to the configuration. <i>goil</i> generates this file using the 'tpl_service_idsh.goilTemplate' template file.
tpl_dispatch_table.c	This file is generated only if Trampoline is compiled with service calls implemented using a system call. It contains the dispatch table definition. See section ??. goil generates this file using the 'tpl_dispatch_table_c.goilTemplate' template file.
tpl_invoque.S	This file is generated only if Trampoline is compiled with service calls implemented using a system call. It contains the API functions for system services. See section ??. The extension (here .S) may change according to the assembler used. goil generates this file using the 'tpl_invoque.goilTemplate' and 'service_call.goilTemplate' template files.
MemMap.h	This file is generated only if memory mapping is enabled. It contains macros for compiler abstraction memory mapping of functions and data as defined in the Autosar standard [?]. <i>goil</i> generates this file using the 'MemMap_h.goilTemplate' template file.
Compiler.h	This file is generated only if memory mapping is enabled. It contains macros for the compiler abstraction of functions and pointer qualifier as defined in the Autosar standard [?]. <i>goil</i> generates this file using the 'Compiler_h.goilTemplate' template file.
$Compiler\_Cfg.h$	This file is generated only if memory mapping is enabled. It contains macros for the compiler abstraction configuration as defined in the Autosar standard [?]. <i>goil</i> generates this file using the 'Compiler_Cfg_h.goilTemplate' template file.
script.ld	This file is generated only if memory mapping is enabled. It contains a link script to map the executable in the target memory. <i>goil</i> generates this file using the 'script.goilTemplate' template file.

The following sections give details about the content of these files.

# 11.2 The Configuration Macros

Trampoline can be compiled with various options. These options are controlled by setting the appropriate preprocessor configuration macros. These macros are usually set by goilusing the

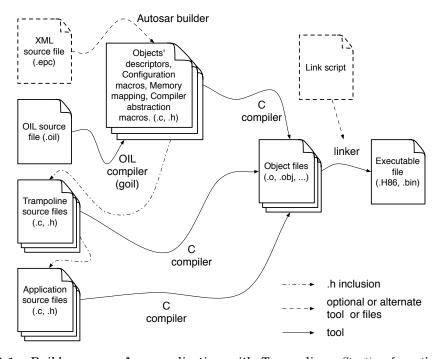


Figure 11.1: Build process of an application with Trampoline. Starting from the left, the .c and .h corresponding to the application description given in OIL (or XML) are generated by goil (or another system generation tool, for instance an Autosar compliant one) and compiled using a C compiler. Trampoline source files are compiled too and include .h from the description for configuration purpose (see section 11.2). Application files are compiled and include .h files from Trampoline. All the object files are then linked together using an optional link script generated by goil or provided with the application.

template found in 'tpl\_app\_define\_h.goilTemplate' file to produce the 'tpl\_app\_define.h' file that is included by the files of Trampoline. However, a different generation tool may be used and it should comply to the specification presented in the following tables. When Trampoline is compiled, the coherency and consistency of the configuration macros are checked, by using the preprocessor macros located in the 'tpl\_config\_check.h' file, to ensure they correspond to a supported configuration.

3 kinds of configuration macros are used: boolean macros, numerical macros, symbol macros and string macros. Boolean macros may take 2 values: YES or NO. All macros should be defined, Trampoline does not use the **#ifdef** or **#ifndef** scheme to limit the occurrences of unwanted misconfigurations except to prevent multiple inclusions of the same header file.

## 11.2.1 Number of objects macros

These macros gives the number of objects of each kind (tasks, ISRs, resources, ...) and other values. They are used in Trampoline to check the validity of the various identifiers and to define tables of the corresponding size.

Macro	Kind	Effect			
PRIO_LEVEL_COUNT	Integer	The number of priority levels used in the system.			
TASK_COUNT	Integer	The number of tasks (basic and extended) used in the sys-			
		tem.			
EXTENDED_TASK_COUNT	Integer	The number of extended tasks used in the system.			
ISR_COUNT	Integer	The number of ISR category 2 used in the system.			
ALARM_COUNT	Integer	The number of alarms used in the system.			
RESOURCE_COUNT	Integer	The number of resources used in the system.			
SEND_MESSAGE_COUNT	Integer	The number of send messages used in the system.			
RECEIVE_MESSAGE_COUNT	Integer	The number of receive messages used in the system.			
SCHEDTABLE_COUNT	Integer	The number of schedule tables used in the system. This			
		macros is only used when WITH_AUTOSAR is set to YES.			
COUNTER_COUNT	Integer	The number of counters used in the system. This macros			
		is only used when WITH_AUTOSAR is set to YES.			
APP_COUNT	Integer	The number of OS applications used in the system. This			
		macros is only used when WITH_AUTOSAR is set to YES.			
TRUSTED_FCT_COUNT	Integer	The number of trusted functions used in the system. This			
		macros is only used when WITH_AUTOSAR is set to YES.			
RES_SCHEDULER_PRIORITY	Integer	The priority of the RES_SCHEDULER resource. This should			
		be equal to the highest priority among the tasks.			

## 11.2.2 Error Handling Macros

Error handling related macros are used to configure what kind of error Trampoline checks and what extra processing is done when an error is encountered.

Macro Kine	Effect
------------	--------

WITH_OS_EXTENDED	Bool	When set to YES, Trampoline system services perform error checking on their arguments. WITH_OS_EXTENDED is set to YES with a STATUS = EXTENDED and is set to N with a STATUS = BASIC in the OIL OS object.			
WITH_ERROR_HOOK	Bool	When set to YES, the ErrorHook() function is called if an error occurs. WITH_ERROR_HOOK is set to YES/NO with a ERRORHOOK = TRUE/FALSE in the OIL OS object.			
WITH_USEGETSERVICEID	Bool	When set to YES, Trampoline system services store the id of the current service. This id may be retrieved in the ErrorHook() function by using the OSErrorGetServiceId() macro. WITHUSEGETSERVICEID is set to YES/NO with a USEGETSERVICEID = TRUE/FALSE in the OIL OS object.			
WITH_USEPARAMETERACCESS	Bool	When set to YES, Trampoline system services store the arguments of the current service. These arguments may be retrieved in the ErrorHook() function by using the ad-hoc access macros (see WITH_USEGETSERVICEID above). WITH_USEPARAMETERACCESS is set to YES/NO with a USEPARAMETERACCESS = TRUE/FALSE in the OIL OS object.			
WITH_COM_ERROR_HOOK	Bool	When set to YES, the communication error hook is called when error occurs in the communication sub-system. This macro is only available when WITH_COM is set to YES.			
WITH_COM_USEGETSERVICEID	Bool	When set to YES, Trampoline/COM system services store the id of the current service. This id may be retrieved in the COMErrorHook() function by using the COMErrorGetServiceId() macro. WITH_COM_USEGETSERVICEID is set to YES/NO with a COMUSEGETSERVICEID = TRUE/FALSE in the OIL COM object.			
WITH_COM_USEPARAMETERACCESS	Bool	When set to YES, Trampoline/COM system services store the arguments of the current service. These arguments may be retrieved in the COMErrorHook() function by using the ad-hoc access macros (see ??).  WITH_COM_USEPARAMETERACCESS is set to YES/NOwith a COMUSEPARAMETERACCESS = TRUE/FALSE in the OIL COM object.			
WITH_COM_EXTENDED	Bool	When set to YES, Trampoline/COM system services perform error checking on their arguments. WITH_COMEXTENDED is set to YES with a COMSTATUS = EXTENDED and is set to NO with a COMSTATUS = BASIC in the OIL COM object.			

## 11.2.3 Protection Macros

Protection macros deal with protection facilities provided by the AUTOSAR standard.

Macro	Kind	Effect		
WITH_MEMORY_PROTECTION	Bool	When set to YES, Trampoline enables the memory protection facility. This is only supported on some ports (MPC5510 and ARM9 at time of writing). Memory protection requires the memory mapping and the use of system call. WITH_MEMORYPROTECTION is set to YES/NO with the MEMORY_PROTECTION attribute of MEMMAP object (see ??) set to TRUE/FALSE.		
WITH_TIMING_PROTECTION	Bool	When set to YES, Trampoline enables the timing protection facility. WITH_TIMING_PROTECTION is set to YESif the AUTOSAR_SC is 2 or 4 (see ??) and a least one of the objects specifies a timing protection related attribute in the OIL file.		
WITH_PROTECTION_HOOK	Bool	When set to YES, Trampoline calls the ProtectionHook() with the appropriate argument when a protection fault occurs. WITH_PROTECTION_HOOK is set to YESwith a PROTECTIONHOOK = TRUE in the OIL OS object.		
WITH_STACK_MONITORING	Bool	When set to YES, Trampoline enables the stack monitoring. Each time a context switch occurs, the stack pointer is checked. If the stack pointer is outside the stack zone of the process, a fault occurs. WITH_STACK_MONITORING is set to YESwith a STACKMONITORING = TRUE in the oil OS object.		

## 11.2.4 Hook call macros

Hook call macros control whether a hook is called or not.

	Macro	Kind	Effect
Ī	WITH_ERROR_HOOK	Bool	see 11.2.2
	WITH_PRE_TASK_HOOK	Bool	When set to YES, each time a task is scheduled, the function PreTaskHook() is called. WITH_PRE_TASK_HOOK is set to YES/NO with a PRETASKHOOK = TRUE/FALSE in the OIL OS object.
	WITH_POST_TASK_HOOK	Bool	When set to YES, each time a task is descheduled, the function PostTaskHook() is called. WITH_POST_TASK_HOOK is set to YES/NO with a POSTTASKHOOK = TRUE/FALSE in the OIL OS object.
	WITH_STARTUP_HOOK	Bool	When set to YES, the function StartupHook() is called within the StartOS service. WITH_STARTUP_HOOK is set to YES/NO with a STARTUPHOOK = TRUE/FALSE in the OIL OS object.
	WITH_SHUTDOWN_HOOK	Bool	When set to YES, the function ShutdownHook() is called within the ShutdownOS service. WITH_SHUTDOWN_HOOK is set to YES/NO with a SHUTDOWNHOOK = TRUE/FALSE in the OIL OS object.
	WITH_PROTECTION_HOOK	Bool	see 11.2.3

## 11.2.5 Miscellaneous macros

Here are the other available macros:

Macro	Kind	Effect

WITH_USERESSCHEDULER	Bool	When set to YES, the RES_SCHEDULER resource is used by at least one process. WITH_USERESSCHEDULER is set to YES/NO with a USERESSCHEDULER = TRUE/FALSE in the OIL OS object.
WITH_SYSTEM_CALL	Bool	When set to YES, services are called by the mean of a system call, also known as a software interrupt (see section ??). WITH_SYSTEM_CALL is set to YES/NO according to the target architecture and requires a memory mapping
WITH_MEMMAP	Bool	When set to YES, a memory mapping is used. A 'MemMap.h' files giving the available memory segments is included and should be generated or provided by the user. <i>goil</i> generates such a file. WITH_MEMMAP is set to YES/NO with a MEMMAP = TRUE/FALSE in the OIL OS object.
WITH_COMPILER_SETTINGS	Bool	When set to YES, the compiler dependent macros are used. 'Compiler.h' and 'Compiler_Cfg.h' files are includes and should be generated or provided by the user. <i>goil</i> generates these files if MEMMAP is TRUE and the COMPILER sub-attribute is set.
WITH_AUTOSAR	Bool	When set to YES, Trampoline contains additional system services, code and declarations related to the AUTOSAR standard. For instance, the counter descriptor includes the counter type (hardware or software). WITH_AUTOSAR is set to YES/NO when at least one AUTOSAR object is present in the system configuration (OIL file for instance).
TRAMPOLINE_BASE_PATH	String	The path to Trampoline root directory.
AUTOSAR_SC	Integer	The AUTOSAR scalability class ranging from 0 to 4. 0 means OSEK
WITH_OSAPPLICATION	Bool	When set to YES, OS Application are used.
WITH_TRACE	Bool	When set to YES, the tracing of the operating system is enabled.
TRACE_TASK	Bool	When set to YES, task (de)scheduling events are traced. Only available if WITH_TRACE is set to YES.
TRACE_ISR	Bool	When set to YES, ISR category 2 (de)scheduling events are traced. Only available if WITH_TRACE is set to YES.
TRACE_RES	Bool	When set to YES, resources get and release are traced. Only available if WITH_TRACE is set to YES.
TRACE_ALARM	Bool	When set to YES, alarm activities are traced. Only available if WITH_TRACE is set to YES.
TRACE_U_EVENT	Bool	When set to YES, user events are traced. Only available if WITH_TRACE is set to YES.
TRACE_FORMAT	Symbol	Trace format. A function named tpl_traceformat_ <trace_format> is expected. Only available if WITH_TRACE is set to YES.</trace_format>
TRACE_FILE	String	File name where the trace is stored. Usable on Posix target only. Only available if WITH_TRACE is set to YES.

WITH_IT_TABLE	Bool	When set to YES, the external interrupts are dispatched using a table of fonction pointers.		
WITH_COM	Bool	When set to YES, internal communication is used.		
TPL_COMTIMEBASE	Integer	The COMTIMEBASE expressed in nanoseconds.		
WITH_COM_STARTCOMEXTENSION	Bool	When set to YES, the communication extension func-		
		tion is called.		

## 11.3 Application configuration

The application configuration is generated by goil using the template found in 'tpl\_app\_-config\_h.goilTemplate' file and 'tpl\_app\_config\_c.goilTemplate' file to produce the 'tpl\_-app\_define.h' and 'tpl\_app\_define.c' files.

#### 11.3.1 Counter related constants declaration

The 'tpl\_app\_config.h' files contains the counters related constants: those of the System-Counter<sup>1</sup> and those of the counters defined by the user. The SystemCounter constants are located in the generated files because the SystemCounter default attributes may be modified by the user in the OIL or XML file. The constants of a user defined counter are declared as follow:

```
extern CONST(tpl_tick, OS_CONST) OSTICKSPERBASE_<counter name>;
extern CONST(tpl_tick, OS_CONST) OSMAXALLOWEDVALUE_<counter name>;
extern CONST(tpl_tick, OS_CONST) OSMINCYCLE_<counter name>;
```

Where <counter name> is obviously the name given to the counter in the configuration. For the SystemCounter, the following constants are declared:

## 11.3.2 Events definition

The 'tpl\_app\_config.c' file should contain the event mask definitions. For each event defined in the configuration, the following lines should appear:

```
#define API_START_SEC_CONST_UNSPECIFIED
#include "tpl_memmap.h"

#define <event name > _mask <mask value >
CONST(EventMaskType, AUTOMATIC) <event name > = <event name > _mask;

#define API_STOP_SEC_CONST_UNSPECIFIED
#include "tpl_memmap.h"
```

Where <event name> is the name given to the event in the configuration and <mask value> is the value set by the user in the configuration or, when set to AUTO, the value computed by the generation tool.

<sup>&</sup>lt;sup>1</sup>the default counter of an OSEK operating system

#### 11.3.3 Standard resources definition

Standard resources need the definition of an identifier used to reference the resource in a system service (GetResource() and ReleaseResource()) and an instance of a tpl\_resource structure (see ??). This is done with the following definitions:

```
#define API_START_SEC_CONST_UNSPECIFIED
#include "tpl_memmap.h"
#define <resource name>_id <resource id>
CONST(ResourceType, AUTOMATIC) <resource name> = <resource name>_id;
#define API_STOP_SEC_CONST_UNSPECIFIED
#include "tpl_memmap.h"
#define OS_START_SEC_VAR_UNSPECIFIED
#include "tpl_memmap.h"
VAR(tpl_resource, OS_VAR) <resource name>_rez_desc = {
  /* ceiling priority of the resource */ <resource priority>,
  /* owner previous priority
                                      */ 0,
  /* owner of the resource
                                      */ INVALID_PROC_ID,
#if WITH_OSAPPLICATION == YES
  /* OS Application id
                                      */ <resource application id>,
#endif
  /* next resource in the list
                                      */ NULL
#define OS_STOP_SEC_VAR_UNSPECIFIED
#include "tpl_memmap.h"
```

Where <resource name> is the name given to the resource in the configuration, <resource priority> is the priority of the resource that is computed by the generation tool and is the maximum priority of the processes that use the resource and <resource application id> is the identifier of the OS Application the resource belongs to. Since this field is protected by WITH\_-OSAPPLICATION, it may be leaved empty when no OS Application is used.

<resource id> ranges from 0 to the number of standard resources minus 1. Once every standard resource descriptor is defined, a table gathering pointers to the resource descriptors and indexed by the resource id has to be defined. This table is used by system services to get the resource descriptor from the resource id. Suppose 3 standard resource, motor1, motor2 and dac has been defined and RES\_SCHEDULER is used, the table should be as follow:

```
#define OS_START_SEC_CONST_UNSPECIFIED
#include "tpl_memmap.h"
CONSTP2VAR(tpl_resource, AUTOMATIC, OS_APPL_DATA)
tpl_resource_table[RESOURCE_COUNT] = {
    &motor1_rez_desc,
    &motor2_rez_desc,
    &dac_rez_desc,
    &res_sched_rez_desc
};
#define OS_STOP_SEC_CONST_UNSPECIFIED
#include "tpl_memmap.h"
```

&res\_sched\_rez\_desc, the pointer to the resource descriptor of RES\_SCHEDULER should always be the last element of the table. If RES\_SCHEDULER is not used, simply remove it from the table.

#### 11.3.4 Tasks definition

Each task needs an identifier to reference a task un a system service (ActivateTask(), ChainTask(), GetTaskState(), SetEvent() and GetEvent()) and the declaration of the task function. The following definitions should appear for each task:

```
#define API_START_SEC_CONST_UNSPECIFIED
#include "tpl_memmap.h"

#define <task name>_id <task id>
CONST(TaskType, AUTOMATIC) <task name> = <task name>_id;

#define API_STOP_SEC_CONST_UNSPECIFIED
#include "tpl_memmap.h"

#define APP_Task_<task name>_START_SEC_CODE
#include "tpl_memmap.h"

FUNC(void, OS_APPL_CODE) <task name>_function(void);

#define APP_Task_<task name>_STOP_SEC_CODE
#include "tpl_memmap.h"
```

Where <task name> is the name given to the task in the configuration and <task id> is the identifier of the task computed by the system generation tool. Task ids should range from 0 to the number of tasks minus 1. In addition, id allocation must start with extended tasks first and basic task after. In addition an instance of the static task descriptor must be provided:

```
#define OS_START_SEC_CONST_UNSPECIFIED
#include "tpl_memmap.h"
CONST(tpl_proc_static, OS_CONST) <task name>_task_stat_desc = {
  /* context
                                  <task name>_CONTEXT,
  /* stack
                                  <task name>_STACK,
  /* entry point (function)
                                  <task name>_function,
  /* internal ressource
                                  <internal resource>,
  /* task id
                                  <task name>_id,
#if WITH_OSAPPLICATION == YES
  /* OS application id
                              */ <application>,
#endif
  /* task base priority
                                  <task priority>,
  /* max activation count
                                  <task activation>,
                               */ <task type>
  /* task type
#if WITH_AUTOSAR_TIMING_PROTECTION == YES
  /* pointer to the timing
     protection descriptor
                              */ ,<timing protection>
#endif
};
#define OS_STOP_SEC_CONST_UNSPECIFIED
#include "tpl_memmap.h"
```

Chapter 11. System generation and compilation

Where <task name> is the name given to the task in the configuration. <internal resource> mays be one of the following:

- a pointer to the internal resource descriptor (see ??) if an internal resource has been defined in the configuration;
- a pointer to the scheduler internal resource if the task has been defined as non-preemptable in the configuration;
- NULL if none of the above cases apply.

<application> is the id of the OS Application the task belongs to when OS Application are used or, when they are not used, nothing at all. <task priority> is the priority of the task as computed by the system generation tool. <task activation> is the maximum number of task activation allowed as defined in the configuration. <task type> may be EXTENDED or BASIC. <timing protection> is a pointer to the timing protection descriptor or NULL if no timing protection is defined for the task.

Also an instance of the dynamic task descriptor must be provided:

```
#define OS_START_SEC_VAR_UNSPECIFIED
#include "tpl_memmap.h"
VAR(tpl_proc, OS_VAR) <task name>_task_desc = {
                                        NULL,
  /* resources
#if WITH_MEMORY_PROTECTION == YES
  /* if > 0 the process is trusted
                                         <trusted count>,
#endif /* WITH_MEMORY_PROTECTION */
  /* activate count
                                         0,
  /* task priority
                                         <task priority>,
  /* task state
                                         <task state>
#if WITH_AUTOSAR_TIMING_PROTECTION == YES
                                     */ ,TRUE
  /* activation allowed
#endif
#define OS_STOP_SEC_VAR_UNSPECIFIED
#include "tpl_memmap.h"
```

Where <task name> is the name given to the task in the configuration. <trusted count> is 0 if the task belongs to a non trusted OS Application and 1 if the tasks belongs to a trusted OS Application. <task priority> is the priority of the task as computed by the system generation tool. <task state> is the initial state of the task and must be set to AUTOSTART or SUSPENDED.

If the task is an EXTENDED one, an event mask descriptor is added:

```
VAR(tpl_task_events, OS_VAR) <task name>_task_evts = {
   /* event set */ 0,
   /* event wait */ 0
};
```

Where <task name> is the name given to the task in the configuration.

68	11.3.	Application configuration
Chapter 11. System generation and compilation		

**TWELVE** 

# **IMPLEMENTATION DETAILS**

## 12.1 The tpl\_kern structure

The  $tpl\_kern$  structure gathers informations about the RUNNING process and flags to notify if a context switch and/or a context save are needed. It eases the access to these informations when programming in assembly language. The  $tpl\_kern$  structure is an instance of the  $tpl\_kern\_state$  type:

```
typedef struct
{
   P2CONST(tpl_proc_static, TYPEDEF, OS_CONST) s_old;
   P2CONST(tpl_proc_static, TYPEDEF, OS_CONST) s_running;
   P2VAR(tpl_proc, TYPEDEF, OS_VAR) old;
   P2VAR(tpl_proc, TYPEDEF, OS_VAR) running;
   VAR(int, TYPEDEF) running_id;
   VAR(u8, TYPEDEF) need_switch;
} tpl_kern_state;
```

## **THIRTEEN**

# PORTING TRAMPOLINE



In this chapter *arch* is used to designate the instruction set of the target like PowerPC<sup>®</sup>, ARM<sup>®</sup> or AVR<sup>®</sup>; *chip* is used to designate the name of an implementation of the architecture like a PowerPC 5516; *board* is used to designate the name of a development board that uses the chip. *compiler* is used to designate the compiler and *linker* is used to designate the linker used to link the project and produced the executable file.

## 13.1 Adding files to the directory structure

Doing a port of Trampoline on a new target requires

- data structures
- code, some is in C and some is in assembly language of the target
- code templates
- memory mapping templates (depend on the compiler)
- link scripts templates (depend on the linker)

Data structures declarations and code related to the instruction set are located in the 'machines/arch' directory.

Code templates are located in the 'goil/templates/code/arch' directory.

Memory mapping templates are located in the 'goil/templates/compiler/compiler/arch' directory.

Link scripts templates are located in the 'goil/templates/linker/linker/arch' directory.

For instance, if the goal is to port Trampoline to a Freescale<sup>®</sup> ColdFire<sup>®</sup> CPU with the gcc compiler and the gnu ld linker, you have to create a directory 'coldfire' inside the 'machines' directory, inside the 'goil/templates/code' directory and inside the 'goil/templates/code/gnu\_ld' directory.

In addition, some code or link scripts may be specific to the *chip* or the *board*. In this case, create sub-directories in the various *arch* directories using the pattern 'arch/chip/board' to put the corresponding files.

## 13.2 Using a target with goil

The -t or --target option of goil selects the target by using a <code>arch/chip/board</code> path. Goil will look at the code, compiler and linker templates in the corresponding paths. Goil looks for a template at the deeper path first and goes up until it find it or gives an error when it does not find it. This way, a generic <code>chip</code> level template may be overridden by a more specific <code>board</code> level template for instance.

If you use the -g or --generate-makefile option, goil generates a Makefile that includes the Makefiles that exist along the path.

The link script templates (linker) and the memory mapping templates (compiler) are used only if a project is built using memory mapping. MEMMAP is a boolean attribute of the OS object in the OIL file. COMPILER and LINKER are sub-attributes of MEMMAP when it is TRUE. For instance, a MEMMAP using gcc and gnu ld would described like that:

```
MEMMAP = TRUE {
   COMPILER = gcc;
   LINKER = gnu_ld { SCRIPT = "script.ld"; };
   ...
};
```

Using this description and the target option, goil will look for link script templates in 'goil/templates/gnu\_ld/arch/chip/board' path and for memory mapping templates in 'goil/templates/gcc/arch/chip/board' path.

The SCRIPT sub-attributes gives the name of the generated link script file.

## 13.3 Target specific code



The following informations require you use a software interrupt to call the system services.

This code should be located in the 'machines/arch' directory or in a sub-directory ('chip' or 'board') if you want to implement a feature that rely on a specific chip or board (for instance to put peripheral devices in sleep mode in the tpl\_sleep function). Anyway, you should put the relevant code at the corresponding level. If in the rare instances you may need to use conditional compiling, you may use the C macros TARGET\_ARCH, TARGET\_CHIP and TARGET\_BOARD that contains the arch, chip and board respectively as character strings.

### 13.3.1 Functions called by Trampoline

The following functions are needed by Trampoline:

```
extern FUNC(void, OS_CODE) tpl_init_context(
CONST(tpl_proc_id, AUTOMATIC) proc_id);
```

tpl\_init\_context may be written in C. It is called when an activated task runs for the first time. It initializes the context of the task by setting the 'at start' values of registers. Setting at least the values of the stack pointer at the beginning of the stack zone of the task and the return address at the entry point of the task code are required.

```
extern FUNC(void, OS_CODE) tpl_init_machine(void);
```

tpl\_init\_machine is called at the beginning of StartOS before calling the StartupHook and starting the scheduling. tpl\_init\_machine should do the hardware related initializations that are needed to run the OS (for instance starting the timer of the SystemCounter).

```
extern FUNC(void, OS_CODE) tpl_sleep(void);
```

tpl\_sleep is called from the idle task. It should implement a loop around an instruction that put the CPU in a waiting for interrupt mode. If the *arch* does not have such an instruction, an empty loop may be used.



tpl\_sleep should never return.

```
extern FUNC(void, OS_CODE) tpl_shutdown(void);
```

tpl\_shutdown is called from ShutdownOS. It should disable all interrupts and put the CPU in sleep mode. If no sleep mode exists, an empty loop may be used.



tpl\_shutdown should never return.

#### 13.3.2 Service call

A service call is done by using a software interrupt<sup>1</sup>. So any function executed by the kernel as a result of API function call is handled by the software interrupt vector.

This code is called tpl\_sc\_handler and performs the following steps:

- 1. save registers to be able to work
- 2. disable memory protection
- 3. switche to kernel stack if needed
- 4. call the service
- 5. perform a context switch if needed and programs the MPU.
- 6. switche back to the process stack if needed
- 7. enable memory protection
- 8. restore registers saved at step 1
- 9. get back to the process

<sup>&</sup>lt;sup>1</sup>swi on ARM, sc on PowerPC, syscall on Tricore

At step 4, the service identifier is used as an index in the function pointer table where all the services are stored. This table is also generated by goil (this allow to add services by your own and customize Trampoline) and is called tpl\_dispatch\_table. The function pointer corresponding to the service is read from this table and the service is called.

The identifier of the service is passed to tpl\_sc\_handler in one of the following ways:

- the software interrupt instruction of the target has an argument, the identifier of the service is passed in this argument
- the software interrupt instruction of the target does not have an argument or the argument cannot store big enough value, the identifier of the service is passed in a register or on the stack

The way the PowerPC port manages the system call is explained in details in section 14.6.

# 13.3.3 Interrupt management

External interrupt handling should follow the same steps as service call. Of course, step 4 is a little bit different: instead of using a service id, the interrupt handler uses the interrupt source number. Usually the interrupt source number is got by reading a register of the interrupt controller.

goil provides a dispatch table for interrupts. This table is filled according to the SOURCE attribute of counters and ISR category 2. This attribute must be set to a symbolic name that is found in the 'target.cfg' (located in 'goil/templates/config/arch/chip/board' path). Each entry in the 'target.cfg' file lists the correspondance between the interrupt source number and the symbolic name.

So at step 4, the interrupt handler uses the interrupt source number as an index in the  $tpl_--it\_table$ , get the corresponding interrupt handling function pointer and calls the function.

# 13.4 Target specific structures

A file named 'tpl\_machine.h' should exists in the 'machines/arch' directory. This file should contain the declarations and definitions of:

- the tpl\_stack\_word type
- the tpl\_stack\_size type
- the tpl\_context structure
- the tpl\_stack structure
- the IDLE\_ENTRY macro that should set to tpl\_sleep
- the IDLE\_STACK macro
- the IDLE\_CONTEXT macro

The tpl\_stack\_word type is used to achieved a correct alignment of the stack

The tpl\_context context structure contains one of more pointers to structures where all the registers needed for the execution context are stored. More than one pointer may be needed

because on some architectures, contexts may be split in 2 or 3 parts to store the integer context, the floating point context and the vector context for instance. This way a task doing only integer computation needs the integer context only. The other pointers are set to NULL and the context switching code does not save or restore contexts for the NULL pointers. A tpl\_context field is included in the static part of a task descriptor which may be stored in ROM. For instance, on an AVR, the context structure is declared as follow:

```
struct TPL_CONTEXT {
   avr_context *ic;
};
typedef struct TPL_CONTEXT tpl_context;

and an avr_context is defined as follow:

struct AVR_CONTEXT {
   u8 *sp;
   u8 regist[33]; // registers: RO-R15,R17-R31,SREG,R16
};
typedef struct AVR_CONTEXT avr_context;
```

The tpl\_stack stack structure contains one or more pointers to the stack and one or more stack sizes. Some ABI may use more than one stack (an example is the Infineon C166). A tpl\_stack field is included in the static part of a task descriptor. The AVR stack structure is as follow:

```
struct TPL_STACK {
   tpl_stack_word *stack_zone;
   tpl_stack_size stack_size;
};
typedef struct TPL_STACK tpl_stack;
```

The IDLE\_STACK macro should expand to a tpl\_stack initialization. This macro is used to initialize the stack in the idle task descriptor. For instance, the AVR IDLE\_STACK and the component it uses are defined like this:

```
#define SIZE_OF_IDLE_STACK 50
extern VAR(tpl_stack_word, OS_VAR)
  idle_stack[SIZE_OF_IDLE_STACK/sizeof(tpl_stack_word)];
#define IDLE_STACK { idle_stack, SIZE_OF_IDLE_STACK }
```

The IDLE\_CONTEXT should expand to a tpl\_context initialization. This macro is used to initialize the context in the idle task descriptor. For instance, the AVR IDLE\_CONTEXT and the component it uses are defined like this:

```
extern avr_context idle_task_context;

#define IDLE_CONTEXT {&idle_task_context}
```

# 13.5 Code templates

See chapter 15 for informations about the goil templates and the goil templates language.

Since service API functions perform a system call, they are to be written in assembly language. Instead of writting each of these functions by hand, they are generated by goil using 3 templates. 2 are generic, the  $3^{rd}$  one, 'service\_call.goilTemplate', is specific.

'service\_call.goilTemplate' should be located in the 'goil/templates/code/arch/' directory

For instance the ppc *arch* has the following template:

```
.global % !REAL %
% ! REAL %:
 subi r1, r1,4
                               /* make room in stack
 stw r0,0(r1)
                               /* save r0 on stack
      r0, OSServiceId_% !API % /* load the service id in r0 */
 lί
                               /* system call
 lwz r0,0(r1)
                               /* get back r0
                                                            */
 addi r1, r1,4
                               /* free the stack
                               /* returns
 blr
  .type % !REAL %,@function
  .size % !REAL %,$-% !REAL %
```

REAL and API are configuration data provided by goil. Both have a value equal to the name of the service (ActivateTask for instance). StartOS is a special case where API have the value StartOS and REAL have the value tpl\_start\_os. This is because StartOS is the only service that is called before the memory protection is turned on.

For ActivateTask, the template execution produces the following code:

```
.global ActivateTask
ActivateTask:
 subi r1, r1, 4
                                /* make room in stack
                               /* save r0 on stack
  stw r0,0(r1)
      rO,OSServiceId_ActivateTask /* load the service id in rO */
 li
                               /* system call
 SC
 lwz r0.0(r1)
                                /* get back r0
                                /* free the stack
 addi r1,r1,4
                                /* returns
 blr
```

# 13.6 Structures initialization templates

These templates are located in 'goil/templates/code/arch'.

The template 'process\_specific.goilTemplate' is used to generate the instantiation of the context and the stack of a process (task or ISR category 2).

The template 'counter\_call.goilTemplate' is used to wrap a counter interrupt source to the Trampoline function that handle counter incrementation.

# 13.7 The memory mapping and the link script templates

Memory mapping is required with software interrupts because you have to put the interrupt vectors at the good place in memory. Moreover, when you use memory protection, goil generates memory sections for each task and ISR category 2.

The 'MemMap.h' file that defines the sections is generated from the 'MemMap\_h.goilTemplate'. Files 'Compiler\_h.goilTemplate' and 'Compiler\_Cfg\_h.goilTemplate' are used to generate the 'Compiler.h' and 'Compiler\_Cfg.h' files which define the various AUTOSAR macros that assist to the specification of sections in the source files of Trampoline and of the application. These templates are found at the 'goil/templates/compiler/arch/chip/board' path.

Usually these templates depend on the *compiler* only but, for instance, the Metrowerks<sup>®</sup> C compiler uses different #pragma according to the *arch*. So memory mapping templates for the Metrowerks C compiler for PowerPC would be located in 'goil/templates/compiler/mwc/powerpc' and for HCS12 would be located in 'goil/templates/compiler/mwc/hcs12'

To do that a link script template is used. This template is located in the 'goil/templates/linker/linker/arch/chip/board' path.

The best way is to start with an existing template from a different target for the linker you use and to modify it.

8	13.7.	The memory mapping and the link script templates
Chapter 13. Portin	ng Trampoline	

**CHAPTER** 

# **FOURTEEN**

# **PORTS DETAILS**

# 14.1 PowerPC

# 14.1.1 System services

The PowerPC port uses the sc software interrupt to call system services [?]. sc stands for System Call. It saves the current PC in SRR0 register and the current MSR in SRR1 register and jump to the System Call handler.

The id of the system service to call is given in the  $r\theta$  register and  $r\theta$  save and restore are added around. For instance, the following listing gives the ActivateTask service code. These function are generated from templates by goil (see 11.1) and are part of the *invoque* layer (see ??):

```
. \verb|global| ActivateTask|
ActivateTask:
  subi r1, r1, 4
                                     /* make room on stack
  stw r0,0(r1)
                                     /* save r0
      rO,OSServiceId_ActivateTask
                                     /* load rO with the id
 li
                                     /* system call
  sc
  lwz r0,0(r1)
                                     /* restore r0
  addi r1, r1,4
  blr
  .type ActivateTask,@function
  .size ActivateTask,$$-ActivateTask
```

When the System Call begin execution, the process stack has the mapping depicted in figure 14.1.

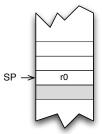


Figure 14.1: Process stack mapping at the beginning of the System Call handler. The grayed zone represents an unknown content depending on from where the service was called.

#### 14.1.2 Dispatching the service call

The System Call handler is usually located in the  $OCOO_H$  exception handler but, depending on the CPU kind, it may be located elsewhere. Since the available memory for the interrupt or exception handler may vary, a jump is made to the tpl\_sc\_handler.

tpl\_sc\_handler performs the following tasks:

- 1. saves additional registers to be able to work
- 2. disables memory protection
- 3. switches to kernel stack if needed
- 4. calls the service
- 5. performs a context switch if needed and programs the MPU.
- 6. switches back to the process stack if needed
- 7. enable memory protection
- 8. restore registers
- 9. get back to the process



Currently the PowerPC port does not support tasks that use floating point registers

#### Saving additional registers

The following registers are saved: lr, cr, r11 and r12. In fact, it should be not necessary to save r11 and r12 because these registers are volatile as defined in the PowerPC EABI [?] but we prefer a conservative approach. Register saving is done by the following code at start of the tpl\_sc\_handler and the mapping of the process stack is depicted at figure 14.2:

```
r1,r1,PS_FOOTPRINT
                           /* Make room on stack */
subi
      r11, PS_R11(r1)
                            /* Save r11
st.w
      r12, PS_R12(r1)
                            /* Save r12
stw
mflr
      r11
                            /* Save lr
      r11, PS_LR(r1)
stw
mfcr
      r11, PS_CR(r1)
                             /* Save cr
```

Chapter 14. Ports details

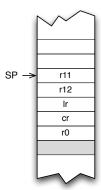


Figure 14.2: Process stack mapping after additional registers have been saved by the beginning of the System Call handler.

#### Disabling memory protection

This part of the dispatch layer is done in the  $tpl_enter_kernel$  function and is assembled only if WITH\_MEMORY\_PROTECTION is set to YES. After saving the lr, the  $tpl_kernel_mp$  function is called and does the actual job. At last lr is restored.

```
#if WITH_MEMORY_PROTECTION == YES
    Switch to kernel mem protection scheme
  subi
        r1, r1, 4
 mflr
        r11
        r11,0(r1)
                         /* save lr on the current stack */
  stw
                         /* disable memory protection
  h1
        tpl_kernel_mp
        r11,0(r1)
                         /* restore lr
  1 wz
        r11
 mtlr
  addi
        r1, r1, 4
#endif
```

#### Switching to the kernel stack

Once the dispatch layer has saved the registers it uses and has switched to the kernel memory protection scheme, it switches to the kernel stack. However the kernel stack could used already because a call to a PreTaskHook or a PostTaskHook is done on the kernel stack and such a hook may call a service. So the dispatch layer is reentrant. The number of reentrant calls is counted by the  $tpl\_reentrancy\_counter$ . In addition the process stack pointer (r1), SRR0 and SRR1 are saved in the kernel stack. The kernel stack mapping is shown in figure 14.3. For a reentrant call, the same frame is build over the current one. The switch to the kernel stack is done as follow:

```
/*
  * Check the reentrency counter value and increment it
  * if the value is 0 before the inc, then we switch to
  * the system stack.
  */
lis    r11,TPL_HIG(tpl_reentrancy_counter)
ori    r11,r11,TPL_LOW(tpl_reentrancy_counter)
```

```
r12,0(r11)
                      /* get the value of the counter */
 lwz
  cmpwi r12,0
  addi r12, r12,1
        r12,0(r11)
  stw
 bne
        no_stack_change
    Switch to the kernel stack
   * Get the pointer to the bottom of the stack
   */
        r11, TPL_HIG(tpl_kernel_stack_bottom)
  lis
       r11,r11,TPL_LOW(tpl_kernel_stack_bottom)
  ori
        r1, KS_SP-KS_FOOTPRINT(r11) /* save the sp of the caller */
  st.w
                                     /* set the kernel stack
        r1, r11
 mr
no_stack_change:
   * make space on the stack to call C functions
  subi r1,r1,KS_F00TPRINT
```

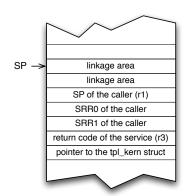


Figure 14.3: Kernel stack mapping after allocation.

#### Calling the service

Since the registers used to pass parameters to a function, that is r3 to r10 as documented in [?], have not been changed until now, calling the function that implements the service respects the register usage conventions.

The first thing to do is to get the function pointer corresponding to the service id. The service id is in  $r\theta$  as explained in 14.1.1 and is used as an index to the  $tpl\_dispatch\_table$ .

Chapter 14. Ports details

```
mtlr r11 /* put it in lr for future use */
```

The second thing to do is to reset the *need\_switch* flag that triggers a context switch. This flag (a byte) is located in the *tpl\_kern* kernel struct. This is done as follow:

```
lis r11,TPL_HIG(tpl_kern)
ori r11,r11,TPL_LOW(tpl_kern)
stw r11,KS_KERN_PTR(r1) /* save the ptr for future use */
li r0,NO_NEED_SWITCH
stb r0,20(r11)
```

In the future tpl\_kern will be reused, so its address is saved in the kernel stack.

Then, to allow reentrancy for a service call in a hook, the RI bit of the MSR is set to 1. Without that, a sc cannot be properly executed.

```
mfmsr r11
ori r11,r11,RI_BIT_1
mtmsr r11
```

At last, the service is called:

blrl

#### Context switch

The *need\_switch* flag that as been possibly modified by the service is now checked to do a context switch if needed.

```
lwz r11,KS_KERN_PTR(r1) /* get back the tpl_kern address */
lbz r12,20(r11) /* get the need_switch flag */
andi. r0,r12,NEED_SWITCH /* check if a switch is needed */
beq no_context_switch
```

A context switch is performed in 3 steps. The first one is the context save of the process that loses the CPU. This step is optional because if the service was a TerminateTask or a ChainTask, the context needs not to be saved. This information is in the <code>need\_switch</code> flag. Before doing the actual context save, the return value of the service must be saved in the proper location of the kernel stack. The <code>tpl\_save\_context</code> function will read it from this location and expects a pointer to the context saving area or the process in <code>r3. s\_old</code>, the address of the context saving area, is in another member of <code>tpl\_kern</code>. At the end, the <code>tpl\_kern</code> address is reread because <code>r11</code> has been destroyed in <code>tpl\_save\_context</code>.

```
stw r3,KS_RETURN_CODE(r1) /* save the return value */
andi. r0,r12,NEED_SAVE /* r12 contains need_switch */
beq no_save
lwz r3,0(r11) /* r11 contains the tpl_kern address */
bl tpl_save_context /* and s_old is put into r3 */
lwz r11,KS_KERN_PTR(r1) /* get back tpl_kern address */
```

The second step consists in loading the configuration of memory protection for the process that get the CPU by calling the  $tpl\_set\_process\_mp$  function. This function expects the id of the process in r3. Again this id is located in member  $proc\_id$  of  $tpl\_kern$ . This is done only if WITH\_MEMORY\_PROTECTION is YES.

```
#if WITH_MEMORY_PROTECTION == YES
  lwz r3,16(r11) /* get the id of the process which get the cpu */
  bl tpl_set_process_mp /* set the memory protection scheme */
#endif
```

The third step loads the context of the process that get the CPU. The address of  $tpl\_kern$  is loaded into r11 because it has been destroyed in  $tpl\_set\_process\_mp$ ,  $s\_running$ , the address of the context saving area of the current process is loaded into r3 and  $tpl\_load\_context$  is called. At last, r3 is restored.

```
lwz r11,KS_KERN_PTR(r1)
lwz r3,4(r11)  /* get s_running */
bl tpl_load_context
lwz r3,KS_RETURN_CODE(r1)
```

#### Switching back to the process stack

At this stage, the SRR0 and SRR1 registers saved in the kernel stack are restored. The space reserved in the kernel stack is freed. The reentrancy counter is decremented and the stack switches to the process stack if the reentrancy counter is 0.

```
r11, KS_SRRO(r1)
mtspr spr_SRR0,r11
    r11, KS_SRR1(r1)
mtspr spr_SRR1,r11
addi r1,r1,KS_FOOTPRINT
                               /* free back space on the stack */
* The reentrency counter is decremented. If it reaches
 * 0, the process stack is restored
*/
lis
     r11, TPL_HIG(tpl_reentrancy_counter)
ori
     r11,r11,TPL_LOW(tpl_reentrancy_counter)
lwz
     r12,0(r11)
                  /* get the value of the counter */
subi
     r12,r12,1
     r12,0(r11)
cmpwi r12,0
bne
     no_stack_restore
 * Restore the execution context of the caller
 * (or the context of the task/isr which just got the CPU)
    r1, KS_SP-KS_FOOTPRINT(r1)
                                /* Restore the SP and switch
lwz
                                      back to the process stack
```

#### **Enabling memory protection**

Then, if memory protection is used, the user scheme is reenabled. The actual works depends on the kind of MPU and is done in tpl\_user\_mp.

```
#if WITH_MEMORY_PROTECTION == YES
  subi
       r1,r1,4
  mflr
        r11
  stw
        r11,0(r1)
                    /* save lr on the current stack
  bl
        tpl_user_mp /* Enable the memory protection
  lwz
        r11,0(r1)
                    /* restore lr
  mtlr
  addi
        r1, r1, 4
#endif
```

#### Restoring registers

Registers saved at stage 1 on the process stack are restored an the stack is freed.

#### Getting back to the process

At last, the dispatch layer is exited using a rfi.

```
rfi /* return from interrupt */
```

# 14.1.3 Interrupt handler

#### 14.1.4 The CallTrustedFunction service

The CallTrustedFunction service is implemented by the tpl\_call\_trusted\_function\_service function. This function is a special case of service because the kernel stack and the process stack have to be modified. In addition, an ExitTrustedFunction service is implemented to restore the process stack when the trusted function exits. Both services have to be written in assembly language since C does not allow to explicitly modify the stack.

tpl\_call\_trusted\_function\_service performs the following steps:

- 1. check the trusted function id is within the allowed range
- 2. increment the trusted counter of the calling process
- 3. build a frame on the process stack to store the registers pushed by a service call except for  $r\theta$  and for  $SRR\theta$  and SRR1; put the address of ExitTrustedFunction in the lr location in the process stack; save  $SRR\theta$  and SRR1 in the process stack
- 4. get the trusted function address and put it in SRR0
- 5. go back to the dispatch layer

#### Checking the trusted function id

The id of the trusted function is checked to avoid to call a function at an arbitrary address.

#### Incrementing the trusted counter

The trusted counter of the process is incremented each time a trusted function is called. When the trusted counter is > 0, the process is trusted. In such a case, the dispatch layer does not enable memory protection when scheduling the process so it has an unlimited access to the whole addressing space.

#### Building the frame

The frame is used to store the calling context of the trusted function and is shown in figure 14.4. The following code builds this frame:

```
* First get back the process stack pointer
lwz
      r11, KS_SP(r1)
 * Make room to prepare the call of the trusted function
subi r11, r11, PS_TRUSTED_FOOTPRINT_IN
 * store ExitTrustedFunction as the return address
 */
      r12, TPL_HIG(ExitTrustedFunction)
lis
      r12, r12, TPL_LOW (ExitTrustedFunction)
      r12, PS_LR(r11)
stw
 * Update the stack pointer
 */
      r11, KS_SP(r1)
stw
 * second get back SRRO and SRR1 and save them to the process stack
 */
      r12, KS_SRRO(r1)
lwz
      r12, PS_SRRO_IN(r11)
```

Chapter 14. Ports details

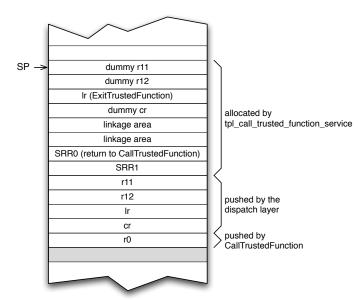


Figure 14.4: Process stack mapping at the end of tpl\_call\_ $trusted\_function\_service.$  r0, atthe bottom of the stack has been pushed by CallTrustedFunction. cr to r11 has been pushed by the dis $patch\ layer.\ SRR0\ and\ SRR1\ are$ saved here by tpl\_call\_trusted\_function\_service to be able to go back to the calling process. Above, the linkage area allows the trusted function to call functions. Above, a frame that will be used by the dispatch layer to restore an execution context for the trusted function is built.

#### Setting the trusted function address

The SRR0 saved by the dispatch layer after the CallTrustedFunction is changed to the address of the trusted function. This way, instead of returning to the caller, the trusted function will be executed.

```
lis    r11,TPL_HIG(tpl_trusted_fct_table)
ori    r11,r11,TPL_LOW(tpl_trusted_fct_table)
slwi    r0,r3,2
lwzx    r12,r11,r0
stw    r12,KS_SRRO(r1)
```

#### Going back to the dispatch layer

A simple blr goes back to the dispatch layer. The latter cleans up the process stack. Once the trusted function starts execution, the process stack is like that:

#### 14.1.5 The ExitTrustedFunction service

When a trusted function finishes, the context of the CallTrustedFunction must be restored to return to the caller. ExitTrustedFunction does not need to be called explicitly because its address has been set as the return address of the trusted function by tpl\_call\_trusted\_function\_service. Calling ExitTrustedFunction explicitly may result in an undefined behavior or in the crash of the calling process but see below. The mapping of the process stack at start of tpl\_exit\_trusted\_function\_service is shown in figure 14.6.

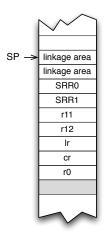
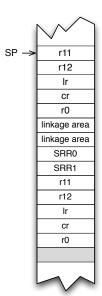


Figure 14.5: Process stack mapping when the trusted function starts its execution.



 $\begin{tabular}{ll} \textbf{Figure 14.6:} & Process \ stack \ mapping \ when \ the \ tpl\_exit\_-trusted\_function\_service \ function \ starts \ its \ execution. \end{tabular}$ 

First, tpl\_exit\_trusted\_function\_service decrements the trusted counter of the calling process. A particular attention must be given to this point because by building a fake stack frame and calling Explicitly ExitTrustedFunction to underflow this counter, a process could get a full access to the memory. So the counter is tested before to avoid to go under 0.

```
r11, KS_KERN_PTR(r1) /* get the ptr to tpl_kern */
lwz
      r11,12(r11)
lwz
                          /* get the ptr to the running process desc */
     r12,4(r11)
                          /* get trusted_count member */
1 wz
* Warning, the trusted counter has to be check (compared to 0) to
* avoid to decrement it if it is already O. Without that a process
 * could build an had-hoc stack an call explicitly ExitTrustedFunction
 * to get access to all the memory.
                          /* check it is not already at 0 */
cmpwi r12,0
                          /* uh uh */
beq
     cracker_in_action
                          /* decrement it */
subi
     r12,r12,1
     r12,4(r11)
                          /* put it back in the process desc */
stw
```

 $tpl_exit_trusted_function_service$  has to remove from the process stack the frame that was built by  $tpl_call_trusted_function_service$ , restore  $SRR\theta$  and SRR1 before returning to the dispatch layer.

```
cracker_in_action:
```

```
* get the process stack pointer
      r11, KS_SP(r1)
lwz
 * get back the SRRO and SRR1
lwz
      r12, PS_SRRO_OUT(r11)
stw
      r12, KS_SRRO(r1)
      r12, PS_SRR1_OUT(r11)
lwz
      r12, KS_SRR1(r1)
stw
 * free the process stack and update it in the kernel stack
     r11,r11,PS_TRUSTED_FOOTPRINT_OUT
addi
      r11, KS_SP(r1)
stw
 * that's all
 */
blr
```

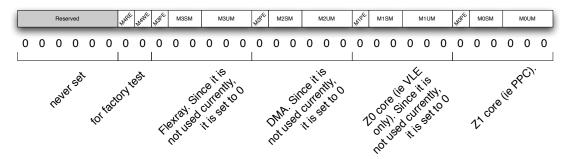
# 14.1.6 Execution of the OS Applications startup and shutdown hooks

These hooks are executed from the kernel but with the access right of a task belonging to the OS Application. The system generation tool should choose one of the tasks of the OS Application to be used as context to execute the OS Application startup and shutdown hooks. Execution of an OS Application startup hook is done by the tpl\_call\_startup\_hook\_and\_resume function. The argument of this function is a function pointer to the hook. Similarly execution of an OS Application shutdown hook is done by the tpl\_call\_shutdown\_hook\_and\_resume function. These functions end by a call to NextStartupHook and NextShutdownHook services respectively to cycle through the hooks.

# 14.1.7 The MPC5510 Memory Protection Unit

The access control rights of the memory region descriptor rules the access of 5 bus masters (labeled from 4 to 0). Unused bus masters are set to the same access right for all the regions. Bus master 4 is used for factory testing only, so the access rights should be set to no access. Bus master 3 is the Flexray controller. Since it is not used in the current version of Trampoline, it is set to no access too. Bus master 2 is the DMA controller and for the same reason it is set to no access. Bus master 1 is the Z0 core. Again it is set to no access.

The access control rights register has the following bit usage:



Bus master 4 is a special case. The 2 bits have the following meaning:

$\mathbf{Bit}$	Meaning
M4RE	If set to 1, bus master 4 may <b>read</b> memory in the region. If 0, no read is allowed
M4WE	If set to 1, bus master 4 may write memory in the region. If 0, no write is allowed

So in our case, these bits are set to 0.

Of course, other bus masters have more sophisticated access right:

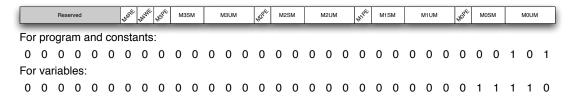
${f Bit}$	Meaning
MxPE	The PID Enable bit. Set to 0 in our case
MxSM	These 2 bits rules the supervisor mode access control with the following meaning: $00 = rwx$ , $01 = rx$ , $10 = rw$ , $11 = same$ as defined by MxUM. In our case, it is set to 00 for code and constants and to 11 for data.
MxUM	These 2 bits rules the user mode access control. The first bit means $r$ , the second one $w$ and the third one $x$ .

Chapter 14. Ports details

Trampoline uses 4 descriptors:

Descriptor	Usage	MxUM value
MPU_RGD0	Constants and program <sup>1</sup> .	rwx = 00 for supervisor mode, $rx = 101$
		for user mode.
MPU_RGD1	Private variables of the process.	rw = 110 for supervisor and user mode.
MPU_RGD2	Stack of the process.	rw = 110 for supervisor and user mode.
MPU_RGD3	Variables of the OS Application	rw = 110 for supervisor and user mode.
	if OS Applications are used.	

So values of access control bits should be:



So in hexa:

Kind	Value
Program region access	0x00000005
Variable region access	0x0000001E

#### What happen in case of memory protection violation?

Two exception handler are used to handle a memory protection violation, one for data access, one for code access.

The Data Storage exception is tied to the IVOR 2 vector (VPR offset = 0x020), see page 8-2 of the MPC5510 Microcontroller Family Reference Manual.

The Instruction Storage exception is tied to the IVOR 3 vector (VPR offset = 0x030), see page 8-2 of the MPC5510 Microcontroller Family Reference Manual.

However, it appears one of these exceptions is raised when the processor is in user mode. The behavior is different in supervisor mode to be completed.

<sup>&</sup>lt;sup>1</sup>This region is set to the whole addressing space. This is not definitive and should be improved because reading a peripheral control register should be protected. So an additional descriptor has to be used to allow the kernel (supervisor mode) a complete access on all the memory space and no access at all for applications (user mode).

# 14.2 ARM – Common conventions

# 14.2.1 File hierarchy

#### 14.2.2 Common definitions

# 14.2.3 Bootstraping

The bootstrap must be made in specific ARM port and must call the main function. If main ever returns, the bootstrap code must fall into an infinite loop.

As a reason, many ARM architectures needs early specific and required initializations. This includes steps like memory mapping configuration, DRAM controller configuration, ...

Besides specific initializations, the bootstrap should:

- initialize stack pointer for every ARM exception modes
- keep all external interrupts locked (will be unlocked at the first task context loading)
- call main in "system" mode (0x1F)

#### 14.2.4 Stacks

#### 14.2.5 Interrupt management

Kernel is not interruptible. So hardware interrupt source are disabled entering in kernel (via any case in system call, interrupt request, abort, ...).

But kernel shall be reentrant via system call (because kernel hooks can call some system calls).

# Interrupt and category classification

All ARM IRQ are category 2 ISR.

All ARM FIQ are category 1 ISR.

#### Vector table

Each ARM exception vector points on a so called "primary" subprogram (like tpl\_primary\_syscall\_handler).

To be located at address 0x00000000, this vector table is assigned to a specific section named .vectbl. The linker script uses this section name to output it to address 0x00000000.

System call

IRQ handling

FIQ handling

# 14.3 ARM – ARM926 chip support

# 14.3.1 Memory protection

To be written...

Some points to explain:

- FCSE mechanism is not used by this port (if someone is interested by this work, she's welcome)
- address translation is not used, all VMA equals physical address
- IDLE task's memory protection configuration is used to provide configuration for trusted applications or kernel

#### MMU tables generation principle

To be written...

Some points to explain:

- MMU is not disabled in privileged mode, but all useful memory areas are accessible. Thus, we hope we can find bugs easily in privileged code.
- useful memory areas, except processes and applications ones, are configured as accessible (read and write) in privileged mode. These memory areas are called system areas
- some memory areas needs to be accessible by anyone (API, GCC builtin functions, common libraries, ...), they are called common areas (they are read only for unprivileged contexts)
- there is one translation table for each process
- all translation tables have the same system and common areas
- there is one page table set for each process. Page tables are fine page tables. Table entries are tiny page descriptors.
- the number of page table in a set depends on the size of the whole trampoline and application memory footprint. Then this information is given by linker via a symbol which is used by the MMU driver.
- Page tables are accessed via a macro, as they are allocated by linker (and we cannot know the number of page tables)

# 14.3.2 CPU cache support

# 14.4 ARM – Armadeus APF27 board

# 14.4.1 Debugging with Abatron BDI2000 or BDI3000 JTAG probe

A configuration file is provided in 'machines/arm/arm926/armadeus-apf27/bdi-config'.

To enable JTAG, if your APF27 has a FPGA, you must load the FPGA to wake it up (TO DO : explain how to do this...).

To start a debug session, follow these steps:

- 1. connects everything together
- 2. power up everything
- 3. reset the APF27 (S2 on APF27-Dev)
- 4. stop u-boot before it loads Linux (if MMU is started, you won't be able to load anything)
- 5. telnet your BDI
- 6. type reset command in the BDI shell
- 7. start GDB session (target remote ...)

# 14.4.2 Configuration

All configuration of port is done in 'apf27\_config.h'.

#### Stacks

Stacks' size (stack of each exception mode) can be adjusted via the following constants. Remember that the size must be aligned to 4.

#### CPU caches

By default, CPU caches are disabled (for real time determinism).

# 14.4.3 Memory mapping

This port can be use in one of these three configurations:

- 1. No memory mapping (and thus no memory protection)
- 2. Memory mapping without memory protection
- 3. Memory mapping and memory protection

# 14.4.4 Memory protection

Memory protection is based on ARM926 shared code (see ?? page ??)

# 14.5 ARM - Simtec EB675001 board

# 14.5.1 Memory map and hardware resources

Talk about configured memory map (use of DRAM, where the bootstrap would be flashed,  $\dots$ ). Tell which hardware resources are used by the kernel.

#### 14.5.2 Booting

There is two way to start Trampoline on APF27:

- from ELF image (in file usually called 'trampoline')
- from raw binary image (in file usually called 'trampoline.bin')

#### Booting from ELF image

Load image with your ELF loader (the file is usually named 'trampoline'). This can be GDB via a JTAG probe for example. Then, just start execution from tpl\_arm\_bootstrap\_entry entry point. Here are commands you can type in GDB:

```
(gdb) load
(gdb) set $pc=tpl_arm_bootstrap_entry
(gdb) break main
(gdb) continue
```

#### Booting from raw binary image

Load image with your binary loader to 0xA0000000 memory address. Then just start execution at this point (0xA0000000).

With u-boot, you can type these commands :

```
BIOS> tftpboot 0xA0000000 192.168.5.20:trampoline.bin BIOS> go 0xA0000000
```

96 14.6. AVR8

- 14.5.3 Internal kernel drivers
- 14.5.4 Hardware interrupts handling
- 14.5.5 Idle task
- 14.5.6 Exceptions handling
- 14.5.7 Kernel sleep service

#### 14.6 AVR8

The AVR port does not use the system call version of trampoline, as this micro-controller does not have any MMU. System calls are just standard function calls.

#### 14.6.1 Context

The context of the AVR is composed of:

- the stack pointer SP
- the program counter PC
- the status register SREG and
- 32 GPRs r0 to r31. This includes the 3 16-bits indirect registers X, Y and Z which are mapped on these GPRs.

However, the context switch uses the stack to save all theses registers (except SP obviously) and the context does not use any structure to save only one variable:

```
typedef u16 avr_context;
typedef avr_context *tpl_context;
```

#### 14.6.2 Context switch

The context switch implementation uses intensively the stack. The two functions for context switches point to the same code:

```
void tpl_switch_context(tpl_context *old, tpl_context *new);
void tpl_switch_context_from_it(tpl_context *old, tpl_context *new);
```

- save the current context (if old is not NULL);
- restore the context from new

14.6. AVR8

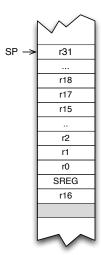


Figure 14.7: Stack of the old\_context at the end of context switch

registers are pushed on the stack like in figure 14.7. The GPR r16 is used during the context switch and is the first on the stack. Then, the status register is saved, and all the remaining registers starting from r0.

The restauration of the new contexte just get the new stack pointer from the argument tpl\_context and pop all these registers.

#### 14.6.3 Context init

The initialization of the context init (tpl\_init\_context)the stack according to be compliant with the context switch.



All the AVR8 do not have the same size of the program counter!! Most of them use a 16-bit program counter, while a few ATMega use a 24-bit program counter!! (if there is more than 64ko of program flash).

So, the AVR kind should be defined:

The stack at the end of context init should be like in figure 14.8:

• The TerminateTask or CallTerminateISR2 is pushed, depending on the type of the process

98 14.6. AVR8

(task or ISR2)

- the PC of the entry point of the process is pushed
- the rest of the context is pushed. All GPRs are init to 0x0, and the status register SREG to 0x80 to enable interrupts.

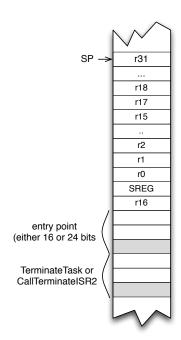


Figure 14.8: Stack at the end of contact init

# 14.6.4 Interrupts

Interrupts are handled directly using the standard way by the compiler GCC. GCC save all the required registers, which are restored at the end of the interrupt.

It should be noted that GCC uses the ISR macro to define an interrupt handler, which is in conflict with the ISR macro defined in Trampoline.

```
//gcc uses ISR as a keyword to define an interrupt handler.
//Osek uses ISR to define an ISR2 :-/
#ifdef ISR
#undef ISR
#undef ISR
#endif
#include <avr/interrupt.h>

ISR(TIMER2_OVF_vect)
{
    tpl_counter_tick(&SystemCounter_counter_desc);
    if (tpl_kern.need_schedule)
    {
```

Chapter 14. Ports details

14.6. AVR8

```
tpl_schedule_from_running();
    LOCAL_SWITCH_CONTEXT()
}
```

100 14.6. AVR8

# Part II The Goil system generator

**CHAPTER** 

# **FIFTEEN**

# THE GOIL TEMPLATES

Goil includes a template interpreter which is used for file generation. Goil generates the structures needed by trampoline to compile the application and may generate other files like a memory mapping file 'MemMap.h', the compiler abstraction files, 'Compiler.h' and 'Compiler\_cfg.h' and a linker script depending on which attributes you set in the OIL file.

A template is a file which is located in the default template directory (set with the environment variable GOIL\_TEMPLATES or with the --templates option on the command line) or in the directory of your project. Goil starts by looking for a template in the directory of your project, then, if the template is not found, in the default templates directory.

Four sets of templates are used:

- code generation templates that are located in the 'code' subdirectory of the template directory;
- build system templates that are located in the 'build' subdirectory;
- compiler dependent stuff in the 'compiler' subdirectory and
- linker script templates in the 'linker' subdirectory.

Templates are written using a simple language which allow to access the application configuration data and to mix them with text to produce files.

Files are produced by a template program located in the 'root.goilTemplate' file which is as the root of the template directory. By default the following files are produced:

- 'tpl\_app\_config.c' by using the 'tpl\_app\_config.c.goilTemplate' file
- 'tpl\_app\_config.h' by using the 'tpl\_app\_config.h.goilTemplate' file
- 'Makefile' (if option -g or --generate-makefile is given) by using the 'Makefile.goilTemplate' file

- 'script.ld' (if memory mapping is used and if the default name is not changed) by using the 'script.goilTemplate' file
- 'MemMap.h' (if memory mapping is used) by using the 'MemMap.h.goilTemplate' file
- 'Compiler.h' (if memory mapping is used) by using the 'Compiler.h.goilTemplate' file
- 'Compiler\_Cfg.h' (if memory mapping is used) by using the 'Compiler\_Cfg.h.goilTemplate' file

# 15.1 The configuration data

The configuration data are computed by Goil from the OIL source files, from the options on the command line and from the 'target.cfg' file. They are available as a set of predefined boolean, string, integer or list variables. All these variables are in capital letters.



Some configuration data are not listed here because they depend on the target. For instance, the STACKSIZE data may be an attribute of each item of a TASKS list for ppc target but are missing for the c166 target.

# 15.1.1 The PROCESSES, TASKS, BASICTASKS, EXTENDEDTASKS, ISRS1 and ISRS2 lists

Theses variables are lists where informations about the processes<sup>1</sup> used in the application are stores:

${f List}$	Content
PROCESSES	the list of processes. The items are sorted in the following order: extended
	tasks, then basic tasks, then ISRs category 2.
TASKS	the list of tasks, basic and extended. The items are sorted in the following
	order: extended tasks, then basic tasks.
BASICTASKS	the list of basic tasks.
EXTENDEDTASKS	the list of extended tasks.
ISRS1	the list of ISR category 1.
ISRS2	the list of ISR category 2.

Each item of these lists has the following attributes:

Item	Type	Content
NAME	string	the name of the process.
PROCESSKIND	string	the kind of process: "Task" or "ISR".
EXTENDEDTASK	boolean	true if the process is an extended task, false otherwise.
NONPREEMPTABLE	boolean	true if the process is a non-preemptable task, false otherwise.
PRIORITY	integer	the priority of the process.

<sup>&</sup>lt;sup>1</sup>In Trampoline, a process is a task or an ISR category 2.

Item	Type	Content
ACTIVATION	integer	the number of activation of a task. 1 for and extended task
		or an ISR.
AUTOSTART	boolean	true if the process is an autostart task, false otherwise.
USEINTERNALRESOURCE	boolean	true if the process is a task that uses an internal resource,
		false otherwise.
INTERNALRESOURCE	string	the name of the internal resource if the process is a task
		that uses an internal resource, empty string otherwise.
RESOURCES	list	The resources used by the process. Each item has the fol-
		lowing attribute: NAME
TIMINGPROTECTION	struct	The timing protection attributes. This attribute does not
		exist if no timing protection is defined for the process. See
		below for the content of this struct.

The  $\it TIMINGPROTECTION$  struct has the following sub-attributes:

Item	Type	Content
EXECUTIONBUDGET	integer	The execution budget of a task. This attribute is not
		defined for an ISR.
EXECUTIONTIME	integer	The execution time of an ISR. This attribute is not de-
		fined for a Task.
TIMEFRAME	integer	The time frame.
MAXOSINTERRUPTLOCKTIME	integer	The maximum locking time of OS interrupts.
MAXALLINTERRUPTLOCKTIME	integer	The maximum locking time of all interrupts.
RESOURCESLOCK	list	The maximum locking time of resources.

Each element of the RESOURCESLOCK list has the following attributes:

Item	Type	Content
RESOURCENAME	string	The name of the locked resource.
LOCKTIME	integer	The maximum locking time of the resource.

# 15.1.2 The COUNTERS, HARDWARECOUNTERS and SOFTWARECOUNTERS lists

These list contains all the informations about the counters used in the application, including the SystemCounter.

List	Content
COUNTERS	the list of counters, both hardware and software as long as the
	SystemCounter.
HARDWARECOUNTERS	the list of hardware counters including the SystemCounter.
SOFTWARECOUNTERS	the list of software counters.

Each item of this list has the following attributes:

Item	Type	Content
NAME	string	the name of the counter.

Item	Type	Content
TYPE	string	the type: "HARDWARE_COUNTER" or "SOFTWARE_COUNTER".
MAXALLOWEDVALUE	integer	the maximum allowed value of the counter.
MINCYCLE	integer	the minimum cycle value of the counter.
TICKPERBASE	integer	the number of ticks needed to increment the counter.
SOURCE	string	the interrupt source name of the counter. This is be used to
		wrap interrupt vector to a counter incrementation function.

# 15.1.3 The EVENTS list

This list contains the informations about the events of the application. Each item has the following attributes:

$\mathbf{Item}$	Type	Content
NAME	string	the name of the event.
MASK	integer	the mask of the event.

# 15.1.4 The ALARMS list

This list contains the informations about the alarms of the application. Each item has the following attributes:

Item	Type	Content
NAME	string	the name of the alarm.
COUNTER	string	the name of the counter that drives the alarm.
ACTION	string	the action to be done when the alarm expire. It can take the following values: "setEvent", "activateTask" and "callback". The last action is not available in AUTOSAR mode.
TASK	string	the name of the task on which the action is performed. This attribute is defined for "setEvent" and "activateTask" actions only.
EVENT	string	the name of the event to set on the target task. This attribute is defined for "setEvent" action only.
AUTOSTART	boolean	true if the alarm is autostart, false otherwise
ALARMTIME	integer	the alarm time of the alarm. This attribute is set if AUTOSTART is true.
CYCLETIME	integer	the cycle time of the alarm. This attribute is set if AUTOSTART is true.
APPMODE	string	the application mode in which the alarm is autostart. This attribute is set if AUTOSTART is true.

# 15.1.5 The REGULARRESOURCES and INTERNALRESOURCES lists

These lists contains the informations about the resources of the application.

List	Content
REGULARRESOURCES	the list of STANDARD and LINKED resources.
INTERNALRESOURCES	the list of INTERNAL resources.

Each item has the following attributes:

Item	Type	Content
NAME	string	the name of the resource.
PRIORITY	integer	the priority of the resource.
TASKUSAGE	list	the list of tasks that use the resource. Each item of this list has an attribute NAME which is the name of the task.
ISRUSAGE	list	the list of ISRs that use the resource. Each item of this list has an attribute NAME which is the name of the ISR.

# 15.1.6 The MESSAGES, SENDMESSAGES and RECEIVEMESSAGES lists

These lists contain the informations about the messages of the application.

$\mathbf{List}$	Content
MESSAGES	the list of messages, both send and receive message.
SENDMESSAGES	the list of send messages.
RECEIVEMESSAGES	the list of receive messages.

Each item has the following attributes

Item	Type	Content
NAME	string	the name of the message.
MESSAGEPROPERTY	string	the type of the message. It can be "RECEIVE_ZEROINTERNAL", "RECEIVE_UNQUEUED_INTERNAL", "RECEIVE_QUEUEDINTERNAL", "SEND_ZERO_INTERNAL" or "RECEIVE_ZERO_SENDERS".
NEXT	string	the name of the next message in a receive message chain. This attribute is defined for receive messages only.
SOURCE	string	the name of the send message which is connected to the receive message. This attribute is defined for receive messages only.
СТҮРЕ	string	the C language type of the message. This attribute is not defined for "RECEIVE_ZERO_INTERNAL" and "SEND_ZERO_INTERNAL" messages.
INITIALVALUE	string	initial value of the receive message. This attribute is defined for "RECEIVE_UNQUEUED_INTERNAL" and "RECEIVE_ZERO_SENDERS" messages only.
QUEUESIZE	integer	queue size of a receive queued message. This attribute is defined for "RECEIVE_QUEUED_INTERNAL" messages only.
TARGET	string	target message of a send message. This is the first message in a receive message chain. This attribute is defined for "SENDSTATIC_INTERNAL" and "SEND_ZERO_INTERNAL" messages only.

Item	Type	Content
FILTER	string	the kind of filter to apply. This attribute may take the following values: "ALWAYS", "NEVER", "MASKEDNEWEQUALSX", "MASKEDNEWDIFFERSX", "NEWISEQUAL", "NEWISDIFFERENT", "MASKEDNEWEQUALSMASKEDOLD", "MASKEDNEWDIFFERSMASKEDOLD", "NEWISWITHIN", "NEWISOUTSIDE", "NEWISGREATER", "NEWISLESSOREQUAL", "NEWISLESS", "NEWISGREATEROREQUAL" or "ONEEVERYN".
MASK	integer	Mask of the filter when needed. This attribute is defined for "MASKEDNEWEQUALSX", "MASKEDNEWDIFFERSX", "MASKEDNEWQUALSMASKEDOLD" and "MASKEDNEWDIFFERSMASKEDOLD" filters only.
x	integer	Value of the filter when needed. This attribute is defined for "MASKEDNEWEQUALSMASKEDOLD" and "MASKEDNEWDIFFERSX" filters only.
MIN	integer	Minimum value of the filter when needed. This attribute is defined for "NEWISWITHIN" and "NEWISOUTSIDE" filters only.
MAX	integer	Maximum value of the filter when needed. This attribute is defined for "NEWISWITHIN" and "NEWISOUTSIDE".
PERIOD	integer	Period of the filter. This attribute is defined for "ONEEVERYN" filter only.
OFFSET	integer	Offset of the filter. This attribute is defined for "ONEEVERYN" filter only.
ACTION	string	the action (or notification) to be done when the message is delivered. It can take the following values: "setEvent" or "activateTask".
TASK	string	the name of the task on which the notification is performed. This attribute is defined for "setEvent" and "activateTask" actions only.
EVENT	string	the name of the event to set on the target task. This attribute is defined for "setEvent" notification only.

# 15.1.7 The SCHEDULETABLES list

This list contains the informations about the schedule tables of the application.

Item	Type	Content
NAME	string	the name of the schedule table.
COUNTER	string	the name of the counter which drives the schedule table.
PERIODIC	boolean	true if the schedule table is a periodic one, false otherwise.
SYNCSTRATEGY	string	the synchronization strategy of the schedule table. This attribute may take the following values: "SCHEDTABLE_NO_SYNC", "SCHEDTABLE_IMPLICIT_SYNC" or "SCHEDTABLE_EXPLICIT_SYNC".
PRECISION	integer	the precision of the synchronization. This attribute is define when SYNCSTRATEGY is "SCHEDTABLE_EXPLICIT_SYNC".
STATE	string	the state of the schedule table. This attribute may take the following values: "SCHEDULETABLE_STOPPED", "SCHEDULETABLEAUTOSTART_SYNCHRON", "SCHEDULETABLE_AUTOSTART_RELATIVE" or "SCHEDULETABLE_AUTOSTART_ABSOLUTE".

Chapter 15. The Goil templates

Item	Type	Content
DATE	integer	the start date of the schedule table. This attribute has an
		actuel value when STATE is "SCHEDULETABLE_AUTOSTART_RELATIVE" or
		"SCHEDULETABLE_AUTOSTART_ABSOLUTE", otherwise it is set to 0.
LENGTH	integer	The length of the schedule table.
EXPIRYPOINTS	list	The expiry points of the schedule table. See the following table for
		items attributes.

Each item of the  ${\tt EXPIRYPOINTS}$  list has the following attributes:

Item	Type	Content		
ABSOLUTEOFFSET	integer	the absolute offset of the expiry points.		
RELATIVEOFFSET	integer	the relative offset of the expiry points from the previous expiry point.		
MAXRETARD	integer	maximum retard to keep the schedule table synchronous.		
MAXADVANCE	integer	maximum advance to keep the schedule table synchronous.		
ACTIONS	list	the actions to perform on the expiry point. See the following table for items attributes.		

Each item of the  ${\tt ACTIONS}$  list has the following attributes:

Item	Type	Content
ACTION	string	the action to be done when the alarm expire. It can take the fol-
		lowing values: "setEvent", "activateTask", "incrementCounter" and
		"finalizeScheduleTable".
TASK	string	the name of the task on which the action is performed. This at-
		tribute is defined for "setEvent" and "activateTask" actions only.
EVENT	string	the name of the event to set on the target task. This attribute is
		defined for "setEvent" action only.
TARGETCOUNTER	string	the name of the counter to increment. This attribute is defined for
		"incrementCounter" action only.

# 15.1.8 The OSAPPLICATIONS list

This list contains the informations about the OS Applications of the application.

Item	Type	Content
NAME	string	the name of the OS Application.
RESTART	string	the name of the restart task. This attribute is not
		defined is there is no restart task for the OS Appli-
		cation.
PROCESSACCESSVECTOR	string	access right for the processes
PROCESSACCESSITEMS	string	access right for the processes as bytes in a table
PROCESSACCESSNUM	integer	number of elements in the previous table
ALARMACCESSVECTOR	string	access right for the alarms
ALARMACCESSITEMS	string	access right for the alarms as bytes in a table
ALARMACCESSNUM	integer	number of elements in the previous table
		•

Item	Type	Content
RESOURCEACCESSVECTOR	string	access right for the resources
RESOURCEACCESSITEMS	string	access right for the resources as bytes in a table
RESOURCEACCESSNUM	integer	number of elements in the previous table
SCHEDULETABLEACCESSVECTOR	string	access right for the schedule tables
SCHEDULETABLEACCESSITEMS	string	access right for the schedule tables as bytes in a table
SCHEDULETABLEACCESSNUM	integer	number of elements in the previous table
COUNTERACCESSVECTOR	string	access right for the software counters
COUNTERACCESSITEMS	string	access right for the software counters as bytes in a table
COUNTERACCESSNUM	integer	number of elements in the previous table
PROCESSES	list	list of the processes that belong to the OS Applica- tion. Each item has an attribute NAME which is the name of the process.
HASSTARTUPHOOK	boolean	true if the OS Application has a startup hook.
HASSHUTDOWNHOOK	boolean	true if the OS Application has a shutdown hook.
TASKS	list	list of the tasks that belong to the OS Application. Each item has an attribute NAME which is the name of the task.
ISRS	list	list of the ISRs that belong to the OS Application. Each item has an attribute NAME which is the name of the ISR.
ALARMS	list	list of the alarms that belong to the OS Application. Each item has an attribute NAME which is the name of the alarm.
RESOURCES	list	list of the resources that belong to the OS Applica- tion. Each item has an attribute NAME which is the name of the resource.
REGULARRESOURCES	list	list of the standard or linked resources that belong to the OS Application. Each item has an attribute NAME which is the name of the resource.
INTERNALRESOURCES	list	list of the internal resources that belong to the OS Application. Each item has an attribute NAME which is the name of the resource.
SCHEDULETABLES	list	list of the schedule tables that belong to the OS Application. Each item has an attribute NAME which is the name of the schedule table.
COUNTERS	list	list of the counters that belong to the OS Applica- tion. Each item has an attribute NAME which is the name of the counter.
MESSAGES	list	list of the messages that belong to the OS Applica- tion. Each item has an attribute NAME which is the name of the messages.

# 15.1.9 The TRUSTEDFUNCTIONS list

This list contains the informations about the trusted functions of the application. Each item contains one attribute only.

$\mathbf{Item}$	Type	Content
NAME	string	the name of the trusted function.

#### 15.1.10 The READYLIST list

This list contains the informations about the ready list. Items are sorted by priority from 0 to the maximum computed priority. The only attribute of each item is the size of the queue.

Item	Type	Content
SIZE	integer	the size of the queue for the corresponding priority.

# 15.1.11 The SOURCEFILES, CFLAGS, CPPFLAGS, ASFLAGS, LDFLAGS and TRAMPOLINESOURCEFILES lists

The SOURCEFILES list contains the source files as found in attributes APP\_SRC of the OS object in the OIL file. Each item in the list has one attribute.

Item	Type	Content
FILE	string	the source file name.

The *CFLAGS* list contains the flags for the C compiler as found in attributes *CFLAGS* of the OS object in the OIL file. Each item in the list has one attribute.

Item	Type	Content
CFLAG	string	the C compiler flag.

The *CPPFLAGS* list contains the flags for the C++ compiler as found in attributes *CPPFLAGS* of the OS object in the OIL file. Each item in the list has one attribute.

Item	Type	Content
CPPFLAG	string	the C++ compiler flag.

The ASFLAGS list contains the flags for the assembler as found in attributes ASFLAGS of the OS object in the OIL file. Each item in the list has one attribute.

$\mathbf{Item}$	Type	Content
ASFLAG	string	the assembler flag.

The *LDFLAGS* list contains the flags for the linker as found in attributes *LDFLAGS* of the OS object in the OIL file. Each item in the list has one attribute.

Item	$\mathbf{Type}$	Content
LDFLAG	string	the linker flag.

The TRAMPOLINESOURCEFILES list contains the trampoline source files used by the application. Each item in the list has two attributes.

Item	Type	Content
DIRECTORY	string	the directory of the source file relative to the Trampoline root directory
		('os', 'com' or 'autosar').
FILE	string	the source file name.

# 15.1.12 The INTERRUPTSOURCES list

This list is extracted from the 'target.cfg' file. Each item has the following attributes:

Item	Type	Content	
NAME	string	the name of the interrupt source. This is one of the name used in the OIL	
		file as value for the SOURCE attribute.	
NUMBER	string	the id of the interrupt source.	

# 15.1.13 Scalar data

The following scalar data are defined:

Data Type Content		Content
APPNAME	string	name of executable as given in the APP_NAME attribute in
		the OS object
ARCH	string	name of the architecture. This is the first item in the target.
ASSEMBLER	string	name of the assembler used. This is the ASSEMBLER attribute in the MEMMAP attribute of the OS object. It is used for assembler dependent templates.
ASSEMBLEREXE	string	name of the assembler executable used. This is the
		ASSEMBLER attribute in the OS object. It is set to as by
		default. It is used for build dependent templates.
AUTOSAR	boolean	true if Trampoline is compiled with the Autosar extension.
BOARD	string	name of the board. This is the third item (if any) in the
		target.
CHIP	string	name of the chip. This is the second item (if any) in the target.
COMPILER	string	name of the compiler used. This is the COMPILER attribute in the MEMMAP attribute of the OS object. It is used for compiler dependent templates.
COMPILEREXE	string	name of the compiler executable used. This is the COMPILER attribute in the OS object. It is set to <i>gcc</i> by default. It is used for build dependent templates. Do not confuse with the COMPILER data.
CPUNAME	string	name given to the OIL CPU object
EXTENDED	boolean	true if Trampoline is compiled in extended error handling mode.
FILENAME	string	the name of the file which will be written as the result of the computation of the current template.

Chapter 15. The Goil templates

Data	Type	Content		
FILEPATH string		the full absolute path of the file which will be written as the result of the computation of the current template.		
ITSOURCESLENGTH	integer	number of interrupt sources as defined in the 'target.cfg' file.		
LINKER	string	name of the linker used. This is the LINKER attribute in the MEMMAP attribute of the OS object. It is used for linker dependent templates.		
LINKEREXE	string	name of the linker executable used. This is the LINKER attribute in the OS object. It is set to <i>gcc</i> by default. It is used for build dependent templates. Do not confuse with the LINKER data.		
LINKSCRIPT	string	name of the link script file as given in the MEMMAP attribute of the OS object.		
MAXTASKPRIORITY	integer	the highest computed priority among the tasks.		
NATIVEFILEPATH	string	the full absolute path of the file which will be written as the		
		result of the computation of the current template in native OS format.		
OILFILENAME	string	name of the root OIL source file		
PROJECT	string	name of the project. The name of the project is the -p (orproject) value if it is set or the name of the oil file without the extension.		
SCALABILITYCLASS	integer	the Autosar scalability class used by the application. If Autosar is not enabled, SCALABILITYCLASS is set to 0.		
value		name of the target. This is the -t (ortarget) option value of goil.		
TEMPLATEPATH	string	path to the template root directory. This is thetemplates option value of goil or the value of the GOIL-TEMPLATES environment variable.		
TIMESTAMP	string	current date		
TRAMPOLINEPATH	string	path to the trampoline root directory. This is the TRAMPOLINE_BASE_PATH attribute of the OS object. It defaults to "".		
USEBUILDFILE	boolean	true if a build file is used for the project ie option -g orgenerate-makefile is given.		
USECOM	boolean	true if the application uses OSEK COM.		
USECOMPILERSETTINGS boolean true if 'Compil		true if memory mapping is enabled (Goil generates the 'Compiler.h' and 'Compiler_Cfg.h' files and Trampoline includes them).		
USEERRORHOOK	boolean	true if Trampoline uses the Error Hook.		
USEGETSERVICEID	boolean	true if Trampoline uses the service ids access macros.		
USEINTERRUPTTABLE	boolean	true if the wrapping of interrupt vector to glue functions used to increment a counter or to activate an ISR2 (for instance) should be generated. The actual code generation is up to the port.		
USELOGFILE	boolean	true if goil generates a log file, ie option -1 orlogfile is given.		
USEMEMORYMAPPING	boolean	true if memory mapping is enabled (Goil generates the 'MemMap.h' file and Trampoline includes it).		

Data	$\mathbf{Type}$	Content
USEMEMORYPROTECTION	boolean	true if Trampoline uses the Memory Protection.
USEOSAPPLICATION	boolean	true if Trampoline uses OS Applications.
USEPARAMETERACCESS	boolean	true if Trampoline uses the parmaters access macros.
USEPOSTTASKHOOK	boolean	true if Trampoline uses the Post-Task Hook.
USEPRETASKHOOK	boolean	true if Trampoline uses the Pre-Task Hook.
USEPROTECTIONHOOK	boolean	true if Trampoline uses the Protection Hook.
USERESSCHEDULER	boolean	true if Trampoline uses the RES_SCHEDULER resource.
USESHUTDOWNHOOK	boolean	true if Trampoline uses the Shutdown Hook.
USESTACKMONITORING	boolean	true if Trampoline uses the Stack Monitoring.
USESTARTUPHOOK	boolean	true if Trampoline uses the Startup Hook.
USESYSTEMCALL	boolean	true if services are called using a System Call (i.e. a soft-
		ware interrupt).
USETIMINGPROTECTION	boolean	true if Trampoline uses Timing Protection.
USETRACE	boolean	true if tracing is enabled.

# 15.2 The Goil template language (or GTL)

A template is a text file with file extension '.goilTemplate'. This kind of file mixes literal text with an embedded program. Some instructions (see section 15.5.6) in the embedded program outputs text as a result of the program execution and this text is put in place of the instructions. The resulting file is then stored.

The template interpreter starts in literal text mode. Switching from literal text mode to program mode and back to text mode is done when a '%' is encountered. A literal '%' and a literal '\' may be used by escaping them with a '\'.

# 15.3 GTL types

GTL supports 5 types: **string**, **integer**, **boolean**, **list** and **struct**. The 4 first types have readers to get informations about a variable. A reader is invokes with the following syntax:

[expression reader]

A struct is an aggregate of data. The '::' allows to get a member of the struct. For instance one of the member of *TIMINGPROTECTION* is **TIMEFRAME** so to get **TIMEFRAME**, the following syntax is used:

TIMINGPROTECTION::TIMEFRAME

# 15.3.1 string readers

The following readers are available for string variables:

15.3. GTL types

Item	$\mathbf{Type}$	Meaning
HTMLRepresentation	string	this reader returns a representation of the string suitable for an HTML en- coded representation. '&' is encoded by & ,'"' by " ,'<' by < and '>' by > .
identifierRepresentation	string	this reader returns an unique representation of the string conforming to a C identifier. Any Unicode character that is not a latin letter is transformed into its hexadecimal code point value, enclosed by '_' characters. This representation is unique: two different strings are transformed into different C identifiers. For example: value3 is transformed to value_33_; += is transformed to _2B3D_; An_Identifier is transformed to An_5F_Identifier.
lowercaseString	string	this reader returns lowercased representation of the string.
length	integer	this reader returns the number of characters in the string
stringByCapitalizingFirstCharacter	string	if the string is empty, this reader returns the empty string; otherwise, it returns the string, the first character being replaced with the corresponding upper case character.
uppercaseString	string	this reader returns uppercased representation of the receiver

# 15.3.2 boolean readers

The following readers are available for boolean variables:

Item	Type	Meaning
trueOrFalse	string	this reader returns "true" or "false" according to the boolean value
yesOrNo	string	this reader returns "yes" or "no" according to the boolean value
unsigned	integer	this reader returns 0 or 1 according to the boolean value

# 15.3.3 integer readers

The following readers are available for integer variables:

Item	Type	Meaning
string	string	This reader returns the integer value as a character string.
hexString	string	this reader returns an hexadecimal string representation of the integer value.

# 15.3.4 list readers

The following reader is available for list variables:

Item	Type	Meaning
length	integer	this reader returns the number of objects currently in the list.

# 15.4 GTL operators

# 15.4.1 Unary operators

Operator	Operand Type	Result Type	Meaning
+	integer	integer no operation.	
$\sim$	integer	integer	bitwise not.
not	boolean	boolean	boolean not.
exists	any variable	boolean	true if the variable is defined, false otherwise. But see below



A second form of exists is:

#### exists var default (expression)

var and expression should have the same type. If var exists, the returned value is the content of var. If it does not exist, expression is returned.

# 15.4.2 Binary operators

Operator	Operands Type	Result Type	Meaning
+	integer	integer	add.
_	integer	integer	substract.
*	integer	integer	multiply.
/	integer	integer	divide.
&	integer	integer	bitwise and.
&	boolean	boolean	boolean and.
	integer	integer	bitwise or.
	boolean	boolean	boolean or.
$\wedge$	integer	integer	bitwise xor.
$\wedge$	boolean	boolean	boolean xor.
	string	string	string concatenation.
<<	integer	integer	shift left.
>>	integer	integer	shift right.
!=	any	boolean	comparison (different).
==	any	boolean	comparison (equal).
<	integer $or$ boolean	boolean	comparison (lower than).

Chapter 15. The Goil templates

15.5. GTL instructions

Operator	Operands Type	Result Type	Meaning
<=	integer $or$ boolean	boolean	comparison (lower or equal).
>	integer $or$ boolean	boolean	comparison (greater).
>=	integer or boolean	boolean	comparison (greater or equal).

#### 15.4.3 Constants

Constant	Type	Meaning
emptyList	list	this constant is an empty list
true	boolean	true boolean
false	boolean	false boolean
yes	boolean	true boolean
no	boolean	false boolean

# 15.5 GTL instructions

# 15.5.1 The *let* instruction

Data assignment instruction. The general form is:

```
let var := expression
```

A second form allows to add a string to a list (only, this should be extended in the future). The string is added with the NAME attribute.

```
let var += expression
```

var is a list and expression is a string.

The scope of a variable depends on the location where the variable is assigned the first time. For instance, in the following code:

```
let a := 1
foreach task in TASKS do
  let b := INDEX
  let a := INDEX
end foreach
!a !b
```

Because a is assigned outside the foreach loop, it contains the value of the last INDEX after the foreach. Because b is assigned inside the foreach loop, it does not exist after the loop anymore and !b will trigger and error.

# 15.5.2 The *if* instruction

Conditional execution. The forms are:

```
if expression then ... end if if expression then ... else ... end if
```

118 15.5. GTL instructions

```
if expression then ... elsif expression then ... end if if expression then ... elsif expression then ... else ... end if
```

The *expression* must be boolean. In the following example, the blue text (within the %) is produced only if the *USECOM* boolean variable is true:

```
if USECOM then %
#include "tpl_com.h" %
end if
```

#### 15.5.3 The *foreach* instruction

This instruction iterates on the elements of a list. Each element may have many attributes that are available as variables within the **do** section of the foreach loop. The simplest form is the following one

```
for each var in expression do ... end for each
```

In the following example, for each element in the ALARMS list, the text between the do and the end foreach is produced with the NAME attribute of the current element of the ALARMS list inserted at the specified location. INDEX is not an attribute of the current element. It is generated for each element and ranges from 0 to the number of elements in the list minus 1.

```
foreach ALARMS do
%
/* Alarm % !NAME % identifier */
#define % !NAME %_id % !INDEX %
CONST(AlarmType, AUTOMATIC) % !NAME % = % !NAME %_id;
%
end foreach
```

A more general form of the foreach instruction is:

```
foreach expression prefixedby string
  before ...
  do ...
  between ...
  after ...
end foreach
```

prefixedby is optional and allows to prefix the attribute names by *string*. If the list is not empty, the **before** section are executed once before the first execution of the **do** section. The **between** section is executed between the execution of the **do** section. If the list is not empty, the after section is executed once after the last execution of the **do** section.

In the following example, a table of pointers to alarm descriptors is generated:

```
foreach ALARMS
  before %

tpl_time_obj *tpl_alarm_table[ALARM_COUNT] = {
    do % &% !NAME %_alarm_desc%
    between %,
```

15.5. GTL instructions 119

```
%
  after %
};
%
end foreach
```

# 15.5.4 The for instruction

The **for** instruction iterates along a literal list of elements.

```
for var in expression, ..., expression do ... end for
```

At each iteration, var gets the value of the current expression. As in the foreach instruction, INDEX is generated and ranges from 0 to the number of elements in the list minus 1.

# 15.5.5 The *loop* instruction

The **loop** instruction is the classical integer loop. Its simplest form is:

```
loop var from expression to expression do ... end loop
```

Like in the foreach instruction, before, between and after sections may be used:

```
loop var from expression to expression
before ...
do ...
between ...
after ...
end loop
```

#### 15.5.6 The ! instruction

```
! emits an expression. The form is:
```

```
! expression
```

# 15.5.7 The ? instruction

? stores in a variable a number of spaces equal to the current column in the output. The form is:

```
? var
```

120 15.5. GTL instructions

# 15.5.8 The template instruction

The template instruction includes the output of another template in the output of the current template. Its simplest form is the following one:

```
\verb|template| template_file_name|
```

If the file *template\_file\_name*.goilTemplate does not exist, an error occurs. To include the output of a template without generating an error, use the following form:

```
template if exists template\_file\_name
```

A third form allows to execute instructions when the included template file is not found:

```
template if exists template\_file\_name or ... end template
```

At last, it is possible to search templates in a hierarchy (code, linker, compiler, build) different from the current one. For instance to include a template located in the linker hierarchy, use one of the following forms:

```
template template\_file\_name in hierarchy template if exists template\_file\_name in hierarchy template if exists template\_file\_name in hierarchy or ... end template
```

In all cases, the included template inherits from the current variables table but works on its own local copy.

#### 15.5.9 The write instruction

The write instruction defines a block where the template processing output is captured to be written to a file. The general form is:

```
write to expression :
    ...
end write
```

Where expression is a string expression.

In the following example, the result of the 'script' template is written to the link script file.

```
if exists LINKER then
  write to PROJECT."/".LINKSCRIPT:
    template script in linker
  end write
end if
```

# 15.5.10 The *error* and *warning* instructions

It can be useful to generate an error or a warning if a data is not defined or if it looks strange. For instance if a target needs a STACKSIZE for a task or if the STACKSIZE is too large for a 16bit target. **error** and **warning** have 2 forms:

15.6. Examples 121

```
error var expression
warning var expression
and
error here expression
warning here expression
```

expression must be of type string. In the first form, var is a configuration data. The file location of this configuration may be a location in the OIL file or in the template file if the variable was assigned in the template. In the second form, here means the current location in the template file.

In the following example an error is generated for each task with not STACKSIZE attribute in the OIL file:

```
foreach TASKS do
  if not exists STACKSIZE then
   error NAME "STACKSIZE of Task " . NAME . " is not defined"
  end if
end foreach
```

In this second example, a warning is generated if a template is not found:

```
template if exists interrupt_wrapping or
   warning here "interrupt_wrapping.goilTemplate not found"
end template
```

# 15.6 Examples

Here are examples of code generation using GTL.

# 15.6.1 Computing the list of process ids

```
foreach PROCESSES do
   if PROCESSKIND == "Task" then
%
/* Task % !NAME % identifier */
#define % !NAME %_id % !INDEX %
CONST(TaskType, AUTOMATIC) % !NAME % = % !NAME %_id;
%
   else
%
/* ISR % !NAME % identifier */
#define % !NAME %_id % !INDEX
   if AUTOSAR then
   #
   # ISR ids constants are only available for AUTOSAR
   #
%
CONST(ISRType, AUTOMATIC) % !NAME % = % !NAME %_id;
```

122 15.6. Examples

```
%
    end if
    end if
end foreach
```

# 15.6.2 Computing an interrupt table

```
if USEINTERRUPTTABLE then
  loop ENTRY from 0 to ITSOURCESLENGTH - 1
    before
#define OS_START_SEC_CONST_UNSPECIFIED
#include "tpl_memmap.h"
CONST(tpl_it_vector_entry, OS_CONST)
tpl_it_table[% !ITSOURCESLENGTH %] = {
    dο
      let entryFound := false
      foreach INTERRUPTSOURCES prefixedby interrupt_ do
        if ENTRY == interrupt_NUMBER then
          # check first for counters
          foreach HARDWARECOUNTERS prefixedby counter_ do
            if counter_SOURCE == interrupt_NAME & not entryFound then
              % { tpl_tick_% !interrupt_NAME %, (void *)NULL }%
              let entryFound := true
            end if
          end foreach
          if not entryFound then
            foreach ISRS2 prefixedby isr2_ do
              if isr2_SOURCE == interrupt_NAME & not entryFound then
                % { tpl_central_interrupt_handler_2, (void*)%
                !([TASKS length] + INDEX) % }%
                let entryFound := true
              end if
            end foreach
          end if
        end if
      end foreach
      if not entryFound then
        % { tpl_null_it, (void *)NULL }%
      end if
   between %,
    after
%
};
#define OS_STOP_SEC_CONST_UNSPECIFIED
#include "tpl_memmap.h"
end loop
end if
```

Chapter 15. The Goil templates

15.6. Examples 123

#### 15.6.3 Generation of all the files

```
This is the default 'root.goilTemplate' file
write to PROJECT."/tpl_app_config.c":
  template tpl_app_config_c in code
end write
write to PROJECT. "/tpl_app_config.h":
  template tpl_app_config_h in code
end write
write to PROJECT."/tpl_app_define.h":
  template tpl_app_define_h in code
end write
if exists COMPILER then
  write to PROJECT."/MemMap.h":
    template MemMap_h in compiler
  end write
  write to PROJECT."/Compiler.h":
    template Compiler_h in compiler
  end write
  write to PROJECT."/Compiler_Cfg.h":
    template Compiler_Cfg_h in compiler
  end write
end if
if exists LINKER then
  write to PROJECT."/".LINKSCRIPT:
    template script in linker
  end write
end if
```