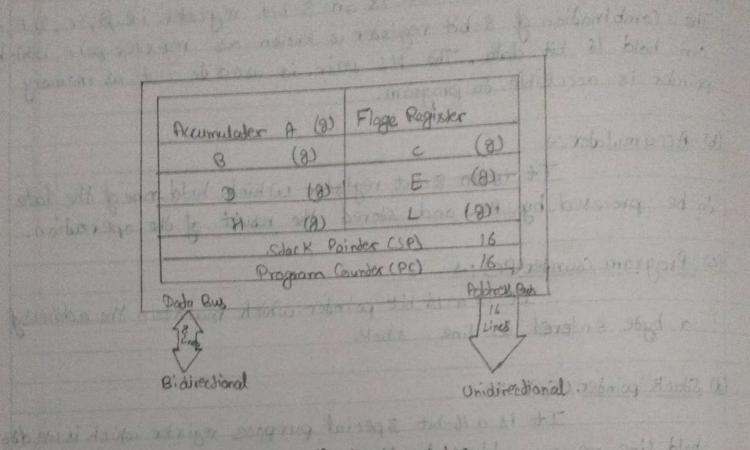
Page No. 01 Aim To study the microprocessor 8085 The Combination of 8-bit register is known as register pair which can hold 16 bit dada. The HL pair is used to act as memory pointer is accessible to program. (a) General purpose register:> It is an 8-bit register which hold one of the data to be processed by ALU and stored the result of the operation. (Program Counder(PC):> a byte entered to line stack. d) Stack pointer (Sp):> It is a 16-bit special purpose register which is we do hold line memory address for line next instruction to be executed. (e) Arithmetic and logical unit: -> It carries out arithmetic and logical aperation by 8 bic address it uses the accumulator content as input the ALU result is Stored back into accumulator. (Temporary register:) It is an 8-bit register associated with ALU hold data endering an operation, used by the microprocessor and not accessible to programs

Teacher's Signature_



microproces 8085 registers who would had

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which because the balance of the state of th

(g) Flags:> Flag register is a group of fire, individual flip-flop line consent of line flag register will change after execution of arithmetic and logic operation. The line states flags are i) Carry Hag (c) ii) Paridy Hag (P) iii) zero Hag(Z) in) Auxilary carryflog (Ac) 1) sign flag (s).

(b) Timing and combrol unit:>

Synchronous all microprocess, operation with the clock and generator and control signal from it necessary to communicate between controller and peripherals.

(i) Instruction register and decoder:

Instruction is federhed from line memory and x bared in line instruction register decodes the stored information.

(Register Array:) These are used to store 8-bit data during execution of same instruction.

In Ender Program indo Trainer Kit

1. Pres 'RESET' Key

2. Sub (Key processor represent address field)

- 3. Ender the address (16 bit) and digit in hex Press 'NEXT' Key
- 4. Ender the data.

5. Again press NEXT'

6. Again ofter taking the program, are use HLT instruction its Herade. 7. Press NEXT.

Result: - Thus 8085 microprocessor was studied successfully Teacher's Signature_

and an high to be dealers in successful to the cost which is the cost Stratelling productions while the strains while the same Address (Hex) Address Dodo Solder Free Brust 10 53 8192 2000 The transfer of a major of most transfer to the relation for which designed but as before and The second was asset to represent to the but you are will made had the administration address of bride all shoots assign reduction and well by modern to a grande deal, the a specie of the said section In Enter Program and Mainer With 104 (1959) assign (kish divided through the see self) di2 - 1 god treated was a sign has the standard on one of the Public Tan Boss what range the same shall be said the

Page No. 0.3

Aim:	Store 8-bit dada in memory.
	Algorithm:
) Store data in the Accumulator
	2) copy accumulatur constent at a particular location.
	3) terminate the program.
	A STATE OF THE PARTY OF THE PAR
	Program:-)
	MVI A, 35H
	STA 2000H
	htt:
	Result: - 8-bit dada stored in memory successfully.
	Teacher's Signature

Dada Adress Address (Hex) 5 8 182 2000 8193 2001 MYZ A 2511 14000 4T2 hit Hold and Brown or boxels whole tid 8 - Hold

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Page No. 04

lim:	write a program to swap two black of data Stored in memory
	Algorithm:
	1) load dada in accumulator. 2) copy/More accumulator data into general purpos register(B). 3) load accumulator with next data. 4) store accumulator with next data lacation/address. 5) store accumulator by B register to accumulator. 6) store accumulator data to the second data location/address. 7) terminate the program.
	Program:
	1DA 2000H MOV B, A 1DA 2001H STA 2000H MOV A, B STA 2001H htt.
	Result- successfully swap two block of data stored in memory.
	Teacher's Signature

there was by wood such good to the marging of deligate out - lange de l'able o Address 1 Address (Hex) 0002 \$ 3 0003 Result in Accumalater 9. 6 8 VOM 14100C 461 HODOC ATS B. A VOM 131003 ATT 144 provider and brooks that by woold and goods pluggestones & with

Mary V. B. Sales

F. Santa

Expt. No. <u>09</u>	rage No BS
mir Adding 8-bit dada	
Algoridhm:	
3) load the next da	register B from a ccumalater. la and move into register (from commandater) ada for in B to (A) accumalater
Programm	
LDA 0002H MOV B, A LDA 0003H MOV C, A	
ADD B	
Results Addition of 8-6	it dota done successfully

Teacher's Signature_

amped:-

Address (Mex)	Address	dada
0002	2 2	34
0003	3 3	1200
000 4	4	22
0005	5	11
0006	6	56
0007	7	23

11.6000 AE

A A vota

9 418

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pilotones such dob tid a provided was

Page No. ___ 66____

	Addition of 16-bit data.		
	Algorithm:		
	1) load first 16-bit number in 41L		
	2) save First 16-bit number in DE		
	3) load second 16-bit number in HL		
	4) load lower & bydes of first number in accumulater.		
	5) Add lower bytes of Second number & and save in register i		
	6) Add the higher bytes of first and second number as in Step-Yand		
	2) some the result in register H.		
	8) store result at a memory lacation.		
	9) derminede the program.		
	Program:		
	LHLD 0002H		
	XCHA		
	LHLD 000\$H		
	MOV A, E		
	ADD L		
	Mov L, A		
	Mov A, D		
	ADC H		
	MOV M, A		
	SHLD OOOSH		
	hlt		
	Rends- Addition of 16-bit deute done succeptully.		