SPSIL
Language Specification
Version 1.0

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1 Introduction

SPSIL or *System Programmer's Simple Integer Language* is an untyped programming language designed for implementation of an operating system on ESIM *(Extended Simple Integer Machine)* architecture. The language is minimalistic and consists only of basic constructs required for the implementation. Programming using SPSIL requires a basic understanding of the underlying ESIM architecture and operating system concepts.

2 Lexical Elements

2.1 Comments and White Spaces

SPSIL allows only single line comments. Comments start with the character sequence // and stop at the end of the line. White spaces in the program including tabs, newline and horizontal spaces are ignored.

2.2 Keywords

The following are the reserved words in SPSIL and it cannot be used as identifiers.

```
alias     else      if      store   while
define    endif    ireturn  strcmp
  do     endwhile   load     then
```

2.3 Operators and Delimiters

The following are the operators and delimiters in SPSIL

```
(   )    ;    [    ]    /    *    +    -    %
>   <   >=   <=   !=   ==   =   &&   ||   !
```

2.4 Registers

SPSIL allows the use of 19 registers for various operations.(R0-R15, BP, SP, IP)

2.5 Identifiers

Identifiers are used as symbolic names for constants and aliases for registers. Identifiers should start with an alphabet but may contain alphabets, digits and/or underscore (_). No other special characters are allowed in identifiers.

2.6 Literals

Only integer literals are permitted in SPSIL. An integer literal is a sequence of digits representing an integer. Negative integers are represented with a negative sign preceding the sequence of digits.

3 Register Set

SPSIL doesn't allow the use of declared variables. Instead a fixed set of registers is provided. The register set in SPSIL contains 19 registers. There is a direct mapping between these registers and the machine registers in ESIM.

R0-R7	Program Registers
R8-R15	Kernel Registers
BP	Base Pointer
SP	Stack Pointer
IP	Instruction Pointer

3.1 Aliasing

Kernel Registers (R8 to R15) can be referred to by using a different name. A name is assigned to a particular register using the **alias** keyword. Each register can be assigned to only one alias at any particular point of time. However, a register can be reassigned to a different alias at a later point. No two registers can have the same alias name simultaneously.

4 Constants

Symbolic names can be assigned to values using the **define** keyword. Unlike aliasing, two or more names can be assigned to the same value. A constant can only be defined once in a program.

4.1 Predefined Constants

SPSIL provides a set of predefined constants. These predefined constants can be assigned to different values explicitly by the user using **define** keyword. These constants are mostly starting addresses of various OS components in the memory.

The predefined set of constants provided in SPSIL are

Name	Default Value
SCRATCHPAD	256
PAGE_TABLE	512
MEM_LIST	576
FILE_TABLE	640
READY_LIST	736
PROC_TABLE	767
FAT	1024
DISK_LIST	1536
USER_PROG	1792
INTERRUPT	13824

5   Expressions

An expression specifies the computation of a value by applying operators to operands. SPSIL supports arithmetic and logical expressions.

5.1   Arithmetic Expressions

Registers, constants, and 2 or more arithmetic expressions connected using arithmetic operators are categorized as arithmetic expressions. SPSIL provides five arithmetic operators, viz., +, -, *, / (Integer Division) and % (Modulo operator) through which arithmetic expressions may be combined. Expression syntax and semantics are similar to standard practice in programming languages and normal rules of precedence, associativity and paranthesization hold.

5.2   Logical Expressions

Logical expressions may be formed by combining arithmetic expressions using relational operators. The relational operators supported by SPSIL are

    <, >, <=, >=, ==, !=

Standard meanings apply to these operators. A relational operator will take in two arguments and return 1 if the relation is valid and 0 otherwise. Logical expressions themselves may be combined using logical operators, && (logical and) , || (logical or) and ! (not).

5.3 String Comparison

The only operation that can be performed on strings stored in memory is string camparison. **strcmp** is used to compare two strings whose address is stored in the registers that are given as operands.

e.g. *strcmp(R9,R10)*

6 Addressing

Memory of the meachine can be directly accessed in an SPSIL program. A word in the memory is accessed by specifying the addressing element, i.e. memory location within [ ]. This corresponds to the value stored in the given address. An arithmetic expression can be used to specify the address. An addressing element cannot be used in an expression.

Examples of addressing: [1024], [R9], [R10+R11+128], [FAT] etc.

7 Statements

Statements control the execution of the program. All statements in SPSIL are terminated with a semicolon ;

7.1 Define Statement

Define statement is used to define a symbolic name for a value. Define statements should be used before any other statment in an SPSIL program. The keyword **define** is used to associate a literal to a symbolic name.

define *constant_name value;*

7.2 Alias Statement

An **alias** statement is used to associate a kernel register (R8 to R15) with a name. **Alias** statements can be used anywhere in the program except within **if** and **while** statements.

alias *alias_name register_name ;*

7.3 Assignment Statement

The SPSIL assignment statement assigns the value of an expression or value stored in a memory address to a register or a memory address. $=$ is the assignment operator used in SPSIL. The operand on the right hand side of the operator is assigned to the left hand side. The general syntax is as follows

Register / Alias / [Address] = Expression / [Address] ;

7.4 strcpy Statement

The SPSIL **strcpy** statement copies a string in one memory location to another memory location. Registers which store the memory location of the destination and the source are given as R_d and R_s respectively.

strcpy(R_d, R_s);

7.5 If Statement

If statements specify the conditional execution of two branches according to the value of a logical expression. If the expression evaluates to 1, the **if** branch is executed, otherwise the **else** branch is executed. The **else** part is optional. The general syntax is as follows

> **if** *(logical expression)* **then**
>   *statements;*
> **else**
>   *statements;*
> **endif;**

7.6 While Statement

While statement iteratively executes a set of statements based on a condition. The condition is defined using a logical expression. The statements are iteratively executed as long as the condition is true.

> **while** *(logical expression)* **do**
>   *statements;*
> **endwhile;**

7.7   ireturn Statement

ireturn statement is used to pass control from kernel mode to user mode. The **ireturn** is generally used at the end of an interrupt code.

   ireturn;

7.8   Load / Store Statements

Loading and storing between filesystem and memory is accomplished using **load** and **store** statements in SPSIL. **load** statement loads the block specified by *block_number* from the disk to the the page speficied by the *page_number* in the memory. **store** statement stores the page specified by *page_number* in the memory to the the block speficied by the *block_number* in the disk. The page number and block number can be specified using arithmetic expressions.

   load *(page_number, block_number);*
   store *(page_number, block_number);*