



The schematic diagram illustrates the Target USB circuit. It features a USB3343 IC (IC4) which is connected to a +3V3 supply and ground. The IC has several pins for data (D0-D7), control (STP, NXT, DIR, CLK), and power (VDDIO, VBAT, VDD33, VDD18, PAD, RESETB). It is connected to a USB\_CLK\_IN signal and a USB3343 signal. The circuit also includes a DSC6101C12A-026.0000 (IC6) and a MAX9032AKA+T (IC3A, IC3B, IC5A, IC5B, IC7A, IC7B, IC7C, IC7D) comparator. The comparators are connected to a +3V3 supply and ground, and their outputs are connected to a USB\_CLK\_IN signal. The circuit is powered by a +3V3 supply and ground, and includes various passive components like resistors (R10, R11, R12, R13, R14, R15, R16, R17, R18) and capacitors (C12, C13, C14, C15, C16, C17, C18).

# Host USB

