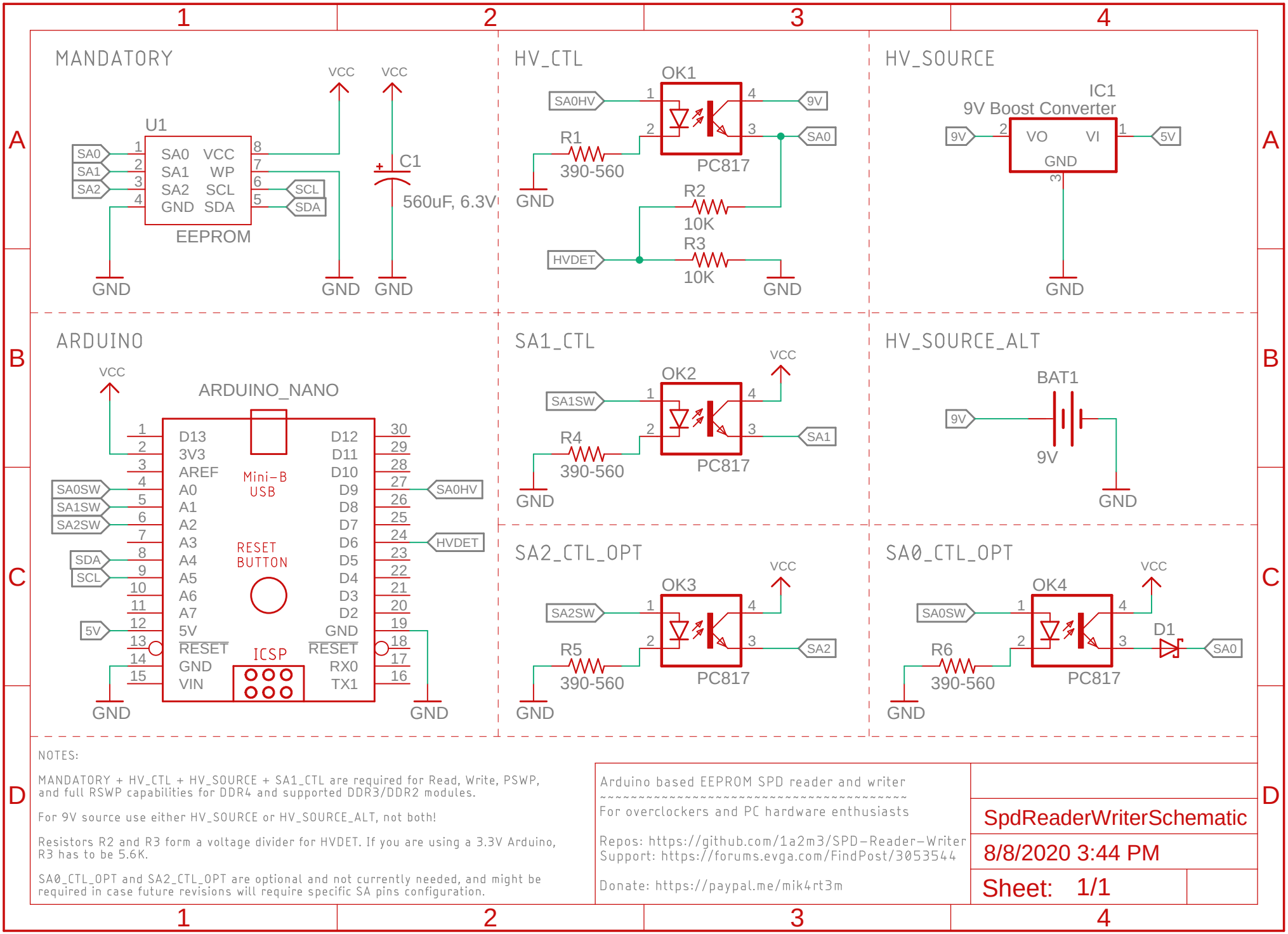

NOTES:

MANDATORY + HV_CTL + HV_SOURCE + SA1_CTL are required for Read, Write, PSWP, and full RSWP capabilities for DDR4 and supported DDR3/DDR2 modules.

For 9V source use either HV_SOURCE or HV_SOURCE_ALT, not both!

Resistors R2 and R3 form a voltage divider for HVDET. If you are using a 3.3V Arduino, R3 has to be 5.6K.

SA0_CTL_OPT and SA2_CTL_OPT are optional and not currently needed, and might be required in case future revisions will require specific SA pins configuration.

Arduino based EEPROM SPD reader and writer

For overclockers and PC hardware enthusiasts

Repos: https://github.com/1a2m3/SPD-Reader-Writer
Support: https://forums.evga.com/FindPost/3053544

Donate: https://paypal.me/mik4rt3m

SpdReaderWriterSchematic

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