## [CE3002] Operating System -- Homework 01

| 1. Choice of Questions (40%)  |
|---|
| 1. Which of the following would lead you to believe that a given system is an SMP-type system   |
| <ul> <li>A) Each processor is assigned a specific task.</li> <li>B) There is a boss-worker relationship between the processors.</li> <li>C) Each processor performs all tasks within the operating system.</li> <li>D) None of the above</li> </ul> |
| 2. A can be used to prevent a user program from never returning control to the operating system.  |
| <ul> <li>A) portal</li> <li>B) program counter</li> <li>C) firewall</li> <li>D) timer</li> </ul>  |
| 3. Two important design issues for cache memory are   |
| <ul> <li>□ A) speed and volatility</li> <li>☑ B) size and replacement policy</li> <li>□ C) power consumption and reusability</li> <li>□ D) size and access privileges</li> </ul>  |
| 4. What statement concerning privileged instructions is considered false?   |
| <ul> <li>A) They may cause harm to the system.</li> <li>B) They can only be executed in kernel mode.</li> <li>C) They cannot be attempted from user mode.</li> <li>D) They are used to manage interrupts.</li> </ul>                                |
| 5. The two separate modes of operating in a system are  |
| <ul> <li>A) supervisor mode and system mode</li> <li>B) kernel mode and privileged mode</li> <li>C) physical mode and logical mode</li> <li>D) user mode and kernel mode</li> </ul>   |
| 6. If a program terminates abnormally, a dump of memory may be examined by a to determine the cause of the problem.   |
| <ul> <li>A) module</li> <li>B) debugger</li> <li>C) shell</li> <li>D) ontrol card</li> </ul>  |

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| 7. Policy  |
| <ul> <li>A) determines how to do something</li> <li>☑ B) determines what will be done</li> <li>☐ C) is not likely to change across places</li> <li>☐ D) is not likely to change over time</li> </ul>   |
| 8. A microkernel is a kernel   |
| <ul> <li>[ ] A) containing many components that are optimized to reduce resident memory size</li> <li>[ ] B) that is compressed before loading in order to reduce its resident memory size</li> <li>[ ] C) that is compiled to produce the smallest size possible when stored to disk</li> <li>[x] D) that is stripped of all nonessential components</li> </ul> |
| 9. To the SYSGEN program of an operating system, the least useful piece of information is  |
| <ul> <li>[ ] A) the CPU being used</li> <li>[ ] B) amount of memory available</li> <li>[x] C) what applications to install</li> <li>[ ] D) operating-system options such as buffer sizes or CPU scheduling algorithms</li> </ul>   |
| 10 provide(s) an interface to the services provided by an operating system.  |

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[ ] A) Shared memory
[ [ ] C) Simulators
[ ] D) ommunication
```

## 二、問答題 (60%)

11. What is the purpose of interrupts? (4%) What are the differences between a trap and an interrupt? (4%)

An interrupt is a hardware-generated change-of-flow within the system. An interrupt handler is summoned to deal with the cause of the interrupt; control is then returned to the interrupted context and instruction

A trap is a software-generated interrupt. An interrupt can be used to signal the completion of an I/O to obviate the need for device polling. A trap can be used to call operating system routines or to catch arithmetic errors.

12. Describe three general methods for passing parameters to the operating system. (6%)

- a. Pass parameters in registers
- b. Registers pass starting addresses of blocks of parameters
- c. Parameters can be placed, or pushed, onto the stack by the program, and popped off the stack by the operating system

13. What are the two models of interprocess communication? (3%) What are the strengths and weaknesses of the two approaches? (3%)

The two models of interprocess communication are message-passing model and the shared-memory model. Message passing is useful for exchanging smaller amounts of data, because no conflicts need be avoided. It is also easier to implement than is shared memory for intercomputer communication. Shared memory allows maximum speed and convenience of communication, since it can be done at memory transfer speeds when it takes place within a computer. However, this method compromises on protection and synchronization between the processes sharing memory.

14. Describe the differences among short-term, medium-term, and long-term scheduling. (5%)

- a. Short-term (CPU scheduler)—selects from jobs in memory those jobs that are ready to execute and allocates the CPU to them.
- b. Medium-term—used especially with time-sharing systems as an intermediate scheduling level. A swapping scheme is implemented to remove partially run programs from memory and reinstate them later to continue where they left off.
- c. Long-term (job scheduler)—determines which jobs are brought into memory for processing.

15. Including the initial parent process, how many processes are created by the program shown in Figure 3.32? (6%)

```
// Figure 3.32 How many processes are created?
#include <stdio.h>
#include <unistd.h>

int main() {
   int i;
   for (i = 0; i < 4; i++)
        fork();
   return 0;
}</pre>
```

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16. Which of the following components of program state are shared across threads in a multithreaded process? (4%)

- A) Register values
- B) Heap memory
- C) Global variables

• D) Stack memory

The threads of a multithreaded process share heap memory and global variables. Each thread has its separate set of register values and a separate stack.

17. Consider the following code segment:

```
pid_t pid;

pid = fork();
if (pid == 0) { /*child process*/
    fork();
    thread_create( ... );
}
fork();
```

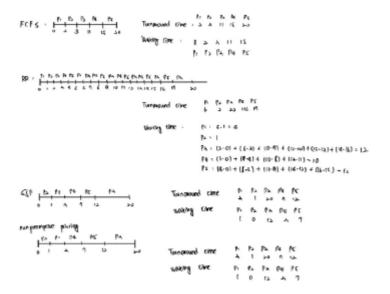
- a. How many processes are created? (include main process) (4%)
  - o 6 processes are created.
- b. How many threads are created? (4%)
  - o 2 threads are created.

18. Consider the following set of processes, with the length of the CPU burst time given in milliseconds:

| Process | <b>Burst Time</b> | Priority |
|---------|-------------------|----------|
| P1      | 2                 | 2        |
| P2      | 1                 | 1        |
| P3      | 8                 | 4        |
| P4      | 4                 | 2        |
| P5      | 5                 | 3        |

The processes are assumed to have arrived in the order P1, P2, P3, P4, P5 all at time 0.

- a. Draw four Gantt charts that illustrate the execution of these processes using the following scheduling algorithms: FCFS, SJF, nonpreemptive priority (a smaller priority number implies a higher priority), and RR (quantum = 1). (4%)
- b. What is the turnaround time of each process for each of the scheduling algorithms in part a? (4%)
- c. What is the waiting time of each process for each of these scheduling algorithms? (4%)
- d. Which of the algorithms results in the minimum average waiting time (over all processes)? (2%)



SJF and Nonpreemptive priority

19. Which of the following scheduling algorithms could result in starvation? (3%)

- $\square$  A) First-come, first-served
- **Ø** B) Shortest job first
- C) Round robin
- D) Priority

Shortest job first and priority-based scheduling algorithms could result in starvation.

20. How do clustered systems differ from multiprocessor systems? What is required for two machines belonging to a cluster to cooperate to provide a highly available service? (3% 3%)