













synth_conditional: Excess share in highest risk decile IV: instrument_ladder_huidig_other: 6 IV: instrument_ladder_huidig_other: 5 IV: instrument_ladder_huidig_other: 4 - see support in the proof of th instrument_ladder_huidig_other: 1 IV: instrument_ladder_huidig_other: 0

0.00

perc_misrep_td_threshold

0.05

-0.05