

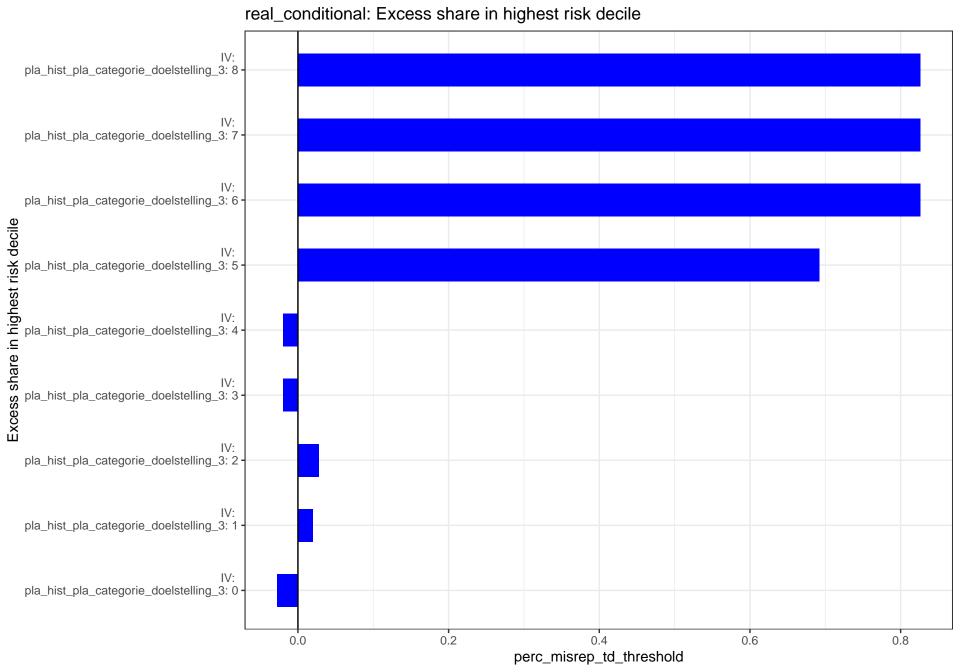
real: Excess share in highest risk decile pla_hist_pla_categorie_doelstelling_3: 8 pla_hist_pla_categorie_doelstelling_3: 7 IV: pla_hist_pla_categorie_doelstelling_3: 6 pla_hist_pla_categorie_doelstelling_3: 5

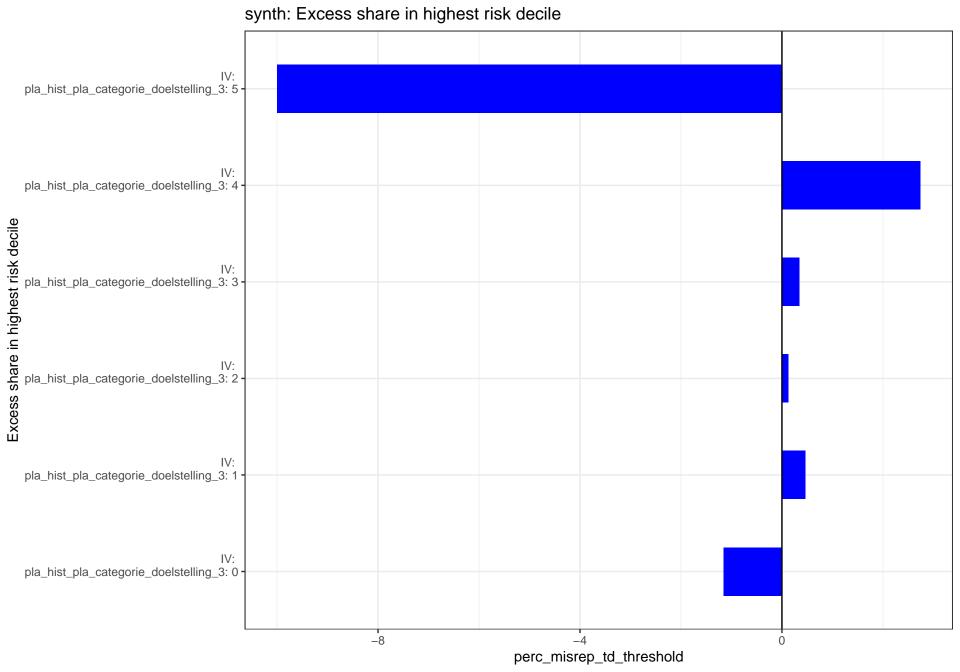
y y pla_hist_pla_categorie_doelstelling_3: 5

IV:
pla_hist_pla_categorie_doelstelling_3: 4

IV:
pla_hist_pla_categorie_doelstelling_3: 3 pla_hist_pla_categorie_doelstelling_3: 2 pla_hist_pla_categorie_doelstelling_3: 1 pla_hist_pla_categorie_doelstelling_3: 0 10 15

perc_misrep_td_threshold





synth_conditional: Excess share in highest risk decile

