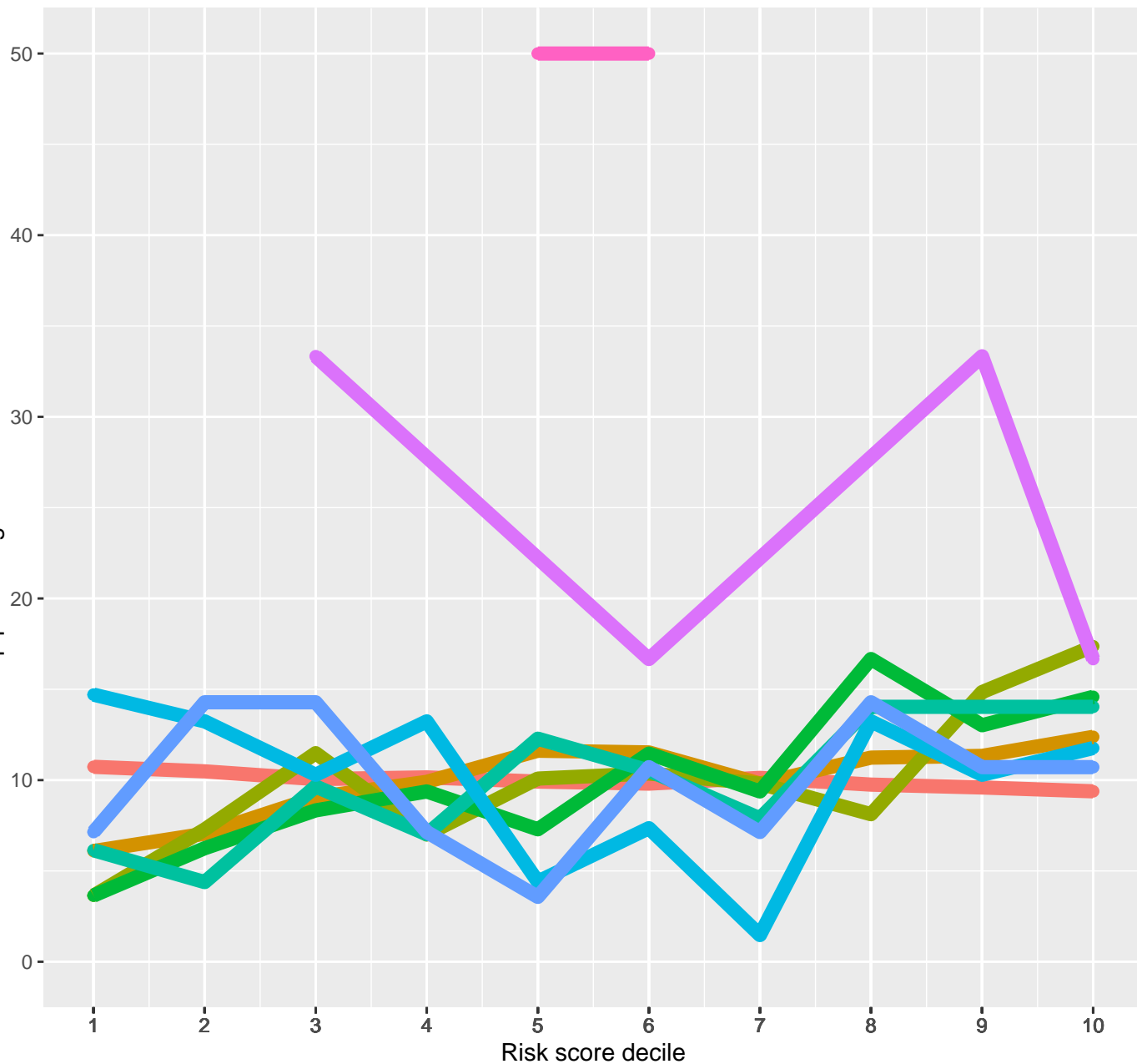


real

Group percentage in risk score decile

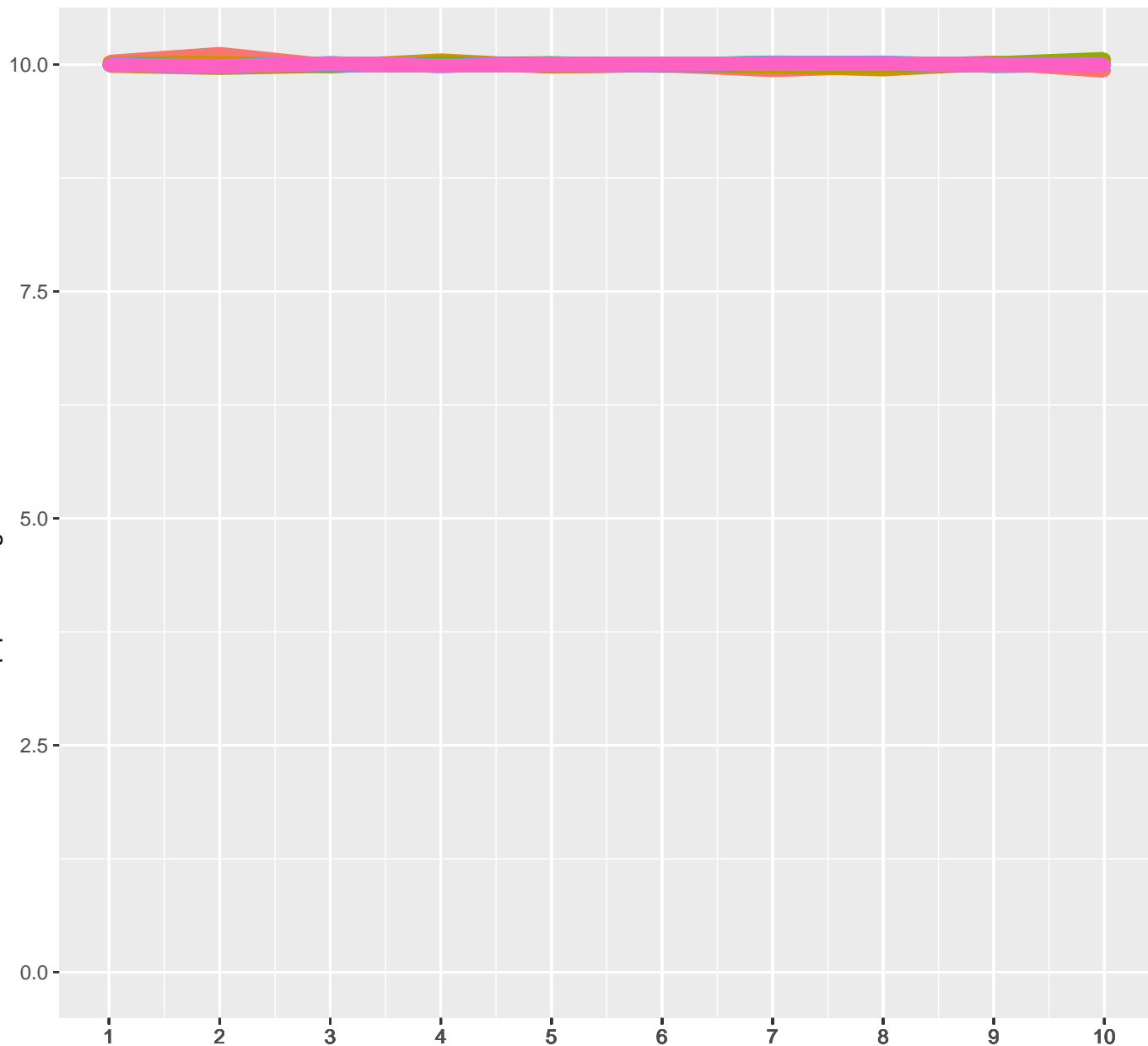


as.factor(IV)

- IV: instrument_ladder_historie_other: 0
- IV: instrument_ladder_historie_other: 1
- IV: instrument_ladder_historie_other: 2
- IV: instrument_ladder_historie_other: 3
- IV: instrument_ladder_historie_other: 4
- IV: instrument_ladder_historie_other: 5
- IV: instrument_ladder_historie_other: 6
- IV: instrument_ladder_historie_other: 7
- IV: instrument_ladder_historie_other: 8

real_conditional

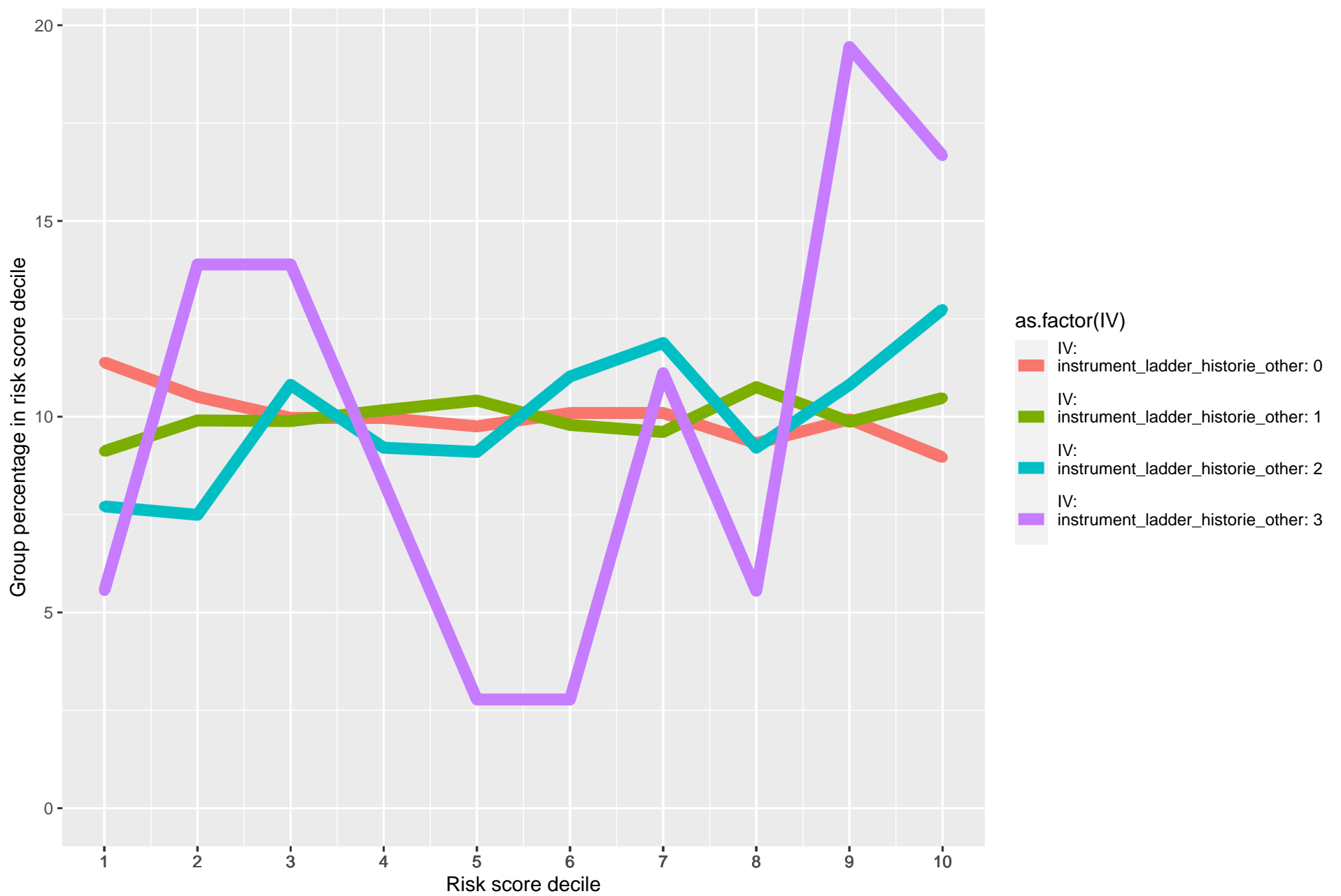
Group percentage in risk score decile



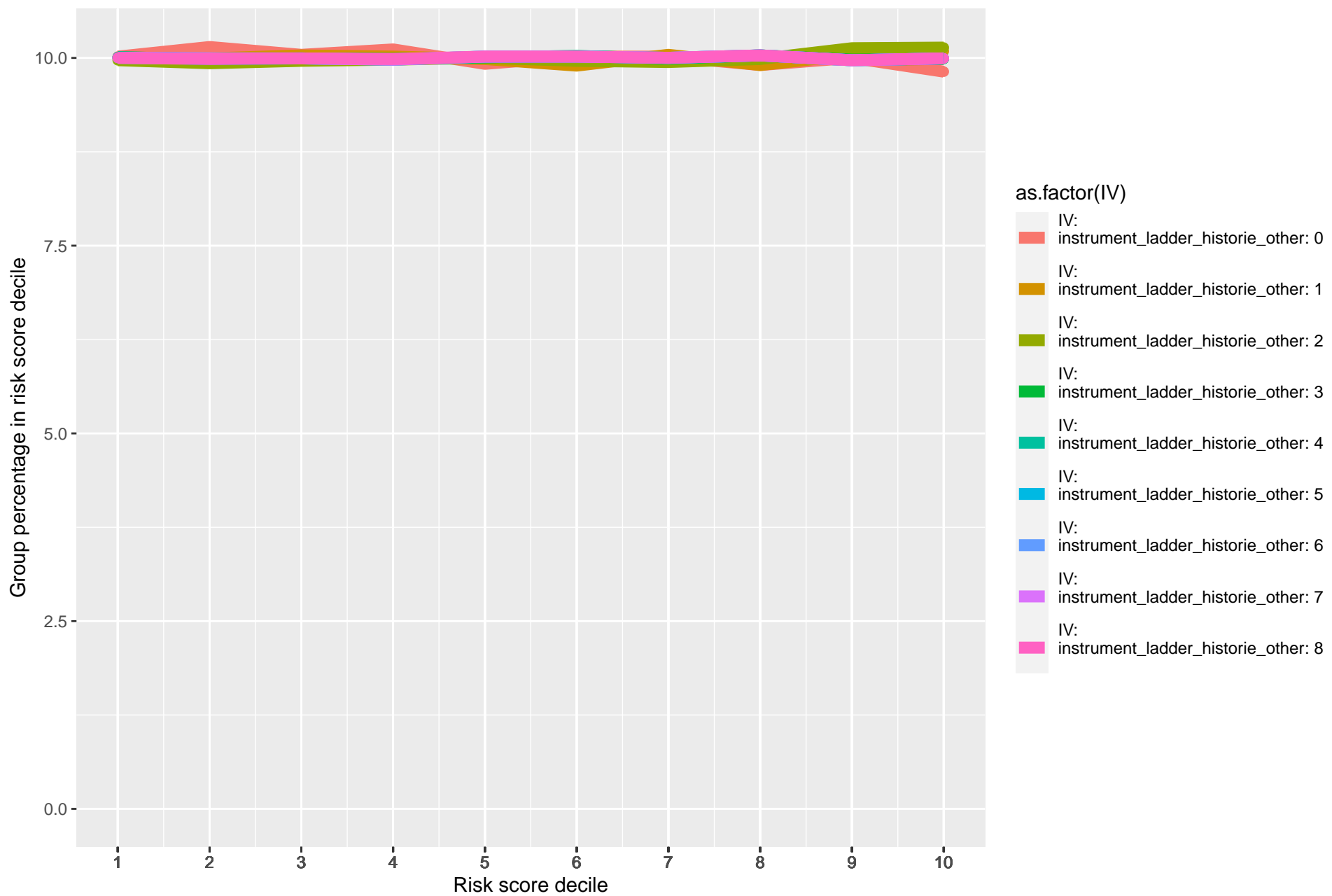
as.factor(IV)

- IV: instrument_ladder_historie_other: 0
- IV: instrument_ladder_historie_other: 1
- IV: instrument_ladder_historie_other: 2
- IV: instrument_ladder_historie_other: 3
- IV: instrument_ladder_historie_other: 4
- IV: instrument_ladder_historie_other: 5
- IV: instrument_ladder_historie_other: 6
- IV: instrument_ladder_historie_other: 7
- IV: instrument_ladder_historie_other: 8

synth

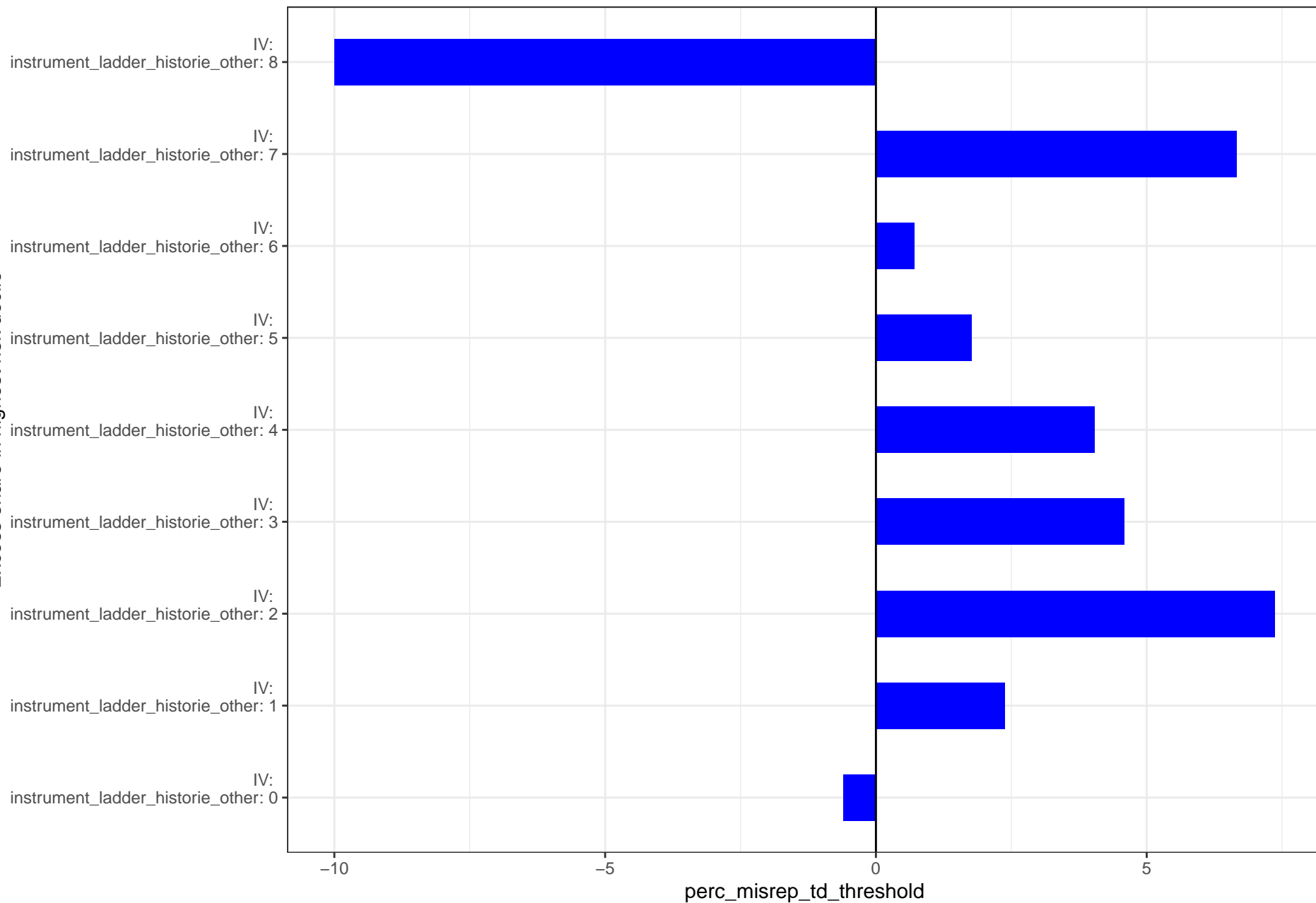


synth_conditional



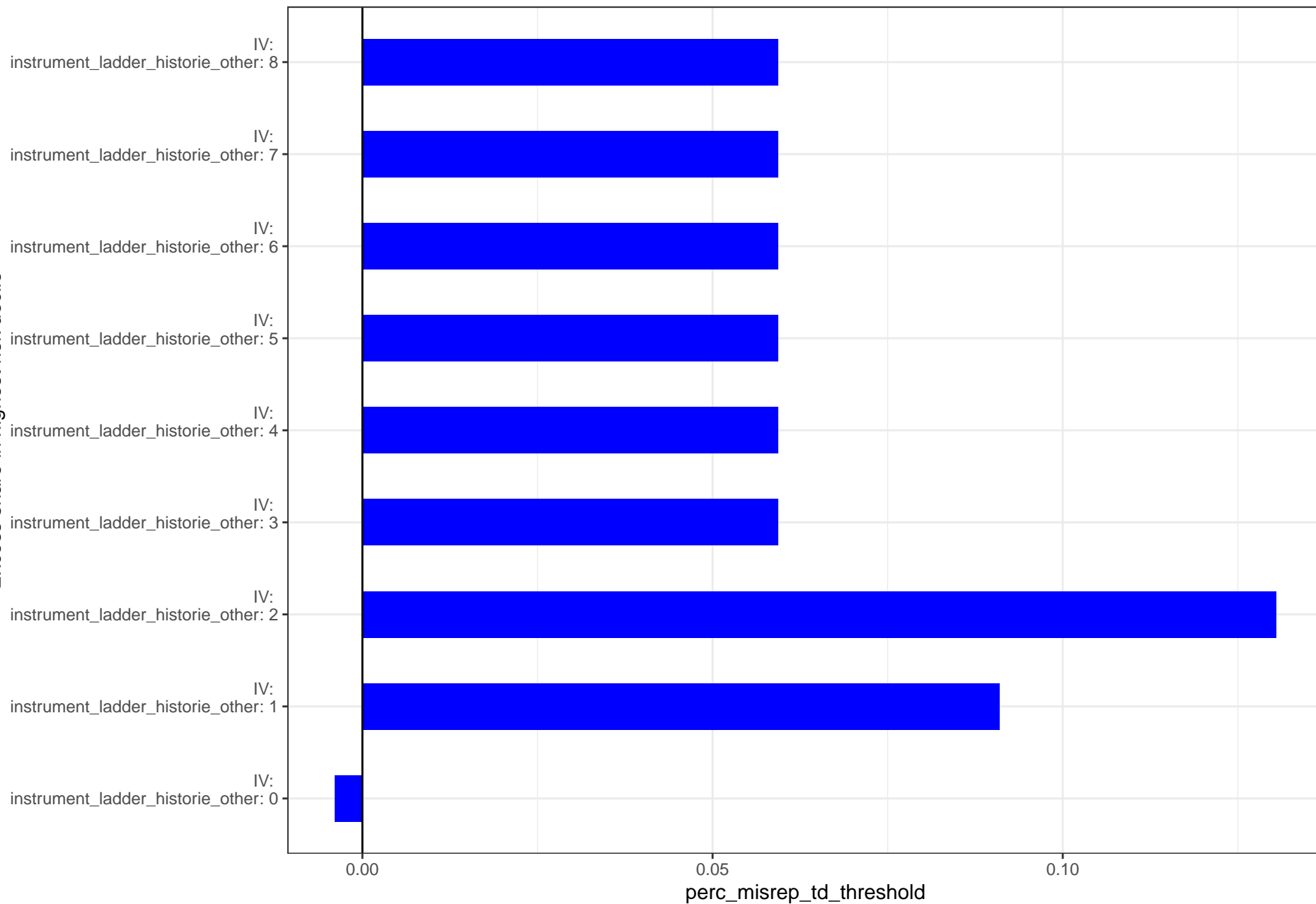
real: Excess share in highest risk decile

Excess share in highest risk decile



real_conditional: Excess share in highest risk decile

Excess share in highest risk decile



synth: Excess share in highest risk decile

Excess share in highest risk decile

IV:
instrument_ladder_historie_other: 3

IV:
instrument_ladder_historie_other: 2

IV:
instrument_ladder_historie_other: 1

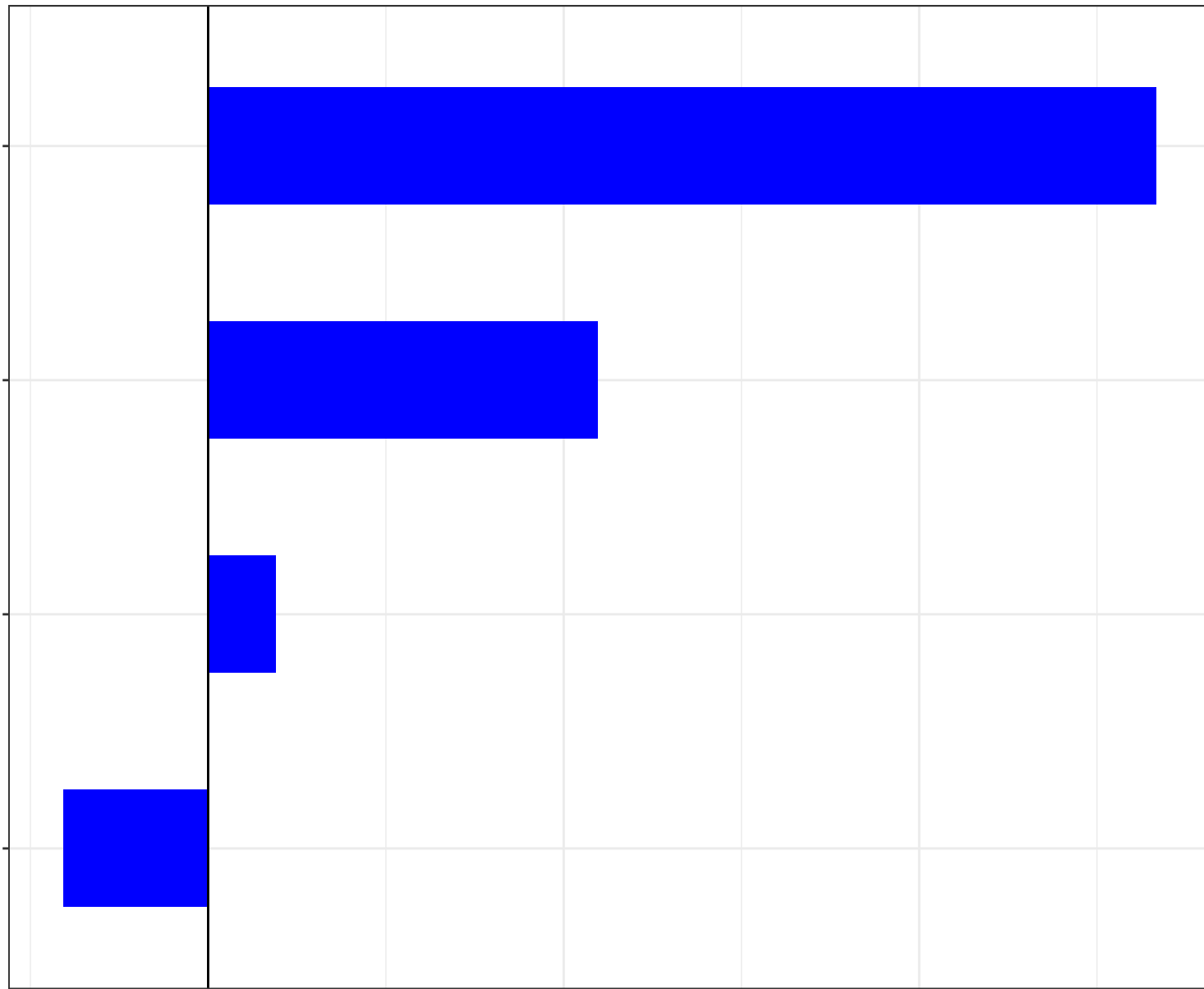
IV:
instrument_ladder_historie_other: 0

0.0

2.5

5.0

perc_misrep_td_threshold



synth_conditional: Excess share in highest risk decile

Excess share in highest risk decile

