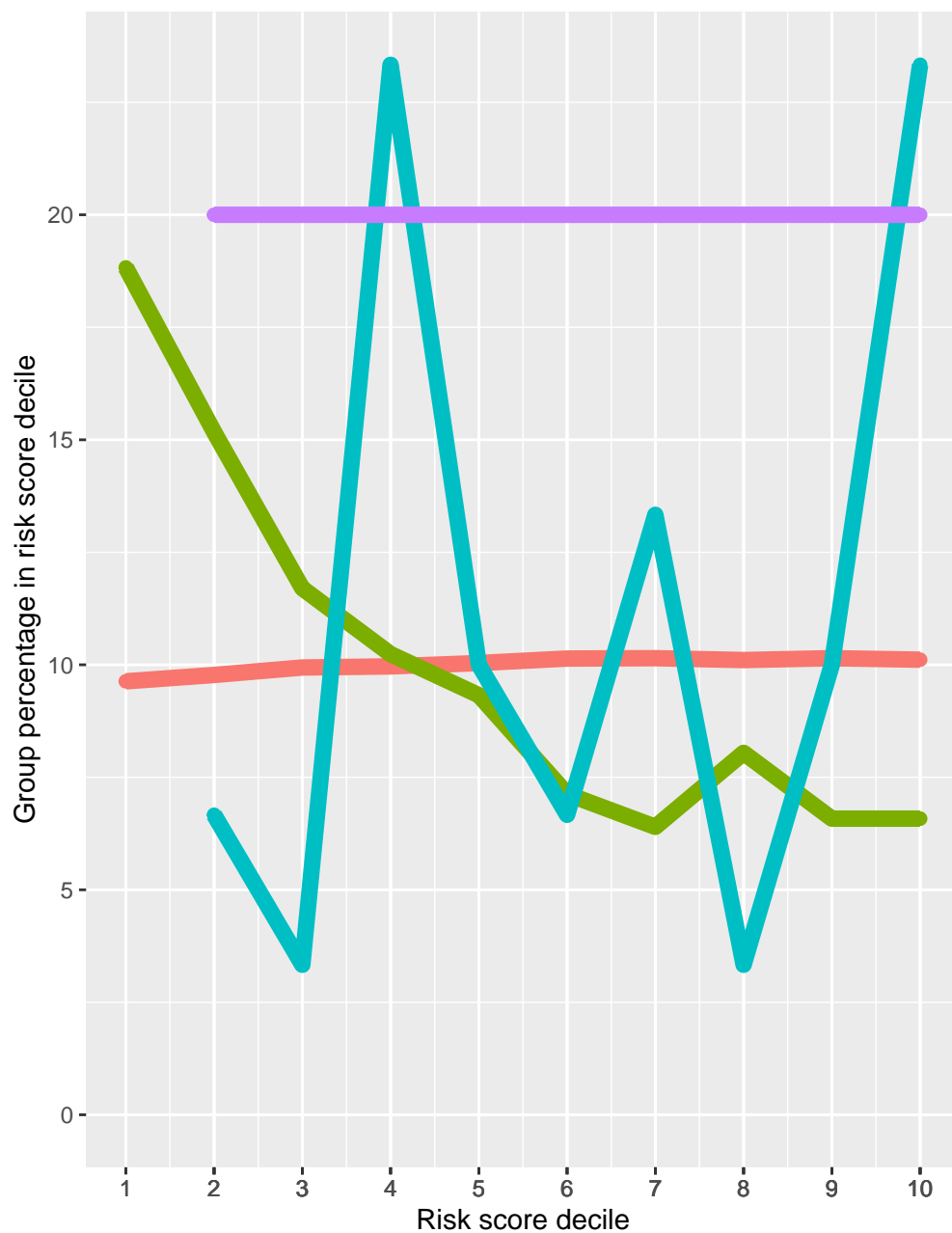


real

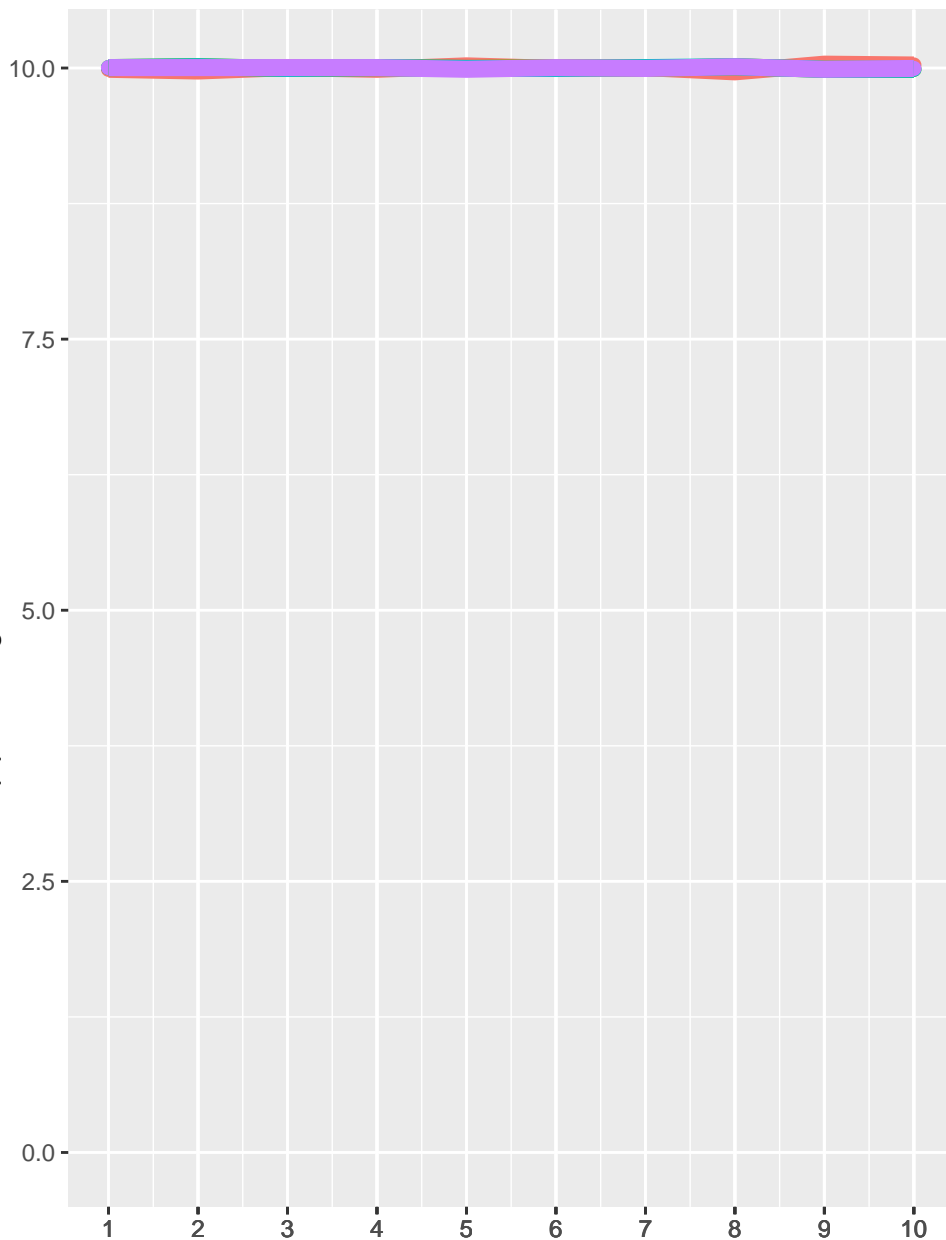


as.factor(IV)

- IV:
instrument_reden_beeindiging_historie_succesvol__doelstelling_bereikt: 0
- IV:
instrument_reden_beeindiging_historie_succesvol__doelstelling_bereikt: 1
- IV:
instrument_reden_beeindiging_historie_succesvol__doelstelling_bereikt: 2
- IV:
instrument_reden_beeindiging_historie_succesvol__doelstelling_bereikt: 3

real_conditional

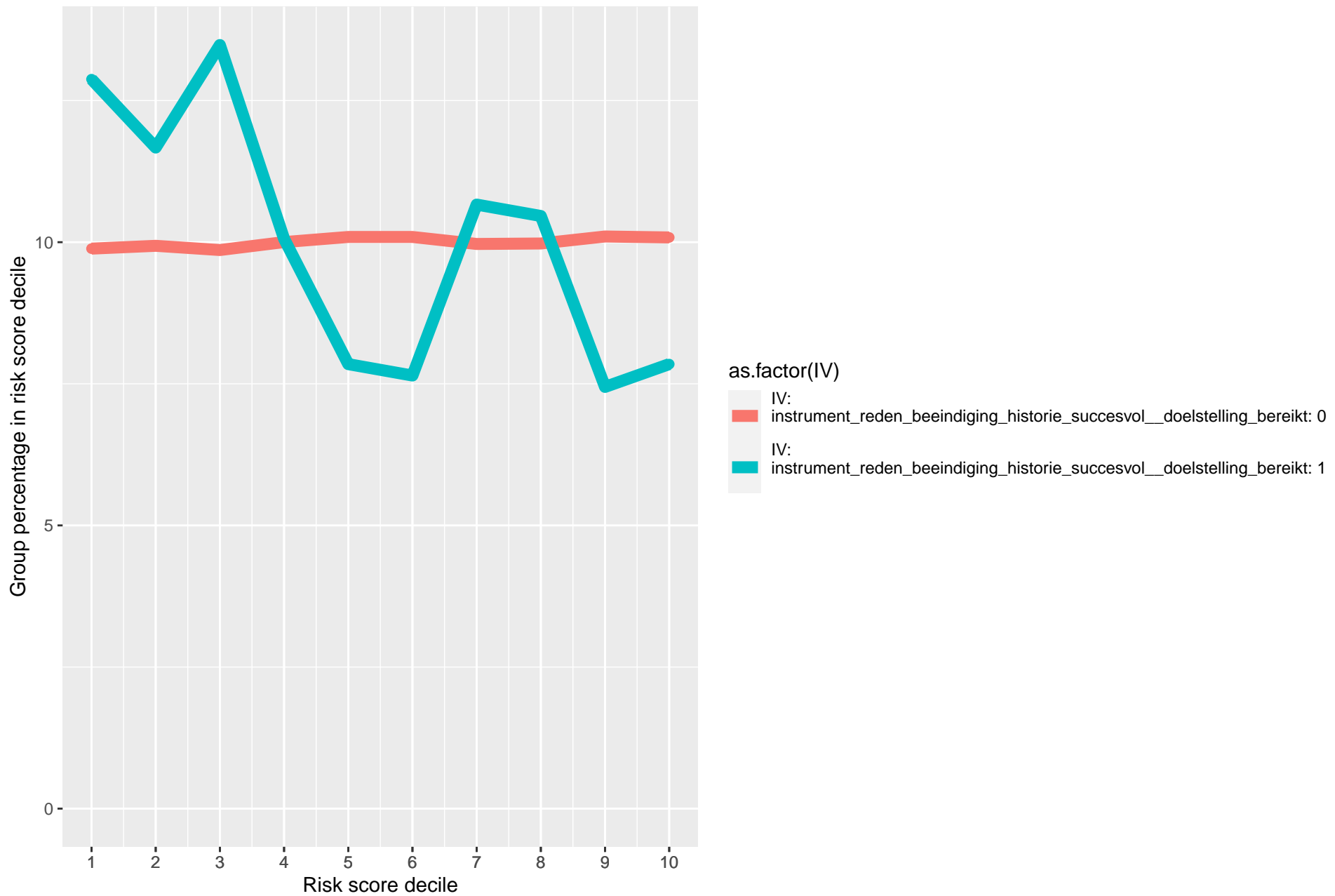
Group percentage in risk score decile



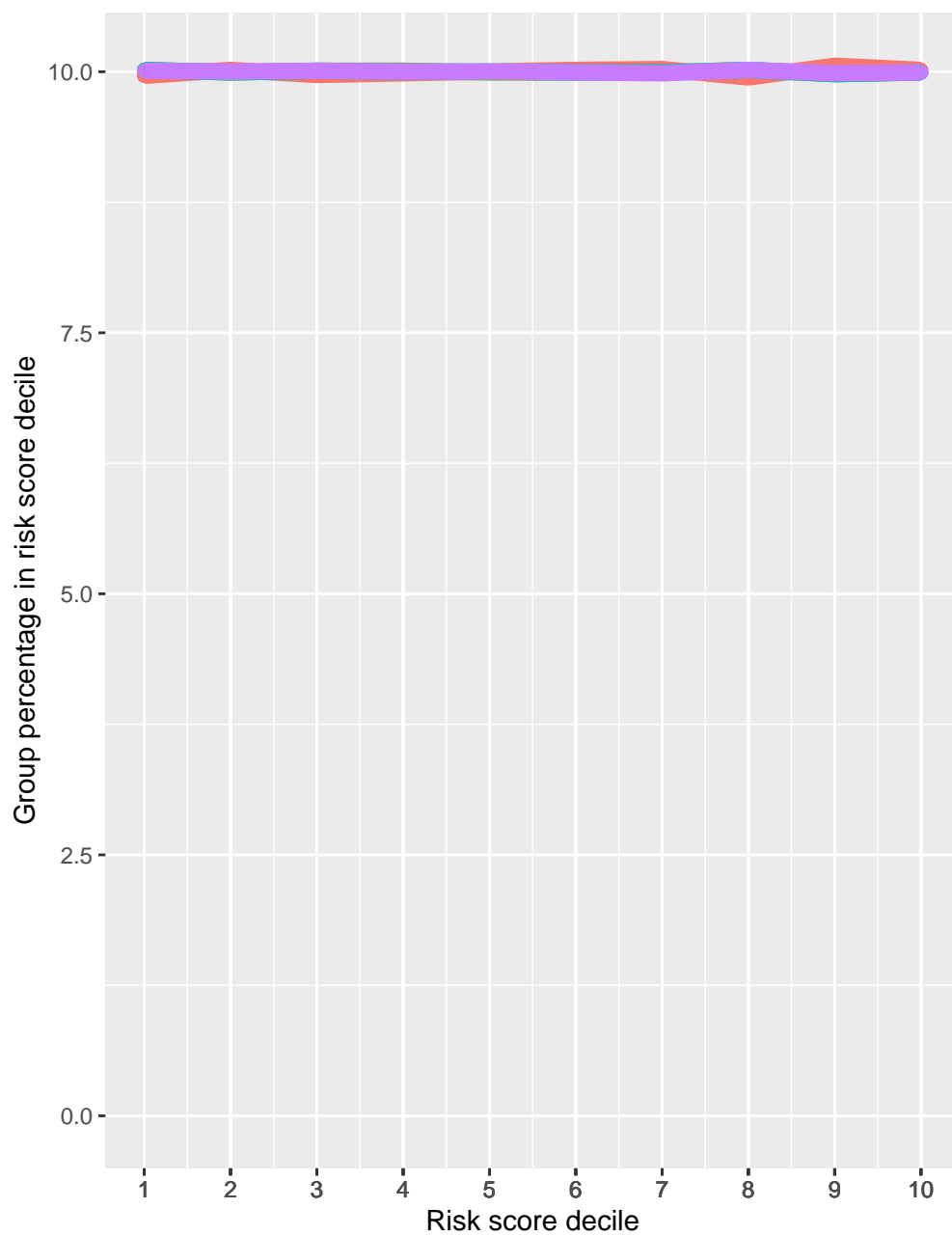
as.factor(IV)

- IV:
instrument_reden_beeindiging_historie_succesvol__doelstelling_bereikt: 0
- IV:
instrument_reden_beeindiging_historie_succesvol__doelstelling_bereikt: 1
- IV:
instrument_reden_beeindiging_historie_succesvol__doelstelling_bereikt: 2
- IV:
instrument_reden_beeindiging_historie_succesvol__doelstelling_bereikt: 3

synth



synth_conditional



as.factor(IV)

- IV: instrument_reden_beeindiging_historie_succesvol__doelstelling_bereikt: 0
- IV: instrument_reden_beeindiging_historie_succesvol__doelstelling_bereikt: 1
- IV: instrument_reden_beeindiging_historie_succesvol__doelstelling_bereikt: 2
- IV: instrument_reden_beeindiging_historie_succesvol__doelstelling_bereikt: 3

real: Excess share in highest risk decile

Excess share in highest risk decile

IV:
instrument_reden_beeindiging_historie_succesvol__doelstelling_bereikt: 3

IV:

IV:
instrument_reden_beeindiging_historie_succesvol__doelstelling_bereikt: 2

IV:

IV:
instrument_reden_beeindiging_historie_succesvol__doelstelling_bereikt: 1

IV:

IV:
instrument_reden_beeindiging_historie_succesvol__doelstelling_bereikt: 0

IV:

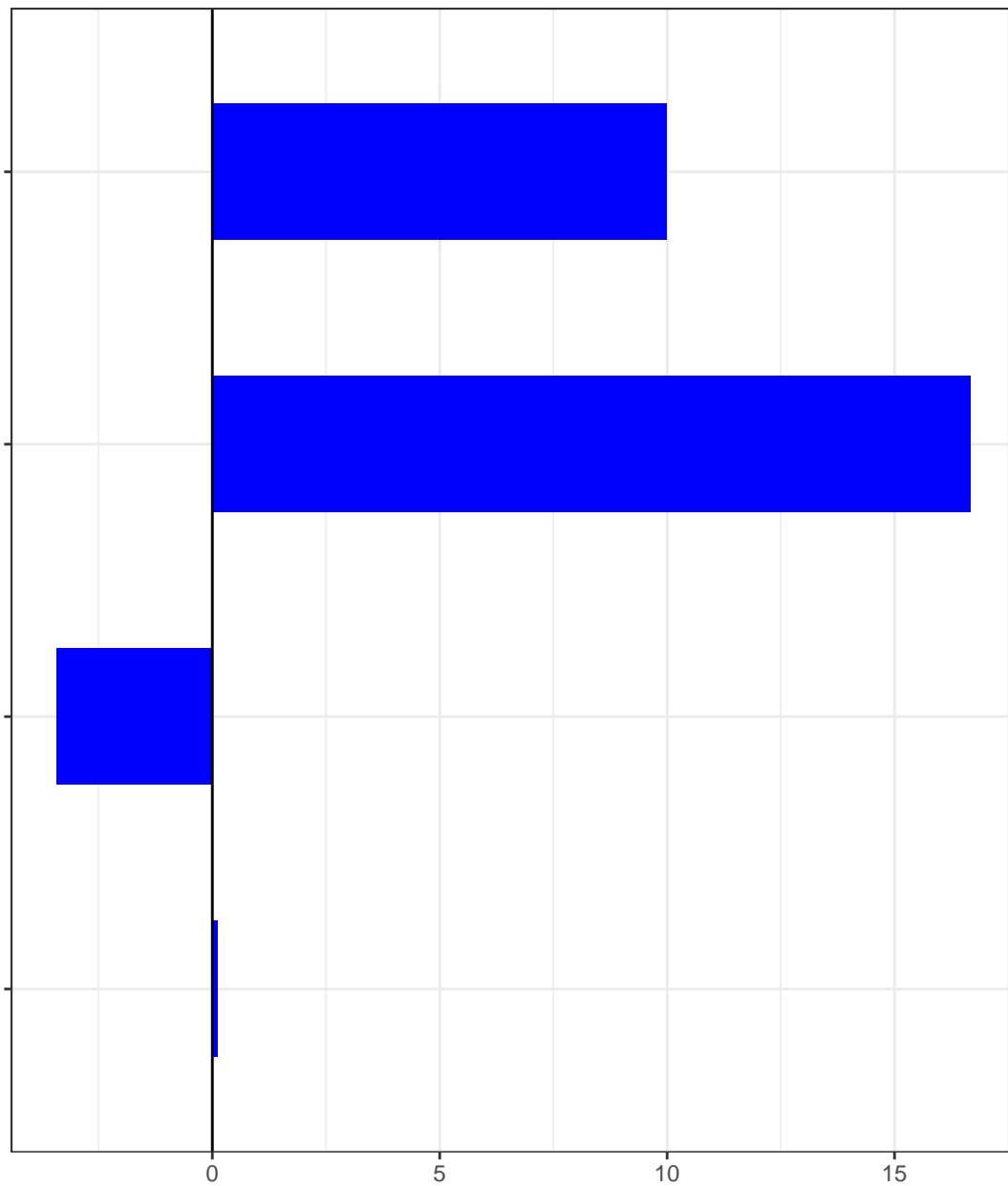
0

5

10

15

perc_misrep_td_threshold



real_conditional: Excess share in highest risk decile

Excess share in highest risk decile

IV:
instrument_reden_beeindiging_historie_succesvol__doelstelling_bereikt: 3

IV:
instrument_reden_beeindiging_historie_succesvol__doelstelling_bereikt: 2

IV:
instrument_reden_beeindiging_historie_succesvol__doelstelling_bereikt: 1

IV:
instrument_reden_beeindiging_historie_succesvol__doelstelling_bereikt: 0

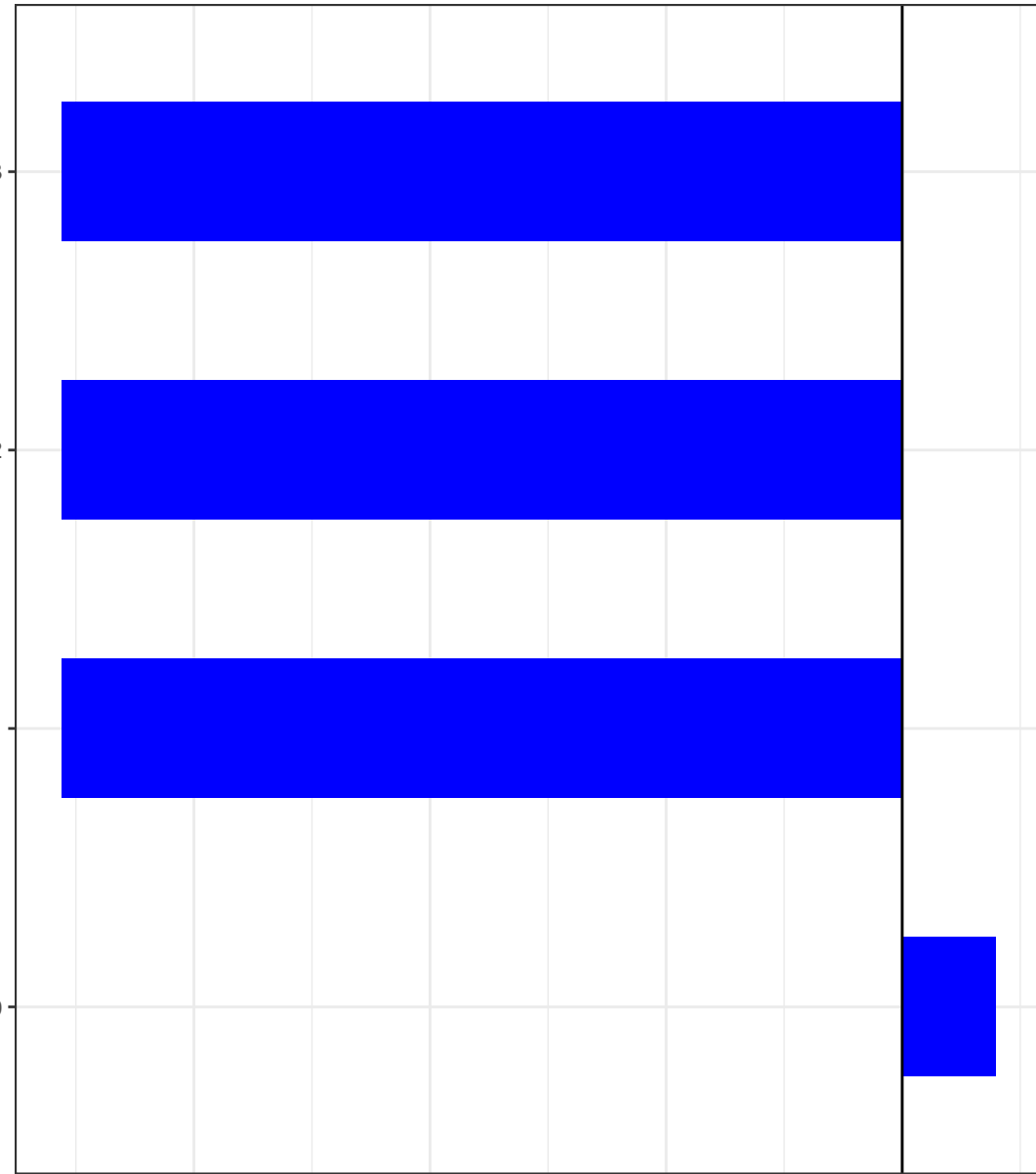
-0.03

-0.02

-0.01

0.00

perc_misrep_td_threshold

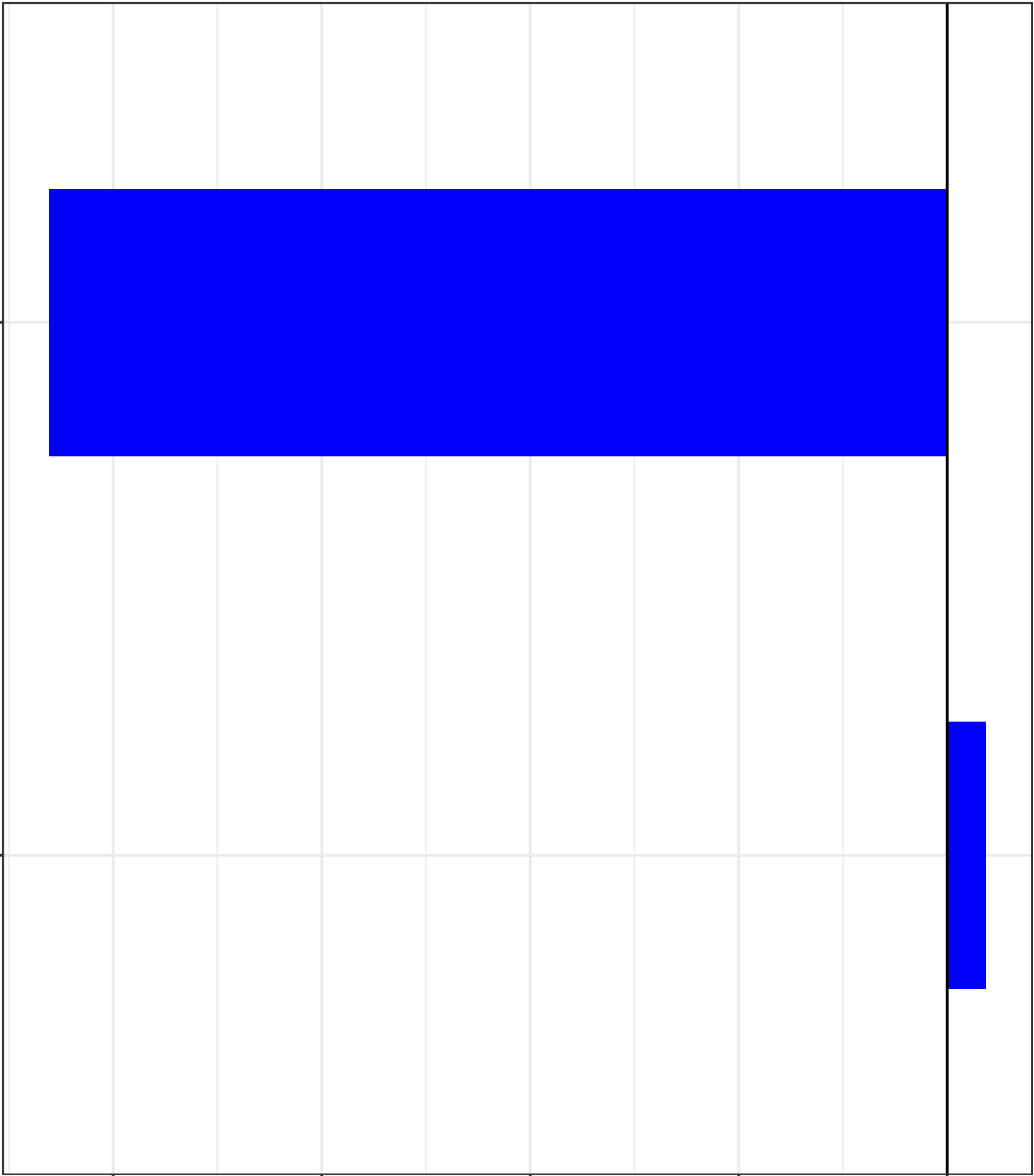


synth: Excess share in highest risk decile

Excess share in highest risk decile

IV:
instrument_reden_beeindiging_historie_succesvol__doelstelling_bereikt: 1

IV:
instrument_reden_beeindiging_historie_succesvol__doelstelling_bereikt: 0



perc_misrep_td_threshold

synth_conditional: Excess share in highest risk decile

Excess share in highest risk decile

IV:
instrument_reden_beeindiging_historie_succesvol__doelstelling_bereikt: 3

IV:
instrument_reden_beeindiging_historie_succesvol__doelstelling_bereikt: 2

IV:
instrument_reden_beeindiging_historie_succesvol__doelstelling_bereikt: 1

IV:
instrument_reden_beeindiging_historie_succesvol__doelstelling_bereikt: 0

