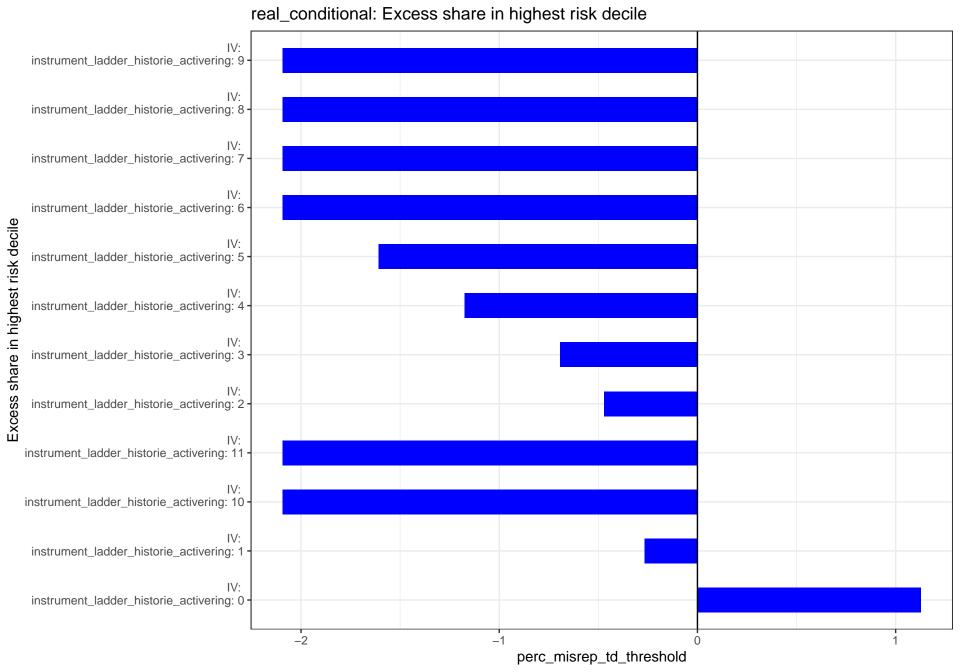


real: Excess share in highest risk decile IV: instrument_ladder_historie_activering: 9 IV: instrument_ladder_historie_activering: 8 IV: instrument_ladder_historie_activering: 7 IV: instrument_ladder_historie_activering: 6 share in highest risk decile IV: instrument_ladder_historie_activering: 5 IV: instrument_ladder_historie_activering: 4 instrument_ladder_historie_activering: 3 Excess instrument_ladder_historie_activering: 2 -IV: instrument_ladder_historie_activering: 11 -IV: instrument_ladder_historie_activering: 10 · instrument_ladder_historie_activering: 1 instrument_ladder_historie_activering: 0 20 -1010 perc_misrep_td_threshold



synth: Excess share in highest risk decile IV: instrument_ladder_historie_activering: 9 IV: instrument_ladder_historie_activering: 8 instrument_ladder_historie_activering: 7 IV: instrument_ladder_historie_activering: 6 share in highest risk decile IV: instrument_ladder_historie_activering: 5 IV: instrument_ladder_historie_activering: 4 instrument_ladder_historie_activering: 3 Excess instrument_ladder_historie_activering: 2 IV: instrument_ladder_historie_activering: 11 -IV: instrument_ladder_historie_activering: 10 · instrument_ladder_historie_activering: 1 instrument_ladder_historie_activering: 0 -5 10 -10perc_misrep_td_threshold

