









synth_conditional: Excess share in highest risk decile IV: typering_other: 6 typering_hist_ind: 1 typering_hist_aantal: 1 IV: typering_other: 5 typering_hist_ind: 1 typering_hist_aantal: 1 IV: typering_other: 4 typering_hist_ind: 1 Excess share in highest risk decile typering_hist_aantal: 1 IV: typering_other: 3 typering_hist_ind: 1 typering_hist_aantal: 1 IV: typering_other: 2 typering_hist_ind: 1 typering_hist_aantal: 1 IV: typering_other: 1 typering_hist_ind: 1 typering_hist_aantal: 1 IV: typering_other: 0 typering_hist_ind: 1 typering_hist_aantal: 1 IV: typering_other: 0 typering_hist_ind: 0 typering_hist_aantal: 0 -0.6-0.4-0.2 0.0 perc_misrep_td_threshold