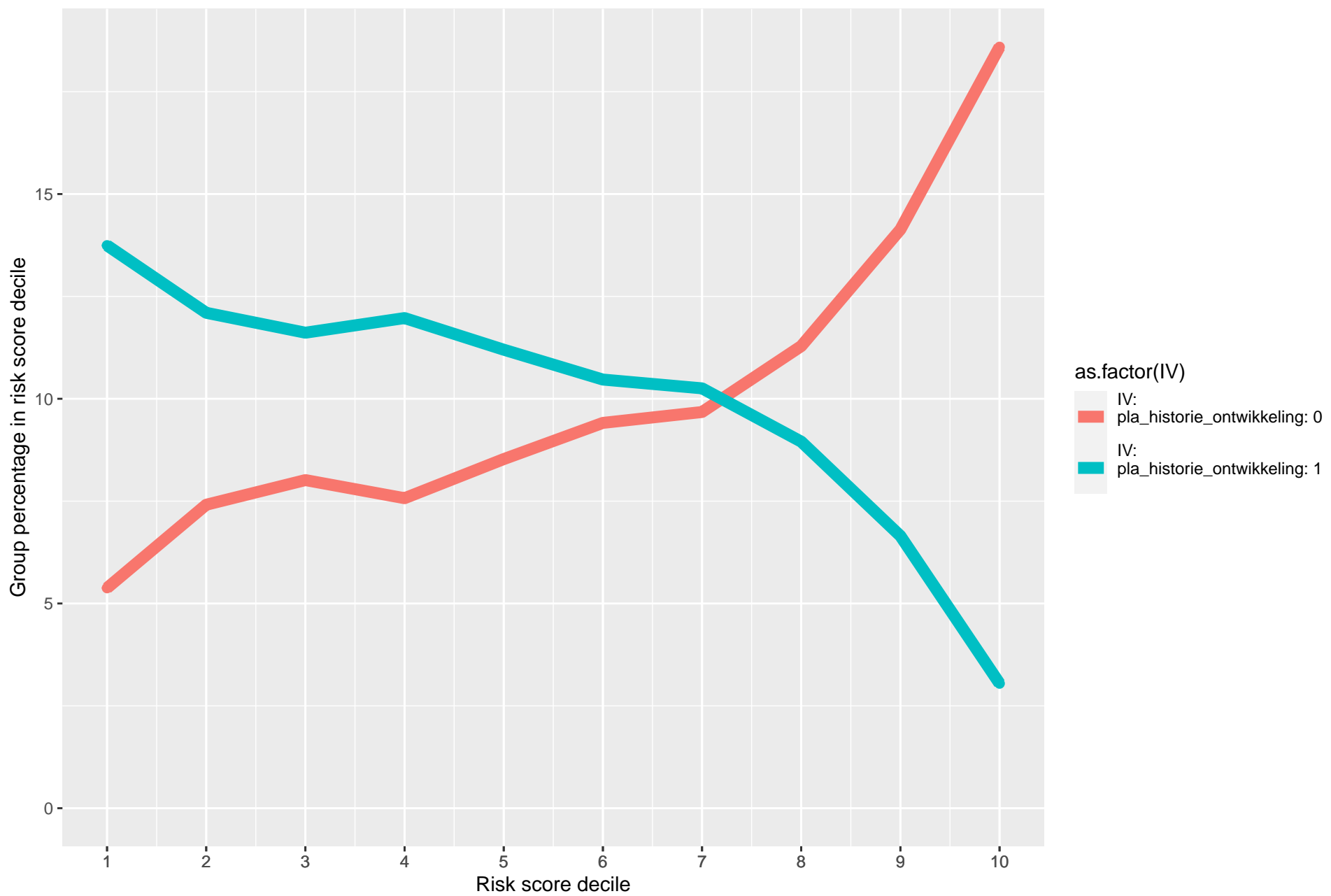
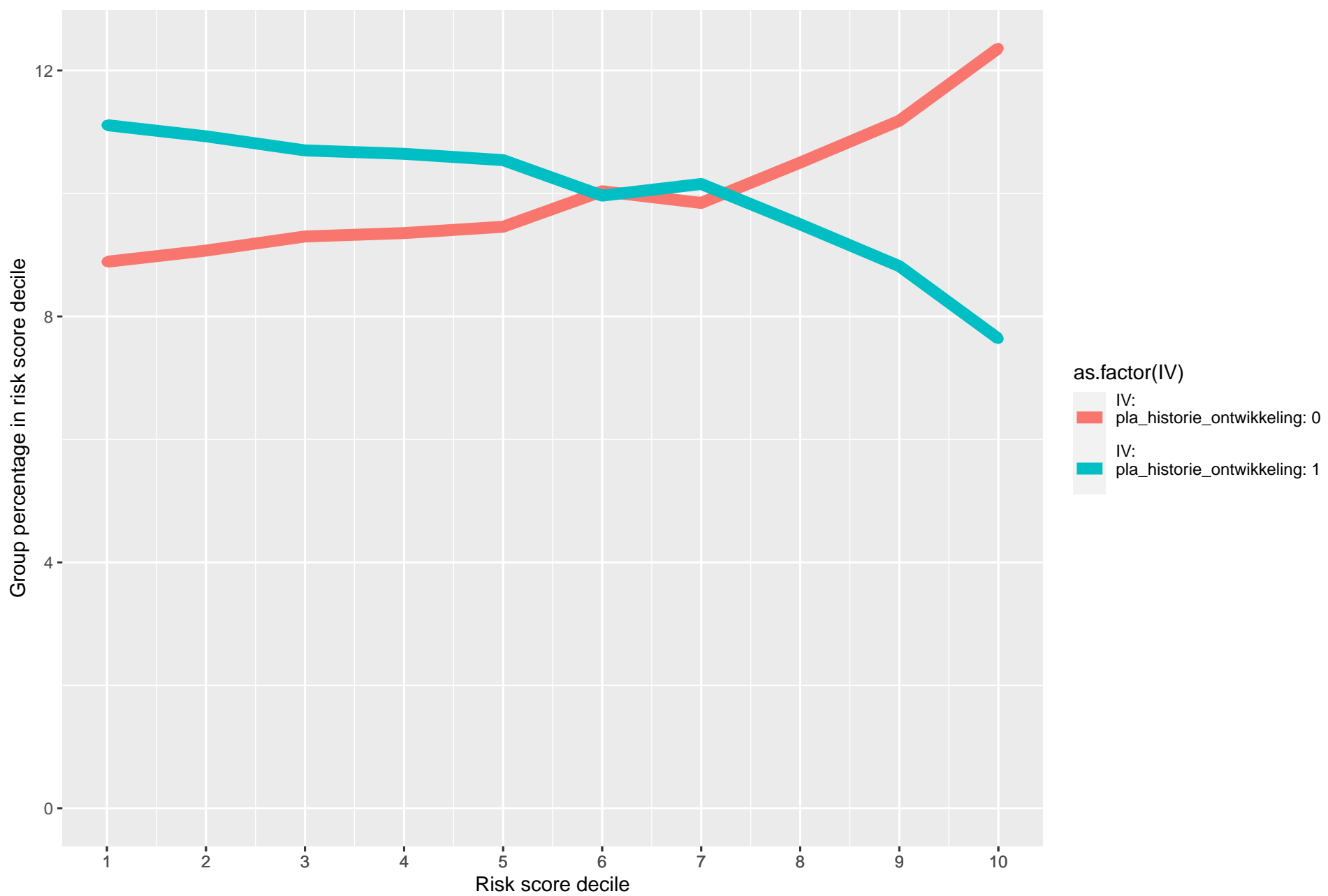


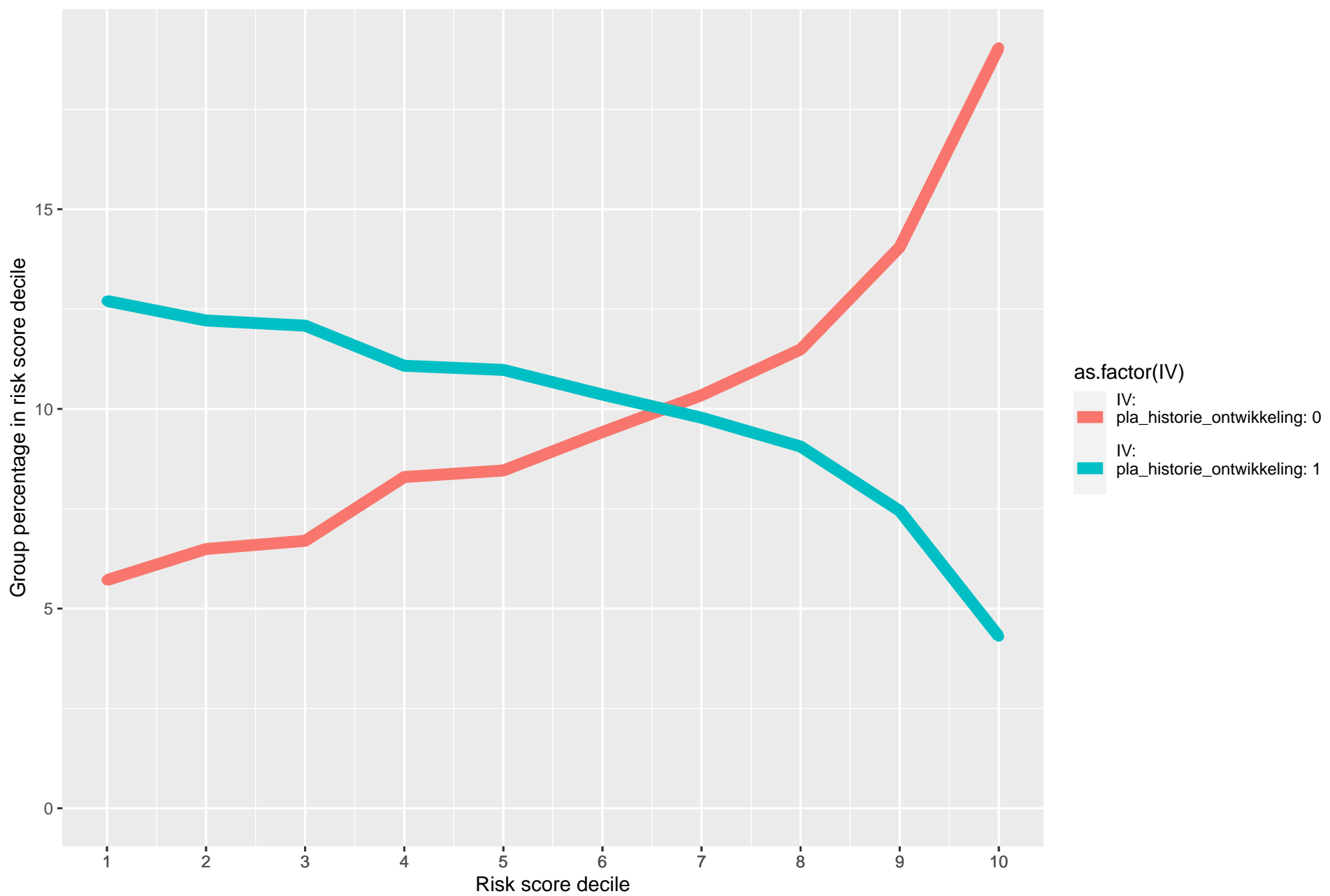
real



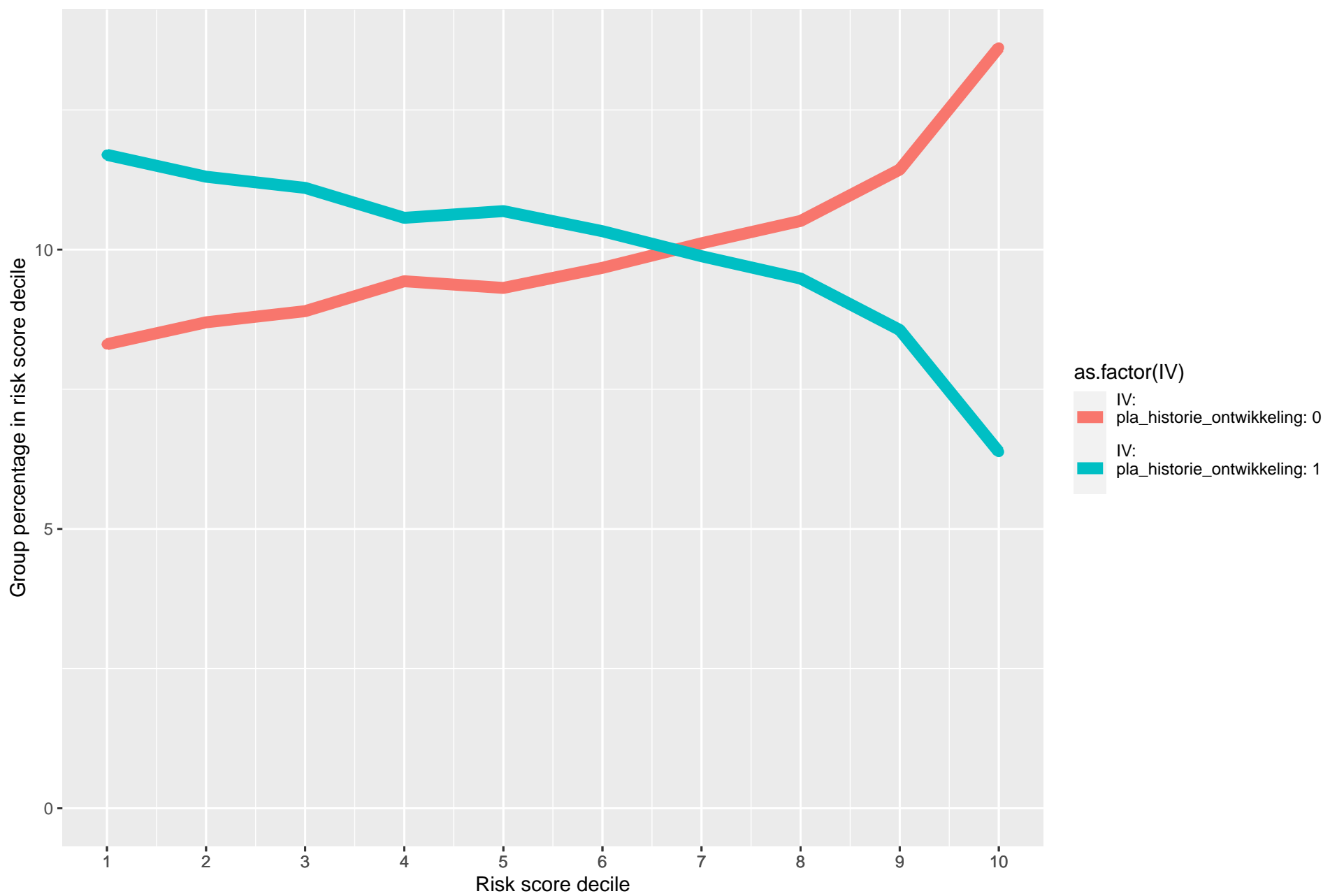
real_conditional



synth



synth_conditional

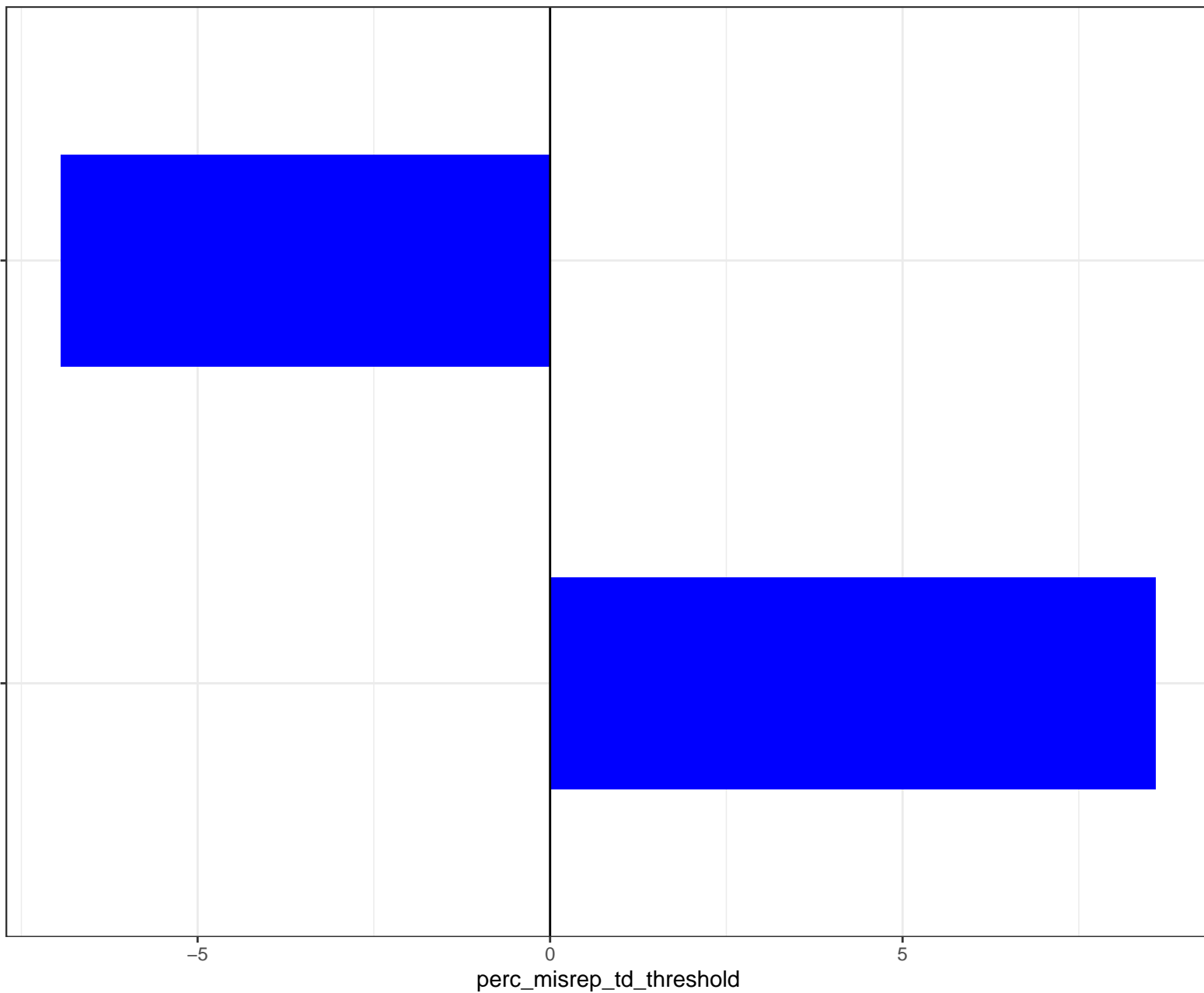


real: Excess share in highest risk decile

Excess share in highest risk decile

IV:
pla_historie_ontwikkeling: 1

IV:
pla_historie_ontwikkeling: 0



real_conditional: Excess share in highest risk decile

Excess share in highest risk decile

IV:
pla_historie_ontwikkeling: 1

IV:
pla_historie_ontwikkeling: 0

-3

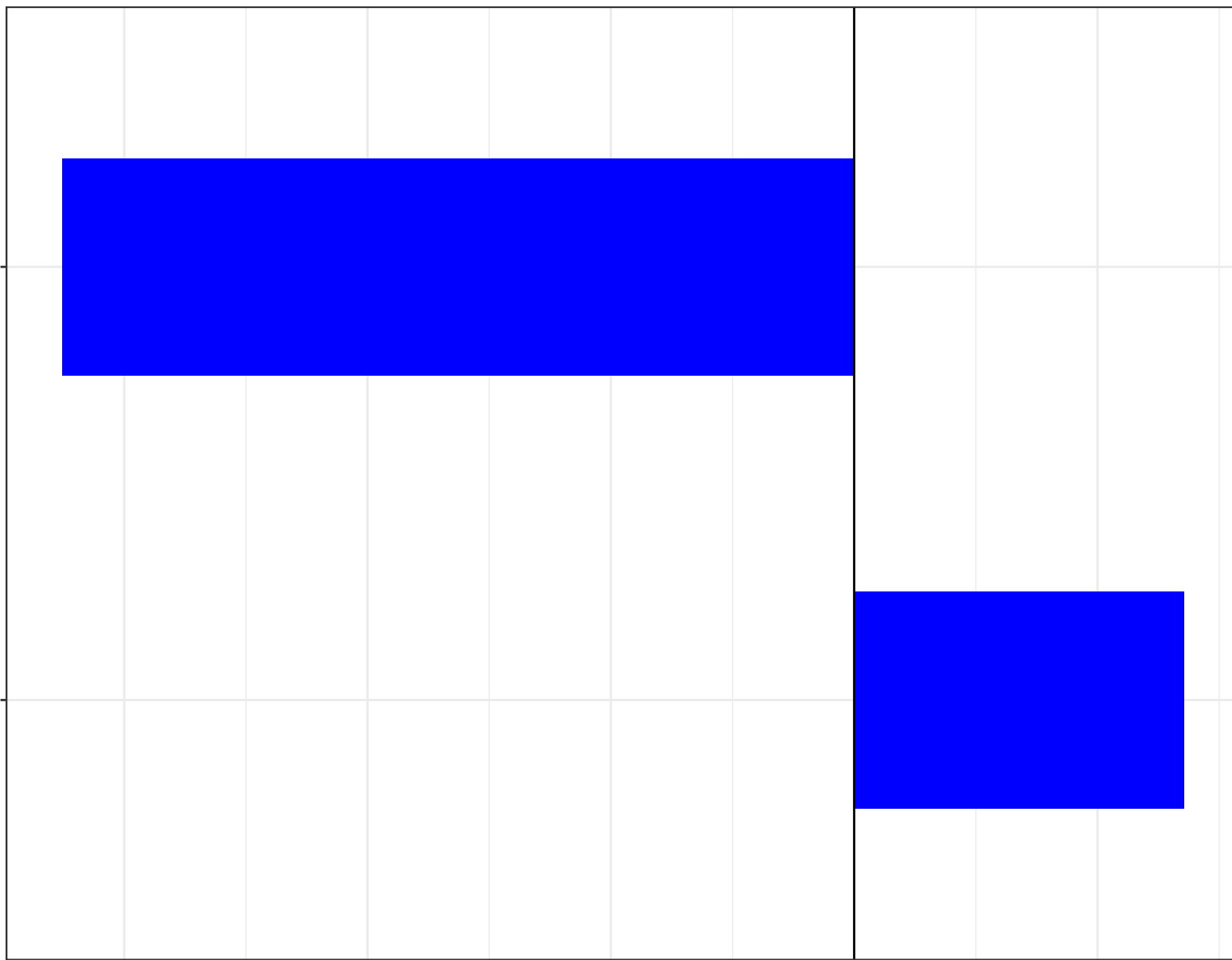
-2

-1

0

1

perc_misrep_td_threshold

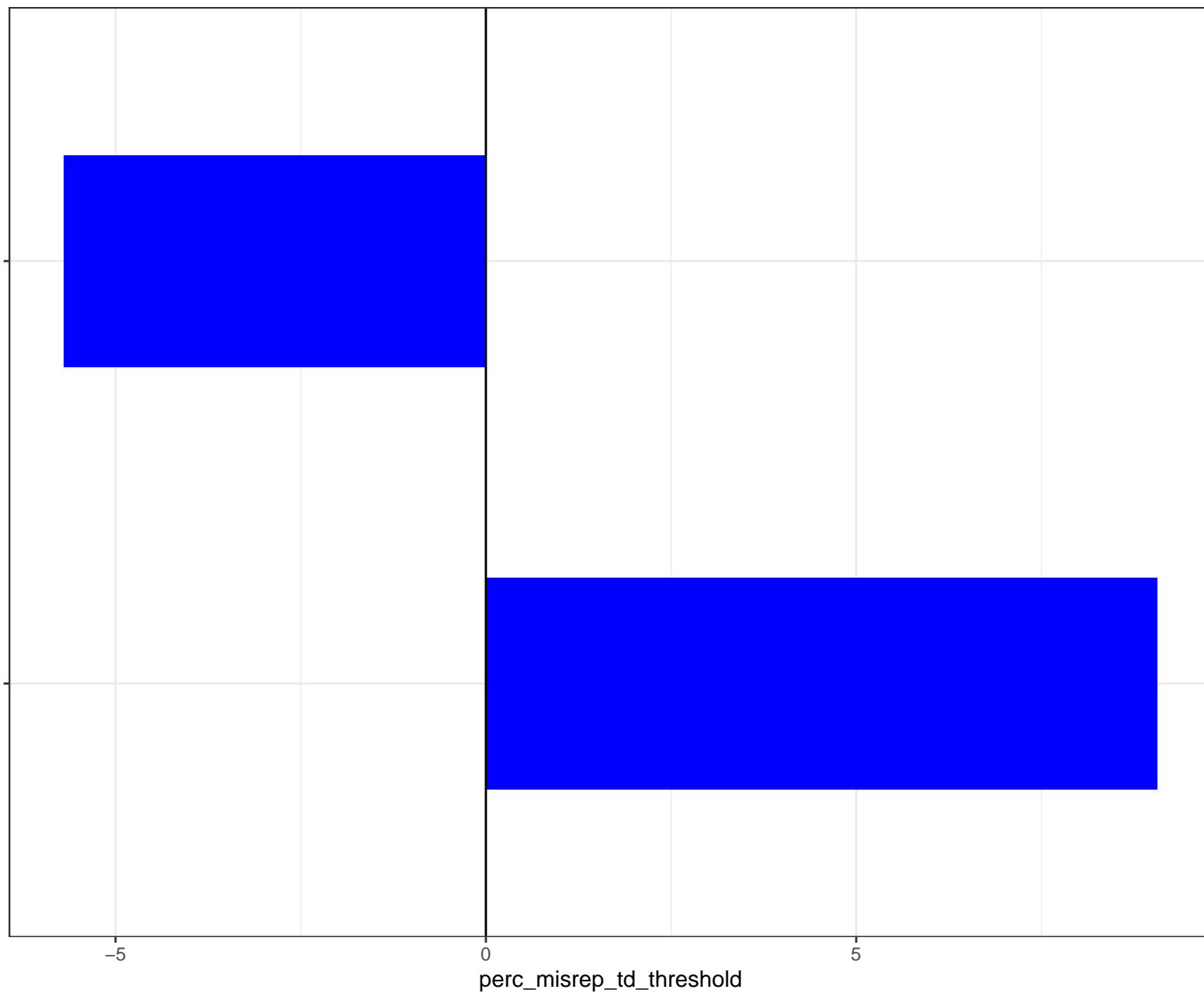


synth: Excess share in highest risk decile

Excess share in highest risk decile

IV:
pla_historie_ontwikkeling: 1

IV:
pla_historie_ontwikkeling: 0



synth_conditional: Excess share in highest risk decile

Excess share in highest risk decile

IV:
pla_historie_ontwikkeling: 1

IV:
pla_historie_ontwikkeling: 0

