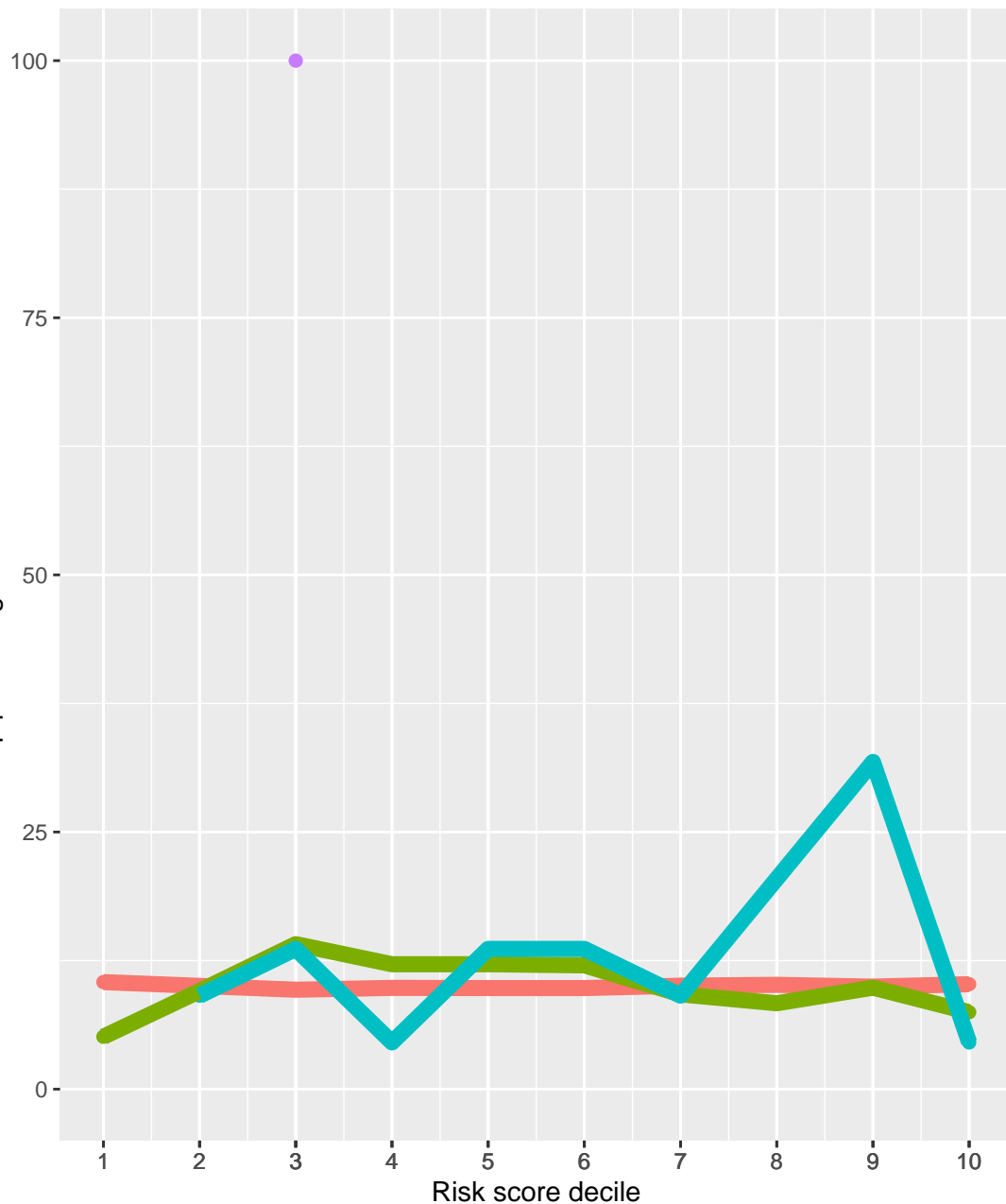


real

Group percentage in risk score decile

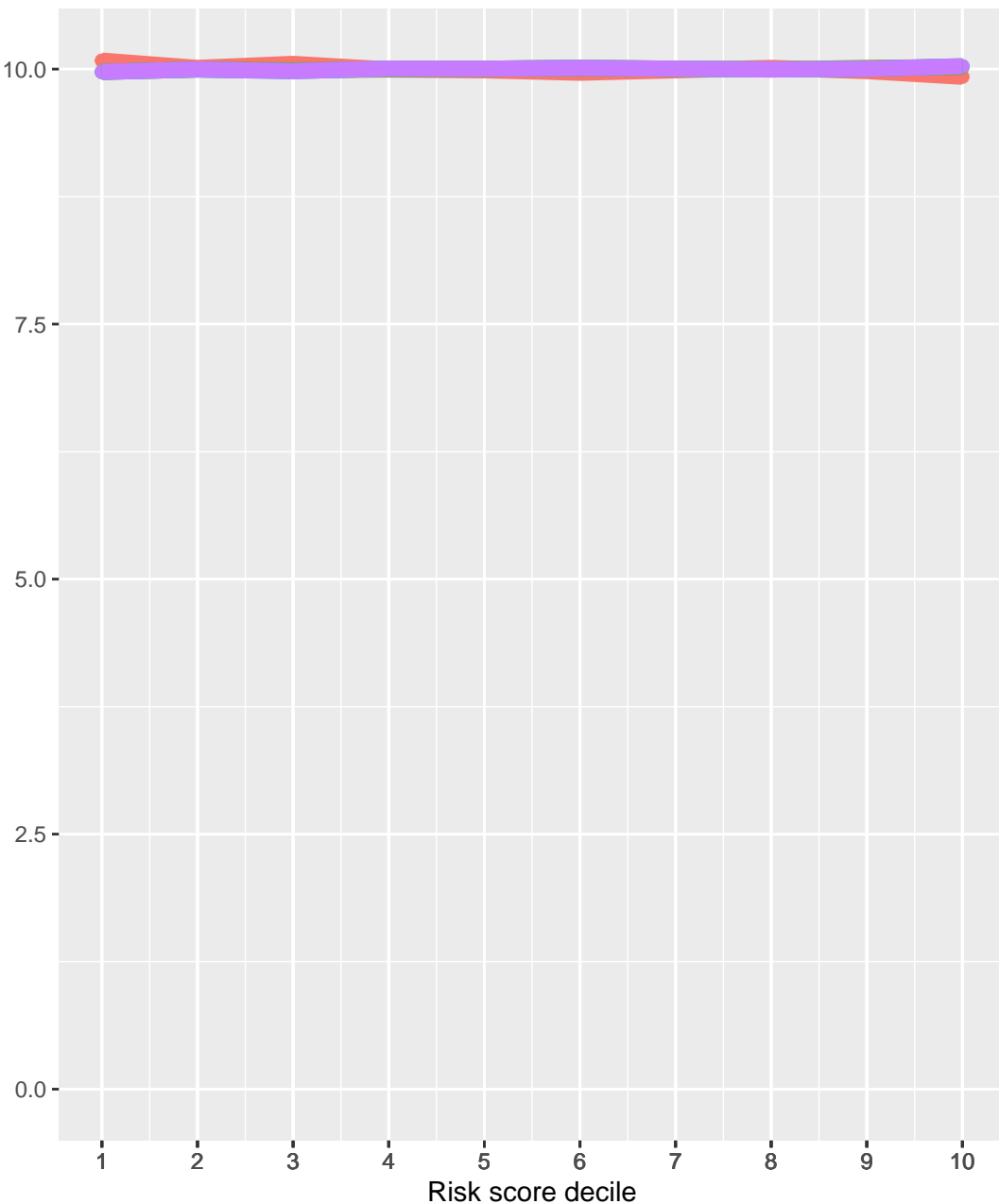


as.factor(IV)

- IV: belemmering_hist_stabiele_mix__sz____dagbesteding_werk: 0
- IV: belemmering_hist_stabiele_mix__sz____dagbesteding_werk: 1
- IV: belemmering_hist_stabiele_mix__sz____dagbesteding_werk: 2
- IV: belemmering_hist_stabiele_mix__sz____dagbesteding_werk: 3

real_conditional

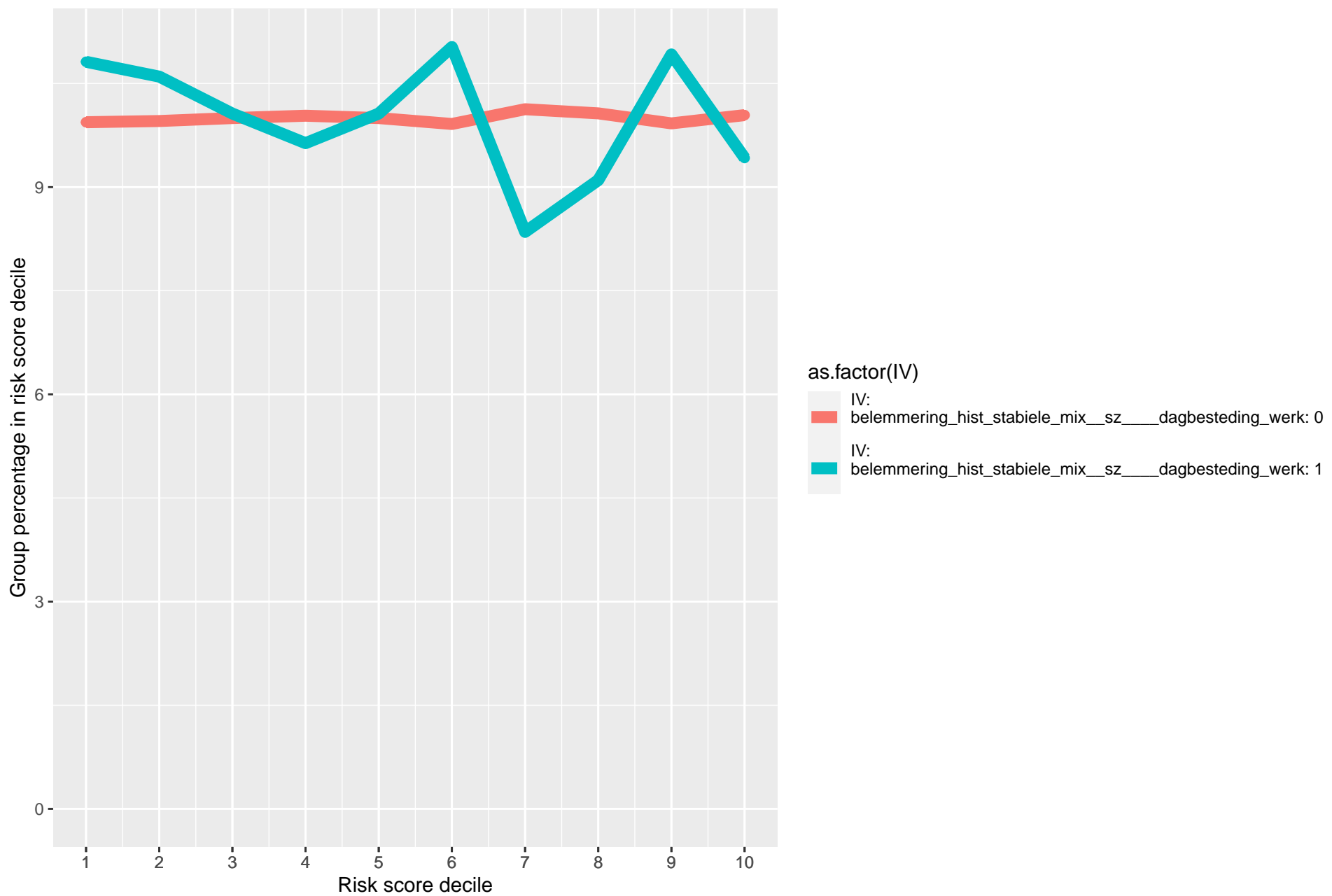
Group percentage in risk score decile



as.factor(IV)

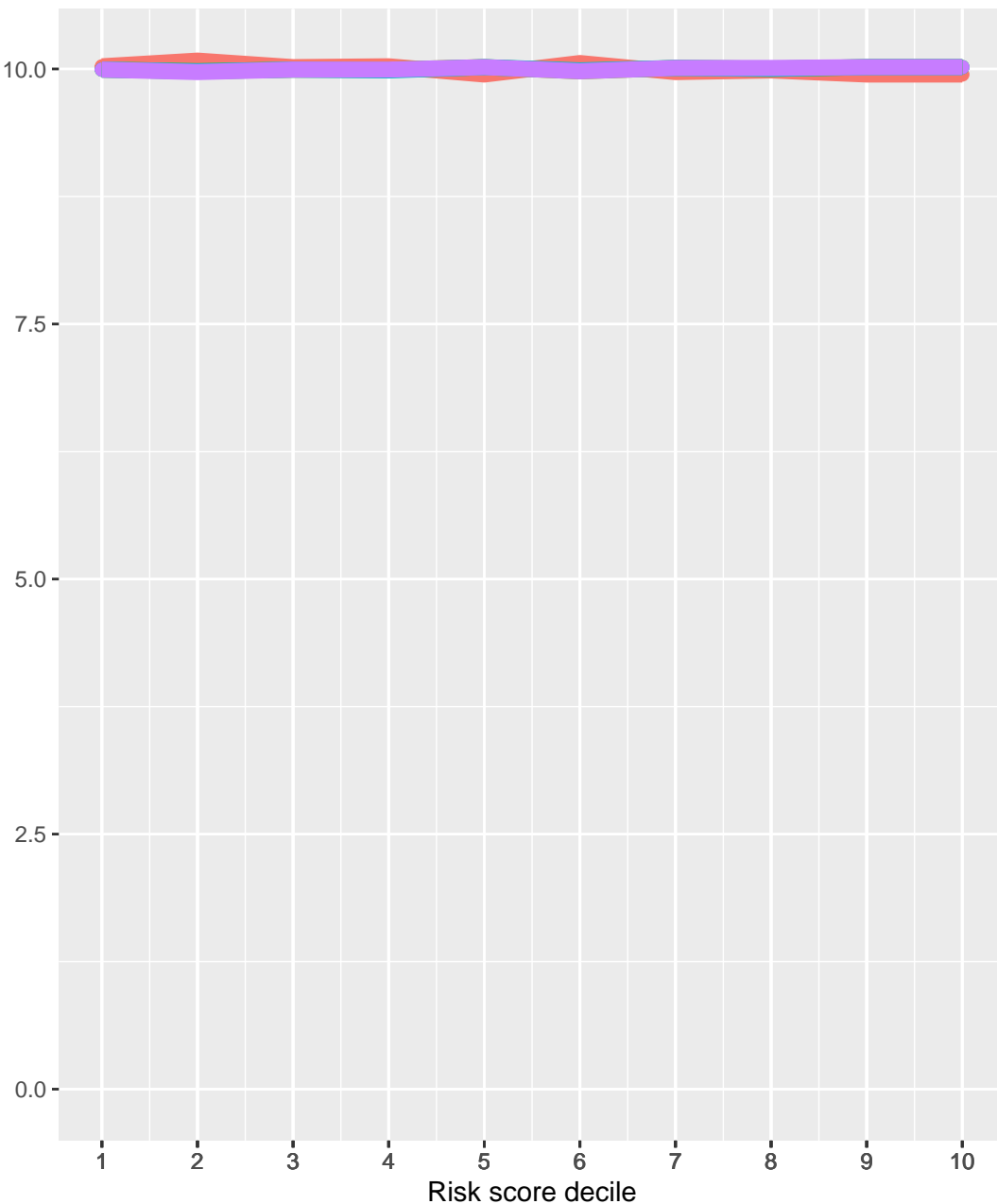
- IV: belemmering_hist_stabiele_mix__sz____dagbesteding_werk: 0
- IV: belemmering_hist_stabiele_mix__sz____dagbesteding_werk: 1
- IV: belemmering_hist_stabiele_mix__sz____dagbesteding_werk: 2
- IV: belemmering_hist_stabiele_mix__sz____dagbesteding_werk: 3

synth



synth_conditional

Group percentage in risk score decile



as.factor(IV)

- IV: belemmering_hist_stabiele_mix__sz____dagbesteding_werk: 0
- IV: belemmering_hist_stabiele_mix__sz____dagbesteding_werk: 1
- IV: belemmering_hist_stabiele_mix__sz____dagbesteding_werk: 2
- IV: belemmering_hist_stabiele_mix__sz____dagbesteding_werk: 3

real: Excess share in highest risk decile

Excess share in highest risk decile

belemmering_hist_stabiele_mix__sz____dagbesteding_werk: 3

IV:

belemmering_hist_stabiele_mix__sz____dagbesteding_werk: 2

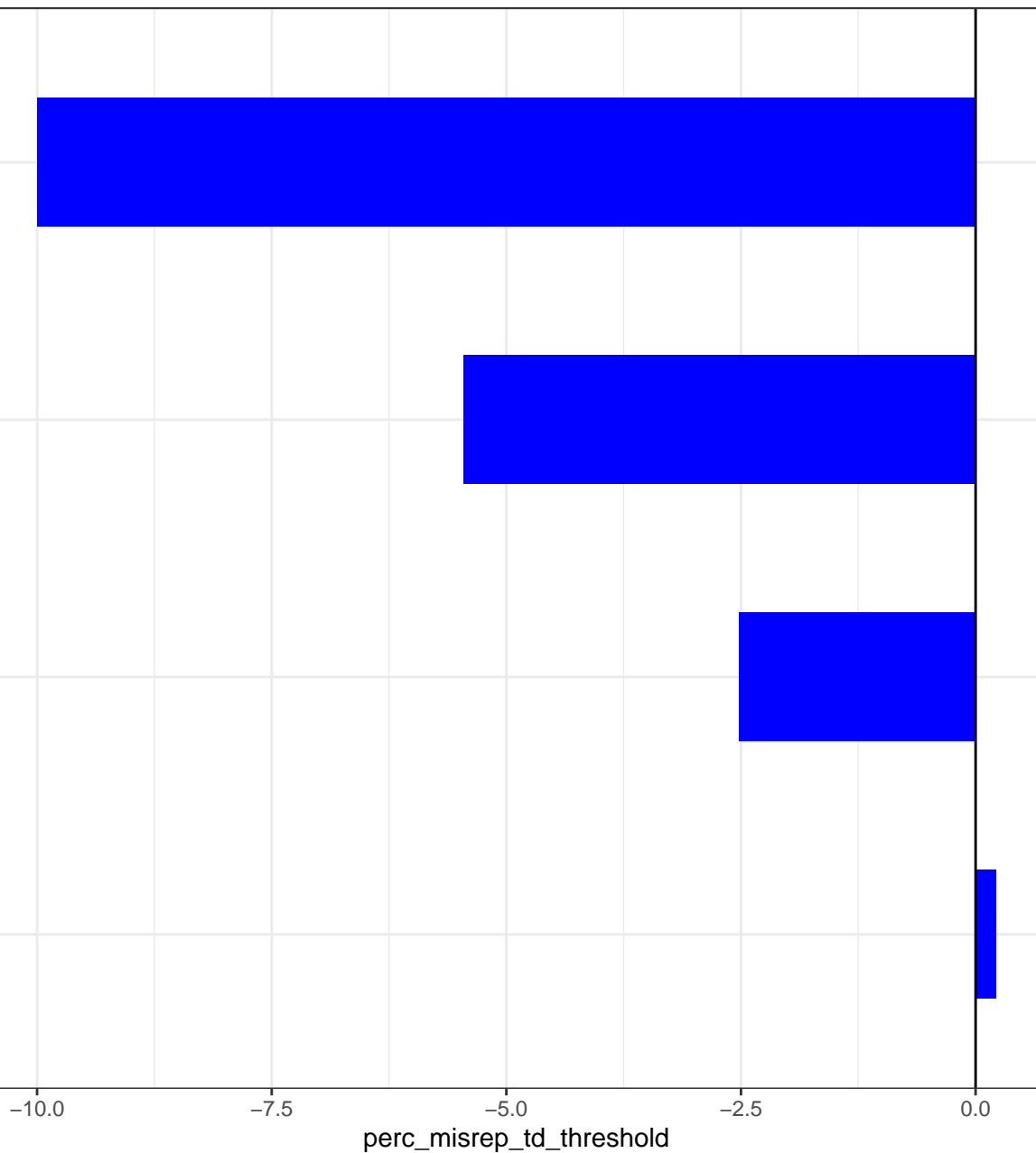
IV:

belemmering_hist_stabiele_mix__sz____dagbesteding_werk: 1

IV:

belemmering_hist_stabiele_mix__sz____dagbesteding_werk: 0

IV:



real_conditional: Excess share in highest risk decile

Excess share in highest risk decile

belemmering_hist_stabiele_mix__sz____dagbesteding_werk: 3
IV:

belemmering_hist_stabiele_mix__sz____dagbesteding_werk: 2
IV:

belemmering_hist_stabiele_mix__sz____dagbesteding_werk: 1
IV:

belemmering_hist_stabiele_mix__sz____dagbesteding_werk: 0
IV:

0.00

0.02

0.04

0.06

perc_misrep_td_threshold



synth: Excess share in highest risk decile

Excess share in highest risk decile

IV:
belemmering_hist_stabiele_mix_sz____dagbesteding_werk: 1

IV:
belemmering_hist_stabiele_mix_sz____dagbesteding_werk: 0

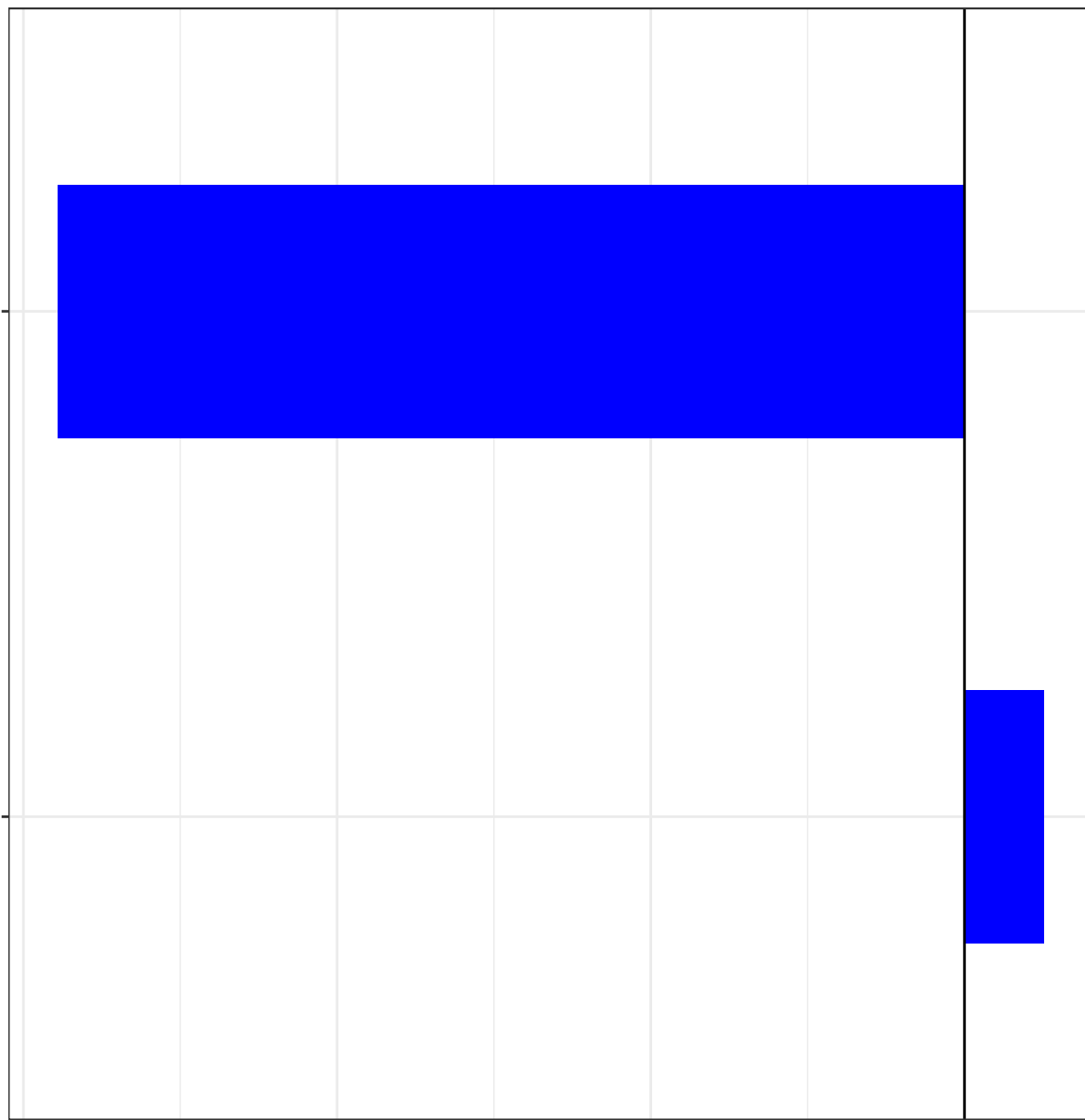
-0.6

-0.4

-0.2

0.0

perc_misrep_td_threshold



synth_conditional: Excess share in highest risk decile

Excess share in highest risk decile

belemmering_hist_stabiele_mix__sz____dagbesteding_werk: 3
IV:

belemmering_hist_stabiele_mix__sz____dagbesteding_werk: 2
IV:

belemmering_hist_stabiele_mix__sz____dagbesteding_werk: 1
IV:

belemmering_hist_stabiele_mix__sz____dagbesteding_werk: 0
IV:

