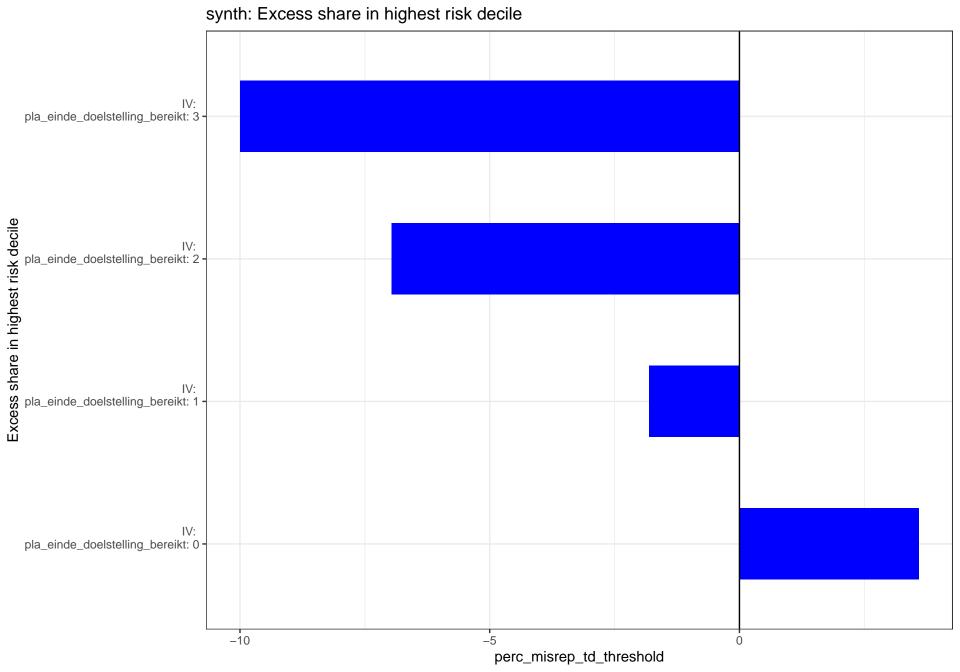


real\_conditional: Excess share in highest risk decile IV: pla\_einde\_doelstelling\_bereikt: 5 -IV: pla\_einde\_doelstelling\_bereikt: 4 · Excess share in highest risk decile IV: pla\_einde\_doelstelling\_bereikt: 3 -IV: pla\_einde\_doelstelling\_bereikt: 2 -IV: pla\_einde\_doelstelling\_bereikt: 1 IV: pla\_einde\_doelstelling\_bereikt: 0 · -0.030.00 0.03 0.06 perc\_misrep\_td\_threshold



synth\_conditional: Excess share in highest risk decile IV: pla\_einde\_doelstelling\_bereikt: 5 IV: pla\_einde\_doelstelling\_bereikt: 4 Excess share in highest risk decile IV: pla\_einde\_doelstelling\_bereikt: 3 -IV: pla\_einde\_doelstelling\_bereikt: 2 -IV: pla\_einde\_doelstelling\_bereikt: 1 IV: pla\_einde\_doelstelling\_bereikt: 0 -0.2 -0.1 0.0 0.1 0.2 perc\_misrep\_td\_threshold