

SOEN228 Midterm Cheat Sheet

System Hardware (Concordia University)



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Cheat Sheet

AΒ	A AND B	A OR B	A XOR B	A NOR B	A NAND B
00	0	0	0	1	1
01	0	1	1	0	1
10	0	1	1	0	1
11	1	1	0	0	0

Clk	S	R	Q(t+1)								
0	x	x	Q(t) (no change)						J	K	Q(t+1)
1	0	0	Q(t) (no change)						0	0	Q(t)
1	0	1	0			6 PWWW.23233.6-16	_	Local to	0	1	0
1	1	0	1	-	D	Q(t+1)	1	Q(t+1)	1	0	1
1	1	1	x	0 1 1	0 1	Q(t) 0 1	0	Q(t) $\overline{Q}(t)$	1	1	$\overline{\mathbf{Q}}(t)$

Name	Algebraic identity	
Commutative Associative Distributive Idempotent	w + y = y + w $(w + y) + z = w + (y + z)$ $w + yz = (w + y)(w + z)$ $w + w = w$	wy = yw $(wy)z = w(yz)$ $w(y+z) = wy + wz$ $ww = w$
Involution Complement de Morgan	$\overline{\overline{w}} = w$ $w + \overline{w} = 1$ $\overline{w + y} = \overline{w} \overline{y}$ $1 + w = 1$ $0 + w = w$	$w\overline{w} = 0$ $\overline{wy} = \overline{w} + \overline{y}$ $0 \cdot w = 0$ $1 \cdot w = w$

RISC-type addressing modes.						
Name	Assembler syntax	Addressing function				
Immediate	#Value	Operand = Value				
Register	Ri	EA = Ri				
Absolute	LOC	EA = LOC				
Register indirect	(Ri)	EA = [Ri]				
Index	X(Ri)	EA = [Ri] + X				
Base with index	(Ri,Rj)	EA = [Ri] + [Rj]				
EA = effective address						
Value = a signed number						
X = index value						

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