Binary Decompilation to LLVM IR

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Analyzing and optimizing programs from their executable has a long history of research pertaining to various applications including security vulnerability analysis, untrusted code analysis, malware analysis, program testing, and binary optimizations.

This work serves the same broader objective by decompiling the input binary into an intermediate form (IR) of LLVM. The main challenge of the work over and above the existing tools is to extract a richer LLVM IR including variable, type information and per procedure stack frames, which will facilitate many sophisticated analysis and optimizations.

State-of-the-art binary analysis & decompilation tools either operate on ad-hoc intermediate representations ([1, 5, 8]) or are not open sourced ([1, 5, 8, 4, 6, 14]). There are some frameworks which recover LLVM IR from executable. RevNIC [10] present a method for dynamically translating x86 to LLVM using QEMU[7]. It recovers the IR by merging the translated blocks, but the recovered IR is incomplete and is only valid for current execution; consequently, various whole program analysis will provide incomplete information. RevGen [11] includes a static disassembler to recover an IR, but they do not provide variable recovery or its promotion to symbols.

For our current work we have used a publicly available tool called McSema [2] which converts x86 machine code to functional LLVM IR. One of the limitation of McSema recovered IR is that the extracted IR misses high level information like variable and types. Moreover, Mcsema uses a big flat array to model the runtime process stack which is shared by all the procedures. This inhibits many aggressive optimizations on the stack because of potential aliases between procedures. Also the machines registers are mapped in memory in the recovered IR and not promoted to virtual registers.

As mentioned earlier, McSema models the reads/writes made by a binary on its runtime stack into a global array. The first step towards our goal of obtaining richer IR is to identify variables in this array and promote them as symbols. This requires deconstructing the global array into per procedure array which is used for modeling the stack frame of that procedure. This stack frame re-construction is important because doing symbol promotion right on the global array could be very conservative because an indirect write made by a different procedure may prevent symbol promotion in the current procedure. This has already been implemented and currently we are working on variable recovery and symbol promotion schemes based on [4, 6].

As a side step, we have developed tools [3] on top of Giri[15] for better debugging of McSema generated IR. Notables are "Source Mapper", which maps source (input binary) information to generated LLVM IR, and a "Backward Slicer" for McSema generated IR. Also we have identified optimization opportunities, like scalar replacement of aggregates, in the design of McSema generated IR to improve its quality.

The next goal, data-type recovery, aims at representing every symbol in the IR with a meaningful type instead of the generic types in the McSema recovered IR. The inferred types not only enables many sophisticated analysis (e.g. pointer analysis) but can also be used to rewrite optimized machine code for different architectures. The plan is to first develop a simple type inferencer based on external function calls and arithmetic operations; then a more sophisticated

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References

- [1] Hex-Rays Decompiler. https://www.hex-rays.com/products/decompiler/index.shtml.
- [2] Mcsema. https://github.com/trailofbits/mcsema.
- [3] Source Mapper and Backward Slicer for Mcsema generated IR. https://github.com/sdasgup3/llvm-slicer.
- [4] K. Anand, M. Smithson, K. Elwazeer, A. Kotha, J. Gruen, N. Giles, and R. Barua, *A compiler-level intermediate representation based binary analysis and rewriting system*, in Proceedings of the 8th ACM European Conference on Computer Systems, EuroSys '13, New York, NY, USA, 2013, ACM, pp. 295–308.
- [5] G. BALAKRISHNAN, R. GRUIAN, T. REPS, AND T. TEITELBAUM, Codesurfer/x86—a platform for analyzing x86 executables, in Proceedings of the 14th International Conference on Compiler Construction, CC'05, Berlin, Heidelberg, 2005, Springer-Verlag, pp. 250–254.
- [6] G. BALAKRISHNAN AND T. REPS, DIVINE: Discovering Variables in Executables, in Proceedings of the 8th International Conference on Verification, Model Checking, and Abstract Interpretation, VMCAI'07, Berlin, Heidelberg, 2007, Springer-Verlag, pp. 1–28.
- [7] F. BELLARD, QEMU, a fast and portable dynamic translator, in Proceedings of the Annual Conference on USENIX Annual Technical Conference, ATEC '05, Berkeley, CA, USA, 2005, USENIX Association, pp. 41–41.
- [8] D. Brumley, I. Jager, T. Avgerinos, and E. J. Schwartz, *BAP: A Binary Analysis Platform*, Springer Berlin Heidelberg, Berlin, Heidelberg, 2011, pp. 463–469.
- [9] V. Chipounov and G. Candea, *Dynamically translating x86 to llvm using QEMU*, tech. rep., 2010.
- [10] ——, Reverse engineering of binary device drivers with RevNIC, in Proceedings of the 5th European Conference on Computer Systems, EuroSys '10, New York, NY, USA, 2010, ACM, pp. 167–180.
- [11] ——, Enabling sophisticated analyses of x86 binaries with RevGen, in 2011 IEEE/IFIP 41st International Conference on Dependable Systems and Networks Workshops (DSN-W), June 2011, pp. 211–216.
- [12] V. CHIPOUNOV, V. KUZNETSOV, AND G. CANDEA, S2e: A platform for in-vivo multi-path analysis of software systems, in Proceedings of the Sixteenth International Conference on Architectural Support for Programming Languages and Operating Systems, ASPLOS XVI, New York, NY, USA, 2011, ACM, pp. 265–278.
- [13] K. ELWAZEER, K. ANAND, A. KOTHA, M. SMITHSON, AND R. BARUA, Scalable variable and data type detection in a binary rewriter, in Proceedings of the 34th ACM SIGPLAN Conference on Programming Language Design and Implementation, PLDI '13, New York, NY, USA, 2013, ACM, pp. 51–60.
- [14] M. NOONAN, A. LOGINOV, AND D. COK, Polymorphic type inference for machine code, in Proceedings of the 37th ACM SIGPLAN Conference on Programming Language Design and Implementation, PLDI '16, New York, NY, USA, 2016, ACM, pp. 27–41.

[15] S. K. Sahoo, J. Criswell, C. Geigle, and V. Adve, *Using likely invariants for automated software fault localization*, in Proceedings of the Eighteenth International Conference on Architectural Support for Programming Languages and Operating Systems, ASPLOS '13, New York, NY, USA, 2013, ACM, pp. 139–152.