**CLOCK GENERATOR VVC** –Quick Reference

**VVC**

For general information see UVVM VVC Framework Essential Mechanisms located in uvvm\_vvc\_framework/doc.

|  |
| --- |
| start\_clock (VVCT, vvc\_instance\_idx, msg, [scope]) |
| Example: start\_clock(CLOCK\_GENERATOR\_VVCT, 1, “Start clock generator”); |

*clock\_generator\_vvc.vhd*

|  |
| --- |
| stop\_clock (VVCT, vvc\_instance\_idx, msg, [scope]) |
| Example: stop\_clock(CLOCK\_GENERATOR\_VVCT, 1, “Stop clock generator”); |

|  |
| --- |
| set\_clock\_period (VVCT, vvc\_instance\_idx, clock\_period, msg, [scope]) |
| Example: set\_clock\_period(CLOCK\_GENERATOR\_VVCT, 1, 10 ns, “Change clock period to 10 ns”); |

|  |
| --- |
| set\_clock\_high\_time(VVCT, vvc\_instance\_idx, clock\_high\_time, msg, [scope]) |
| Example: set\_clock\_high\_time(CLOCK\_GENERATOR\_VVCT, 1, 5 ns , “Change duty cycle to 50%”); |

CLOCK GENERATOR VVC Configuration record **´vvc\_config´ --** accessible via **shared\_clock\_generator\_vvc\_config**

**Common VVC procedures applicable for this VVC**  
- See UVVM Methods QuickRef for details.

**await\_completion**() **enable\_log\_msg**() **disable\_log\_msg**()

**fetch\_result**()

**flush\_command\_queue**()  
**terminate\_current\_command**() **terminate\_all\_commands**() **insert\_delay**()

**get\_last\_received\_cmd\_idx()**

|  |  |  |
| --- | --- | --- |
| **Record element** | **Type** | **C\_CLOCK\_GENERATOR\_VVC\_CONFIG\_DEFAULT** |
| inter\_bfm\_delay | t\_inter\_bfm\_delay | C\_CLOCK\_GENERATOR\_INTER\_BFM\_DELAY\_DEFAULT |
| cmd\_queue\_count\_max | natural | C\_CMD\_QUEUE\_COUNT\_MAX |
| cmd\_queue\_count\_threshold | natural | C\_CMD\_QUEUE\_COUNT\_THRESHOLD |
| cmd\_queue\_count\_threshold\_severity | t\_alert\_level | C\_CMD\_QUEUE\_COUNT\_THRESHOLD\_SEVERITY |
| result\_queue\_count\_max | natural | C\_RESULT\_QUEUE\_COUNT\_MAX |
| result\_queue\_count\_threshold | natural | C\_RESULT\_QUEUE\_COUNT\_THRESHOLD |
| result\_queue\_count\_threshold\_severity | t\_alert\_level | C\_RESULT\_QUEUE\_COUNT\_THERSHOLD\_SEVERITY |
| bfm\_config | t\_bfm\_config | C\_CLOCK\_GENERATOR\_CONFIG\_DEFAULT |
| msg\_id\_panel | t\_msg\_id\_panel | C\_VVC\_MSG\_ID\_PANEL\_DEFAULT |
|  |  |  |

Clock Generator VVC Status record signal **´vvc\_status´ --** accessible via **shared\_clock\_generator\_vvc\_status**

|  |  |  |
| --- | --- | --- |
| **Record element** | **Type** |  |
| current\_cmd\_idx | natural |  | |
| previous\_cmd\_idx | natural |  |
| pending\_cmd\_cnt | natural |  |

VVC target parameters

|  |  |  |  |
| --- | --- | --- | --- |
| **Name** | **Type** | **Example(s)** | **Description** |
| VVCT | t\_vvc\_target\_record | CLOCK\_GENERATOR\_VVCT | VVC target type compiled into each VVC in order to differentiate between VVCs. |
| vvc\_instance\_idx | integer | 1 | Instance number of the VVC |

VVC functional parameters

|  |  |  |  |
| --- | --- | --- | --- |
| **Name** | **Type** | **Example(s)** | **Description** |
| clock\_period | time | 10 ns | Clock period |
| clock\_high\_time | time | 5 ns | Time of the clock cycle that is ‘1’. Value have to be less than clock\_period. |
| msg | string | “Read from DUT” | A custom message to be appended in the log/alert |
| scope | string | “CLOCK GENERATOR VVC” | A string describing the scope from which the log/alert originates. |

VVC entity signals

|  |  |  |  |
| --- | --- | --- | --- |
| **Name** | **Type** | **Direction** | **Description** |
| clk | std\_logic | Output | VVC Clock signal |

VVC entity generic constants

|  |  |  |  |
| --- | --- | --- | --- |
| **Name** | **Type** | **Default** | **Description** |
| GC\_INSTANCE\_IDX | natural | 1 | Instance number to assign the VVC |
| GC\_CLOCK\_GENERATOR\_CONFIG | t\_bfm\_config | C\_CLOCK\_GENERATOR\_CONFIG\_DEFAULT | Configuration for the Clock Generator. |
| GC\_CMD\_QUEUE\_COUNT\_MAX | natural | 1000 | Absolute maximum number of commands in the VVC command queue |
| GC\_CMD\_QUEUE\_COUNT\_THRESHOLD | natural | 950 | An alert will be generated when reaching this threshold to indicate that the command queue is almost full. The queue will still accept new commands until it reaches C\_CMD\_QUEUE\_COUNT\_MAX. |
| GC\_CMD\_QUEUE\_COUNT\_THRESHOLD\_SEVERITY | t\_alert\_level | WARNING | Alert severity which will be used when command queue reaches GC\_CMD\_QUEUE\_COUNT\_THRESHOLD. |
| GC\_RESULT\_QUEUE\_COUNT\_MAX | natural | 1000 | Maximum number of unfetched results before result\_queue is full. |
| GC\_RESULT\_QUEUE\_COUNT\_THRESHOLD | natural | 950 | An alert with severity 'result\_queue\_count\_threshold\_severity' will be issued if result queue exceeds this count. Used for early warning if result queue is almost full. Will be ignored if set to 0. |
| GC\_RESULT\_QUEUE\_COUNT\_THRESHOLD\_SEVERITY | t\_alert\_level | WARNING | Severity of alert to be initiated if exceeding result\_queue\_count\_threshold |

VVC details

All VVC procedures are defined in vvc\_methods\_pkg (dedicated this VVC), and uvvm\_vvc\_framework.td\_vvc\_framework\_common\_methods\_pkg (common VVC procedures)

It is also possible to send a multicast to all instances of a VVC with ALL\_INSTANCES as parameter for vvc\_instance\_idx.

*Note: Every procedure here can be called without the optional parameters enclosed in [ ].*

# VVC procedure details and examples

|  |  |
| --- | --- |
| **Procedure** | **Description** |
| **start\_clock()** | **start\_clock(VVCT, vvc\_instance\_idx, msg, [scope])**  This procedure adds a start\_clock command to the Clock Generator VVCs executor queue, which will run as soon as all preceding commands have completed. When the start\_clock command is scheduled to run, the executor activates the clock generator process in the VVC.  Example:  start\_clock(CLOCK\_GENERATOR\_VVCT, 1, “Start clock generator”, C\_SCOPE); |
| **stop\_clock()** | **stop\_clock (VVCT, vvc\_instance\_idx, msg, [scope])**  This procedure adds a stop\_clock command to the Clock Generator VVCs executor queue, which will run as soon as all preceding commands have completed. When the stop\_clock command is scheduled to run, the executor deactivates the clock generator process in the VVC after finishing current clock cycle.  Example:  stop\_clock(CLOCK\_GENERATOR\_VVCT, 1 “Stop clock generator”, C\_SCOPE); |
| **set\_clock\_period()** | **set\_clock\_period (VVCT, vvc\_instance\_idx, clock\_period, msg, [scope])**  This procedure adds a set\_clock\_period command to the CLOCK GENERATOR VVCs executor queue, which will run as soon as all preceding commands have completed. When the set\_clock\_period command is scheduled to run, the executor will change the clock period instantly.  Examples:  set\_clock\_period(CLOCK\_GENERATOR\_VVCT, 1, 10 ns, “Change clock period to 10 ns”, C\_SCOPE); |
| **set\_clock\_high\_time()** | **set\_clock\_high\_time (VVCT, vvc\_instance\_idx, clock\_high\_time, msg, [scope])**  This procedure adds a set\_clock\_high\_time command to the CLOCK\_GENERATOR VVCs executor queue, which will run as soon as all preceding commands have completed. When the write command is scheduled to run, the executor changes the clock high time and the change will take effect from the next rising edge.  Examples:  set\_clock\_high\_time(CLOCK\_GENERATOR\_VVCT, 1, 6 ns, “Changing the duty cycle to 60%”, C\_SCOPE); |

# VVC Configuration

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **Record element** | **Type** | **C\_CLOCK\_GENERATOR\_CONFIG\_DEFAULT** | | **Description** | |
| inter\_bfm\_delay | t\_inter\_bfm\_delay | C\_CLOCK\_GENERATOR\_INTER\_BFM\_DELAY\_DEFAULT | Delay between any requested BFM accesses towards the DUT. - TIME\_START2START: Time from a BFM start to the next BFM start   (A TB\_WARNING will be issued if access takes   longer than TIME\_START2START).  - TIME\_FINISH2START: Time from a BFM end to the next BFM start. Any insert\_delay() command will add to the above minimum delays, giving for instance the ability to skew the BFM starting time. | |
| cmd\_queue\_count\_max | natural | C\_CMD\_QUEUE\_COUNT\_MAX | Maximum pending number in command queue before queue is full. Adding additional commands will result in an ERROR. | |
| cmd\_queue\_count\_threshold | natural | C\_CMD\_QUEUE\_COUNT\_THRESHOLD | An alert with severity “cmd\_queue\_count\_threshold\_severity” will be issued if command queue exceeds this count. Used for early warning if command queue is almost full. Will be ignored if set to 0. | |
| cmd\_queue\_count\_threshold\_severity | t\_alert\_level | C\_CMD\_QUEUE\_COUNT\_THRESHOLD\_SEVERITY | Severity of alert to be initiated if exceeding cmd\_queue\_count\_threshold | |
| result\_queue\_count\_max | natural | C\_RESULT\_QUEUE\_COUNT\_MAX | Maximum number of unfetched results before result\_queue is full. | | | |
| result\_queue\_count\_threshold | natural | C\_RESULT\_QUEUE\_COUNT\_THRESHOLD | An alert with severity 'result\_queue\_count\_threshold\_severity' will be issued if result queue exceeds this count. Used for early warning if result queue is almost full. Will be ignored if set to 0. | | | |
| result\_queue\_count\_threshold\_severity | t\_alert\_level | C\_ RESULT\_QUEUE\_COUNT\_THRESHOLD\_SEVERITY | Severity of alert to be initiated if exceeding result\_queue\_count\_threshold | | | |
| bfm\_config | t\_bfm\_config | C\_CLOCK\_GENERATOR\_CONFIG\_DEFAULT | Configuration for Clock Generator. | |
| msg\_id\_panel | t\_msg\_id\_panel | C\_VVC\_MSG\_ID\_PANEL\_DEFAULT | VVC dedicated message ID panel | |

# The configuration record can be accessed from the Central Testbench Sequencer through the shared variable array, e.g.:

shared\_clock\_generator\_vvc\_config(1).inter\_bfm\_delay.delay\_in\_time := 50 ns;

# VVC Status

The current status of the VVC can be retrieved during simulation. This is achieved by reading from the shared variable shared\_clock\_generator\_vvc\_status record from the test sequencer. The record contents can be seen below:

|  |  |  |
| --- | --- | --- |
| **Record element** | **Type** | **Description** |
| current\_cmd\_idx | natural | Command index currently running |
| previous\_cmd\_idx | natural | Previous command index to run |
| pending\_cmd\_cnt | natural | Pending number of commands in the command queue |

# Activity watchdog

The VVCs support an activity watchdog which monitors VVC activity and will alert if no VVC activity is registered within a selected timeout value. The VVCs will register their presence to the activity watchdog at start-up, and report when busy and not, using dedicated activity watchdog methods and triggering the global\_trigger\_activity\_watchdog signal, during simulations.

Include activity\_watchdog(num\_exp\_vvc, timeout, alert\_level, msg) in the testbench to start using the activity watchdog.   
Note that the clock generator VVC is included in the total registered VVCs in the activity watchdog register, but its activity is not included in the resetting of the inactivity timeout counter. More information can be found in UVVM Essential Mechanisms PDF in the UVVM VVC Framework doc folder.

# Additional Documentation

Additional documentation about UVVM and its features can be found under “/uvvm\_vvc\_framework/doc/”.

# Compilation

The Clock Generator VVC must be compiled with VHDL 2008.   
It is dependent on the following libraries

* ***UVVM Utility Library (UVVM-Util), version 2.10.0 and up***
* ***UVVM VVC Framework, version 2.7.0 and up***

Before compiling the Clock Generator VVC, assure that uvvm\_vvc\_framework and uvvm\_util have been compiled.

See UVVM Essential Mechanisms located in uvvm\_vvc\_framework/doc for information about compile scripts.

**Compile order for the Clock Generator VVC:**

|  |  |  |
| --- | --- | --- |
| **Compile to library** | **File** | **Comment** |
| bitvis\_vip\_clock\_generator | vvc\_cmd\_pkg.vhd | Clock Generator VVC command types and operations |
| bitvis\_vip\_clock\_generator | ../uvvm\_vvc\_framework/src\_target\_dependent/td\_target\_support\_pkg.vhd | UVVM VVC target support package, compiled into the Clock Generator VVC library. |
| bitvis\_vip\_clock\_generator | ../uvvm\_vvc\_framework/src\_target\_dependent/td\_vvc\_framework\_common\_methods\_pkg.vhd | Common UVVM framework methods compiled into the Clock Generator VVC library |
| bitvis\_vip\_clock\_generator | vvc\_methods\_pkg.vhd | Clock Generator VVC methods |
| bitvis\_vip\_clock\_generator | ../uvvm\_vvc\_framework/src\_target\_dependent/td\_queue\_pkg.vhd | UVVM queue package for the VVC |
| bitvis\_vip\_clock\_generator | ../uvvm\_vvc\_framework/src\_target\_dependent/td\_vvc\_entity\_support\_pkg.vhd | UVVM VVC entity support compiled into the Clock Generator VVC library |
| bitvis\_vip\_clock\_generator | clock\_generator\_vvc.vhd | Clock Generator VVC |

# Simulator compatibility and setup

See README.md for a list of supported simulators.

For required simulator setup see ***UVVM-Util*** Quick reference.

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