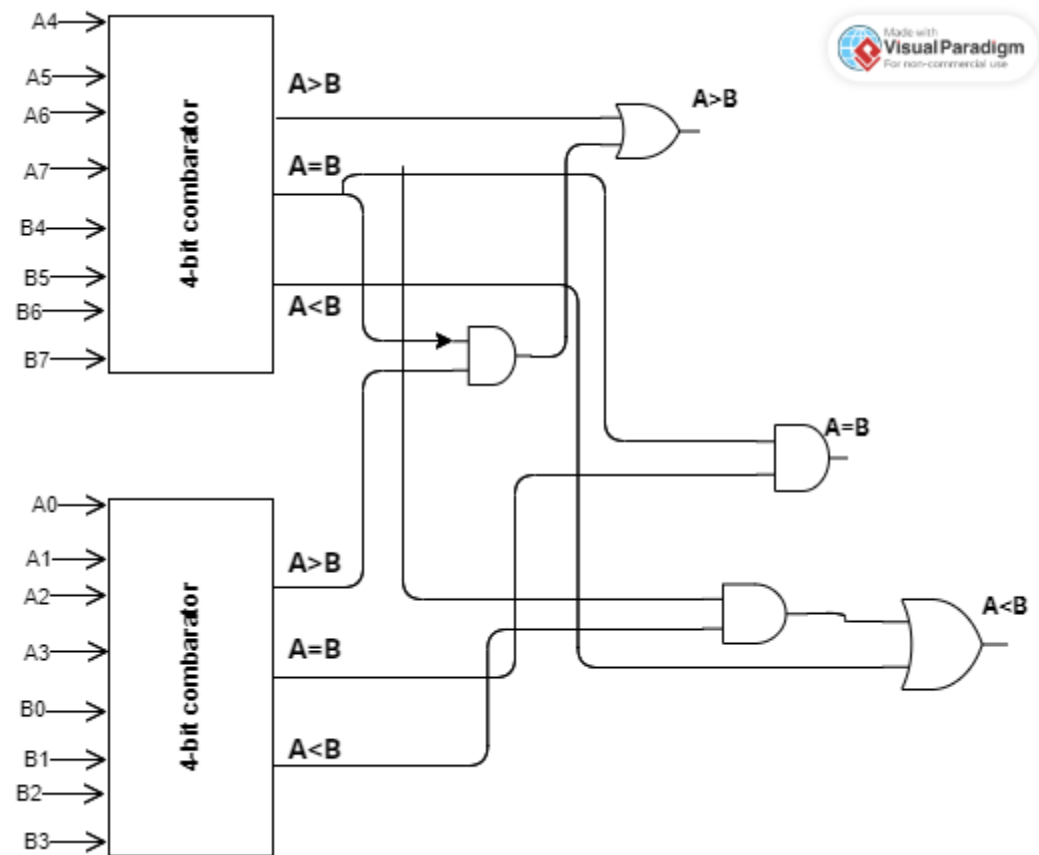


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1.Design 8-bit BCD adder.

The diagram illustrates a 4-bit comparator circuit. It consists of two 4-bit comparators. The first comparator takes inputs A0, A1, A2, A3 and B0, B1, B2, B3 and outputs three comparison signals: A > B, A = B, and A < B. These signals are then fed into a second 4-bit comparator, which produces the final outputs A > B, A = B, and A < B.

And here another answer:



3. A 4-inputs, 3-outputs circuit that compares 2-bit unsigned numbers and outputs a '1' on one of three output lines according to whether the first number is greater than, equal to, or less than the other number. You can only use two 4×1 multiplexer.

