

Aditya T

LinkedIn : Aditya T

Github : 1sand0s (For hardware design files)

YouTube : Audi tt (For software demonstration)

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EDUCATION

- **University of Texas at Austin**

PhD in Electrical and Computer Engineering; GPA: 3.87/4.0

Track : Software Engineering and Systems

Research Area : JIT Compiler Optimization, Regression Test Selection

Austin, TX

Dec. 2021 – Present

- **University of Texas at Austin**

Master of Science in Electrical and Computer Engineering; GPA: 3.87/4.0

Track : Electromagnetics and Acoustics

Research Area : EM Side-Channel Vulnerability Modeling

Austin, TX

Aug. 2019 – Dec. 2021

- **PES Institute of Technology**

Bachelor of Engineering in Electronics and Communication Engineering; GPA: 9.52/10.00

Thesis : Through Wall Imaging Using Micro-Doppler Features

Bangalore, India

Aug. 2013 – July. 2017

RELEVANT COURSE WORK

Graduate		Undergraduate	
Courses	Grade	Courses	Grade
EE382M VLSI-1	A	13MA201 Methods of Applied Mathematics	
PHY387K Electromagnetics-1	A	13EC203 Electronic Devices and Circuits	
EE383L Electromagnetic Field Theory	A	13EC402 Embedded System Design	
EE396V Laser and Optical Engineering	A	13EC353 Digital System Design using Verilog	
EE382N Computer Architecture	B-	13EC452C DSP Processor Architecture	
ASE384P Finite Element Methods	A	13EC255 Microprocessors and Microcontrollers	
SDS322 Intro to Scientific Programming	A	13EC303 Probability and Random Process	
EE379K Antenna Theory and Practice	A	13EC354 Information Theory and Coding	
EE394-7 Power Electronics Laboratory	A	13EC301 Control Engineering	
SDS394 Scientific and Technical Computing	A	13TE365 Artificial Neural Networks	
CS386D Database Systems		13EC253 Signals and System	
		13EC304 Digital Signal Processing	
		13EC302 Analog Communication	
		13EC352 Digital Communication	
		13EC403 Microwave Engineering and Radar	
		13EC205 Electromagnetic Field Theory	
		13EC401 Antenna and Wave Propagation	