Last name:	First name:	Group:
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ANSWER SHEET TO BE HANDED IN

Exercise 1

Instruction	Memory	Register	
Example	\$005000 54 AF 00 40 E7 21 48 C0	A0 = \$00005004 A1 = \$0000500C	
Example	\$005008 C9 10 11 C8 D4 36 FF 88	No change	
MOVE.W \$5006,(A1)+			
MOVE.W #36,4(A1)			
MOVE.B 3(A2),-4(A1,D1.L)			
MOVE.L -8(A1),-32(A1,D0.W)			

Exercise 2

Operation	Size (bits)	Result (hexadecimal)	N	Z	V	C
\$5A + \$A5	8					
\$7F8C + \$2000	16					
\$FFFFFFFF + \$FFFFFFFF	32					

Exercise 3

Values of registers after the execution of the program. Use the 32-bit hexadecimal representation.				
D1 = \$				
D2 = \$ D4 = \$				

sNumber			

	Computer Architecture -	- EPITA – S3 -	- 2017/2018
GetSum			

	Computer Architecture -	<u>- EPITA – S3 -</u>	- 2017/2018
CheckSum			