

## Computer-Aided VLSI System Design

### Lab3: Verification Using JasperGold

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### Checkpoints

Please follow the steps in the guideline and make sure the following goals are accomplished to get credits.

1. Figure 24 property table.

Type	Name	Engine	Bound
Assert	top.props_last_output_count	B	17
Assume	no_read_3	?	?
Assume	no_read_2	?	?
Assume	no_read_1	?	?
Assume	no_read_0	?	?

2. Figure 26 property table.

Type	Name	Engine	Bound
Assert	top.props_last_output_count	B	23
Cover (related)	top.props_i_asm_size3:precondition1	Hp	1
Assume	top.props_i_asm_size3	?	?
Cover (related)	top.props_i_asm_size2:precondition1	Hp	1
Assume	top.props_i_asm_size2	?	?

3. Figure 28 precondition cover.

Type	Name	Engine	Bound
Cover	top.props_i_cov_fifo_16	Hp (1)	Infinite
Cover	top.props_i_cov_fifo_16	Hp (1)	Infinite
Cover	top.props_i_cov_fifo_16	Hp (1)	Infinite
Cover	top.props_i_cov_fifo_16	Hp (1)	Infinite
Cover	top.props_i_cov_fifo_16	Hp (1)	Infinite
Cover	top.props_i_cov_fifo_16	Hp (1)	Infinite
Cover	top.props_i_cov_fifo_16	Hp (1)	Infinite
Cover	top.props_i_cov_fifo_16	Hp (1)	Infinite
Cover	top.props_i_cov_fifo_16	Hp (1)	Infinite
Cover	top.props_i_cov_fifo_16	Hp (1)	Infinite

## Submission

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### 1. Due Tuesday, Nov. 02, 23:59

2. Take the snapshot of the results (3 pictures) shown in previous section and create a folder named **studentID\_lab3**, and put all results into the folder

Note1: Use **lower case** for the letter in your student ID. (Ex. d06943027\_lab3)

3. Compress the folder **studentID\_lab3** in a **tar file (or zip file)** named **studentID\_lab3\_vk.tar (or .zip)** (*k* is the number of version, *k* =1,2,...)

Note2: TA will only check the last version of your result.

4. Submit to FTP
  - IP: 140.112.175.68
  - Port: 21
  - Account: 1101cvsd\_student
  - Password: ilovecvsd