**Computer-Aided VLSI System Design**

**Homework 4 Report**

**Due Tuesday, Dec. 21, 14:00**

**Student IDs:**

**Student Names:**

**Questions and Discussion**

1. Fill in the blanks

|  |  |  |
| --- | --- | --- |
| **Physical category** | | |
| Design Stage | Description | Value |
| Gate-level  Simulation | Cycle time for Gate-level Simulation  (ex. 10ns) |  |
| Gate-level Simulation Time for f1 |  |
| Gate-level Simulation Time for f2 |  |
| Gate-level Simulation Time for f3 |  |
| Gate-level Simulation Time for f4 |  |
| Gate-level Simulation Time for f5 |  |
| Gate-level Simulation Time for f6 |  |
| Gate-level Simulation Time for f7 |  |

1. Specify the methods you adopted for low-power design. (10pts)