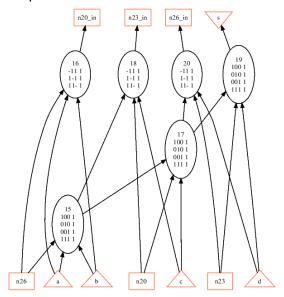
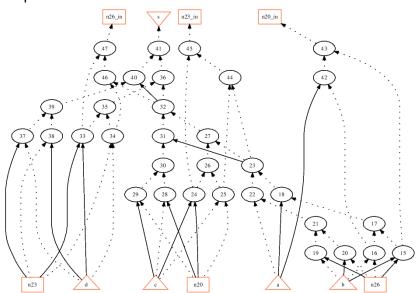
## <u> Part 1</u>

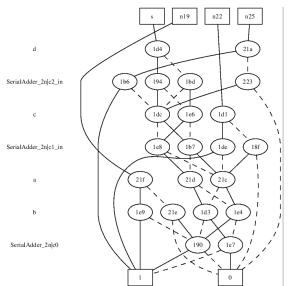
Step 3:



Step 5:



Step 7:



## Part 2

- (a) Compare the following differences with the four-number serial adder example.
- 1. aig vs. strash

aig – Converts local functions of the nodes to AIGs (representation data structure).

<u>strash</u> – Transforms the current network into an global AIG by one-level structural hashing. Structural hashing is a purely combinational transformation, which does not modify the number and positions of latches.

```
abc 09> read /home/frankchiang/cource/lsv/LSV-PA/lsv_fall_2021/pa1/sa4.blif
Hierarchy reader flattened 3 instances of logic boxes and left 0 black boxes.
abc 10> ps
SerialAdder_2n
                                   : i/o =
                                               4/
                                                      1 lat =
                                                                    3 \text{ nd} =
                                                                                  6 \text{ edge} =
                                                                                                  18
                                                                                                       cube =
                                                                                                                   21
                                                                                                                       lev = 3
abc 10> aig
abc 10> ps
SerialAdder_2n
abc 10> strash
                                               4/
                                                      1 lat =
                                                                                                       aig
                                                                                                                   39
                                   : i/o =
                                                                       nd =
                                                                                     edge =
                                                                                                  18
                                                                                                                       lev = 3
abc 11> ps
SerialAdder
                                                                    3
                                                                        and =
```

2. bdd vs. collapse

bdd - Converts local functions of the nodes to BDDs.

<u>collapse</u> – Transforms the current network into an global BDD. It recursively composes the fanin nodes into the fanout nodes resulting in a network, in which each CO is produced by a node, whose fanins are CIs. Collapsing is performed by building global functions using BDDs and is, therefore, limited to relatively small circuits. After collapsing, the node functions are represented using BDDs.

```
abc 11> read /home/frankchiang/cource/lsv/LSV-PA/lsv_fall_2021/pa1/sa4.blif Hierarchy reader flattened 3 instances of logic boxes and left 0 black boxes.
abc 12> ps
SerialAdder
                                                       4/
                                                                1 lat =
                                                                                3 \text{ nd} =
                                                                                                6 edge =
                                         : i/o =
                                                                                                                   18
                                                                                                                                       21 	ext{ lev} = 3
                                                                                                                       cube =
abc 12> bdd
abc 12> ps
SerialAdder_2n
                                         : i/o =
                                                       4/
                                                                1 lat =
                                                                                3
                                                                                    nd =
                                                                                                    edge =
                                                                                                                   18
                                                                                                                         bdd
                                                                                                                                           lev = 3
abc 12> collapse
abc 13> ps
                                                                1 lat =
SerialAdder 2n
                                         : i/o =
                                                                                   nd =
                                                                                                    edge =
```

(b) Given a structurally hashed AIG, find a sequence of ABC command(s) to covert it to a logic network with node function expressed in sum-of-products (SOP).

\$ logic

\$ sop