

## Tutorial-4

1.

Binary	Octal	Decimal	Hexadecimal
11000010	302	194	C2
11000100	704	452	1C4
100111	47	39	27
101116010	562	370	172
11001.01	31.2	25.25	19.4
11101.00001	35.02	29.03125	1D.08
10101101.000011	255.034	173.0546875	AD.0E

$$2(a) \quad (58)_{10} - (43)_{10}$$

$$\begin{array}{r} 58 \rightarrow 111010 \\ 43 \rightarrow 101011 \end{array}$$

$$\begin{array}{r} 111010 \\ + 010100 \\ \hline 1001110 \end{array}$$

As carry is generated in the result, the result is +ve, we add the carry to in the result.

$$\underline{1001111}$$

$$2's \text{ complement of } 101011 \rightarrow 010100 \rightarrow 010101$$



$$\begin{array}{r} 11010 \\ - 010101 \\ \hline 11601111 \end{array}$$

carry is generated hence result is the we ignore the carry.

$$\underline{001111}$$

b)  $(43)_{10} - (58)_{10}$

$$\begin{array}{r} 101011 \\ - 111010 \\ \hline 601111 \end{array}$$

← To verify.

Subtraction using 1's complement.

$$\begin{array}{r} 101011 \\ 000101 \\ \hline 110000 \end{array}$$

Result will be -ve

$$\underline{-001111}$$

(ii) Subtraction using 2's complement method.

$$11010 \rightarrow 000110$$

$$\begin{array}{r} 101011 \\ 000110 \\ \hline 110001 \end{array}$$

Carry is not generated  
result is -ve

$$\underline{-001111}$$



3(a)  $56742 - 2487$  using 10's complement approach.

(9's complement)

10's complement  $\cancel{2487} + 1 = 97512 + 1$

$$\begin{array}{r} 56742 \\ 97513 \\ \hline 154255 \end{array}$$

There is a carry, we ignore it

$$\underline{54255}$$

3(b)  $A2BC - B89$  using 15's complement.

$F - 0B89 \rightarrow F476$ .

$$\begin{array}{r} A2BC \\ F476 \\ \hline 198732 \end{array}$$

It is carry, we add it

$$\underline{98732}$$

4.

Decimal	Binary	8 Bit Binary	18's complement	2's complement
+33	0100001	00100001	00100001	00100001
-33	1100001	10100001	11011110	11011111



$$5. (110101)_2 \rightarrow (101111)_{\text{Gray}}$$

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	8	4	-2	-1
0	0	0	0	0
1	0	1	1	1
2	0	1	1	0
3	0	1	0	1
4	0	1	0	0
5	1	0	1	1
6	1	0	1	0
7	1	0	0	1
8	1	0	0	0
9	1	1	1	1

$$7. 95 - (-85)$$

-85 in 8-bit signed 2's complement  
11001001

-95 in 8-bit signed 2's complement  
10100001

$$\begin{array}{r} 11001001 \\ 10100001 \\ \hline 10110100 \end{array}$$

no carry in MSB, there is carry out from MSB, there is an overflow condition

To resolve overflow, we increase the no of bits

$$\begin{array}{r} 111001001 \\ 110100001 \\ \hline 1101101010 \end{array}$$

$$\rightarrow (010010110)_{\text{2's complement}} \\ -150$$