Simulation and Testing Table

Test	ALUControl[1:0]	А	В	Υ	ALUFlags
ADD 0+0	0	00000000	00000000	00000000	4
ADD 0 + (-1)	0	00000000	FFFFFFF	FFFFFFF	8
ADD 1 + (-1)	0	0000001	FFFFFFF	00000000	6
ADD FF+1	0	000000FF	0000001	00000100	0
SUB 0-0	1	00000000	00000000	00000000	6
SUB 0 - (-1)	1	00000000	FFFFFFF	0000001	0
SUB 1-1	1	0000001	0000001	00000000	6
SUB 100-1	1	00000100	0000001	000000FF	2
AND FFFFFFF, FFFFFFF	2	FFFFFFF	FFFFFFF	FFFFFFF	8
AND FFFFFFF, 12345678	2	FFFFFFF	12345678	12345678	0
AND 12345678, 87654321	2	12345678	87654321	02244220	0
AND 00000000, FFFFFFF	2	00000000	FFFFFFF	00000000	4
OR FFFFFFF, FFFFFFF	3	FFFFFFF	FFFFFFF	FFFFFFF	8
OR 12345678, 87654321	3	12345678	87654321	97755779	8
OR 00000000, FFFFFFF	3	00000000	FFFFFFF	FFFFFFF	8
OR 00000000, 00000000	3	00000000	00000000	00000000	4