Fundamental Concepts

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Key Concepts of OS

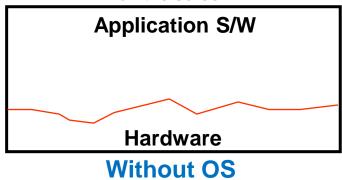
- 1. Abstraction
- 2. Protection
- 3. Illusion
- 4. Coordination and Optimization

Abstraction देशका

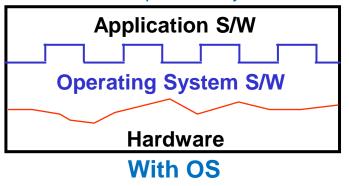
- □ In general
 - Abstraction means picking out only key features to reduce complexity
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- □ In the context of OS 하는데이의 보장성은 없어고 프고메니아 하드웨어를 운영에 제어할수
 - OS hides hardware complexity and provides a clean, 있던 인터테이스를 uniform, and standard interface to programmers 제상는 것
 - e.g.) POSIX APIs (Application Programming Interfaces)

EJARI

How can we display "Hello World" on the screen?



We can simply use printf() that is provided by OS



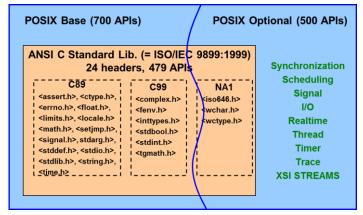
OS can be viewed as a Big Library

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- ☐ An organized collection of reusable program code
 - Bootstrapping, device drivers, scheduling, GUI ...
 - POSIX Standard API implementations

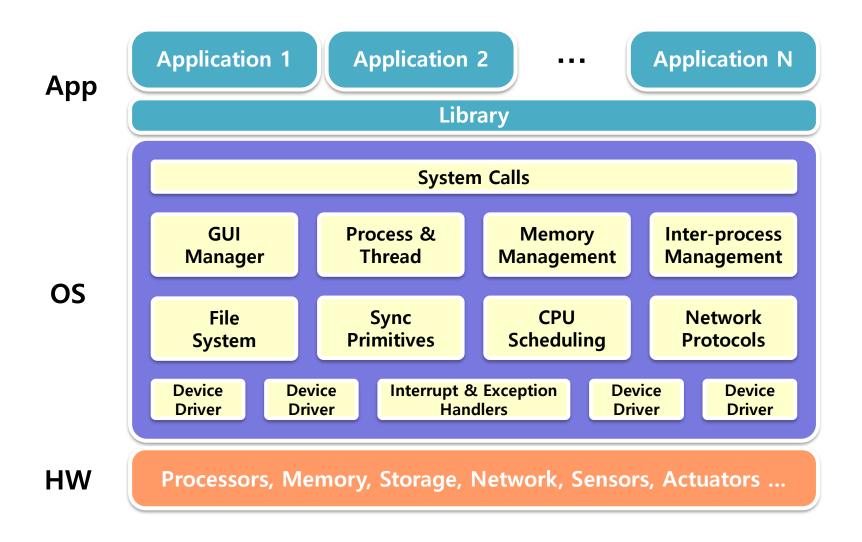


University Library



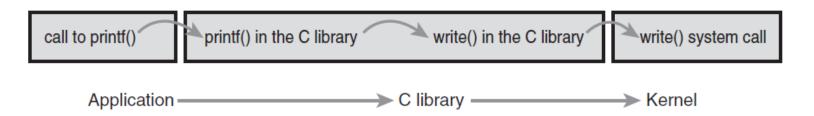
POSIX Standard 1003.1

Typical System Structure



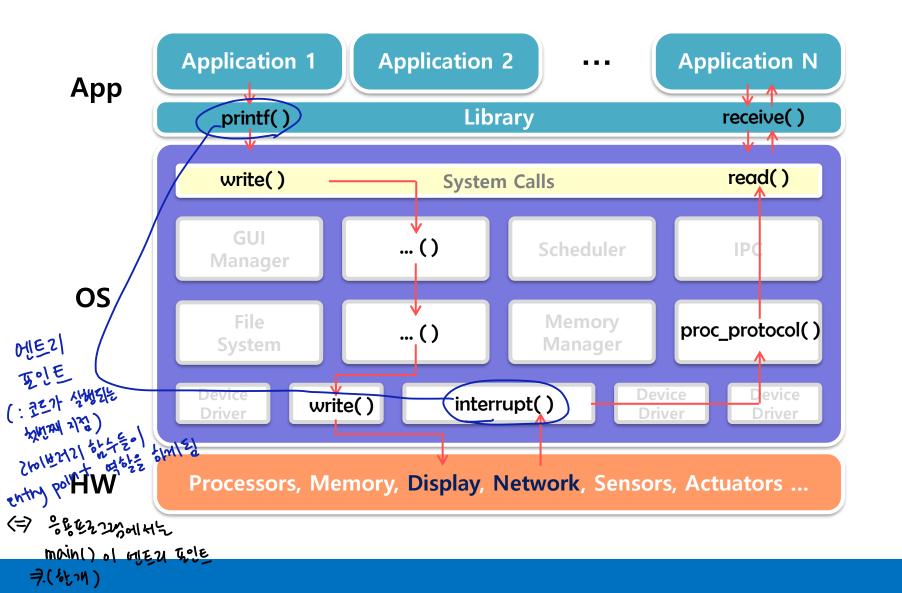
System Calls vs. Library Calls

☐ System calls provide the interface between application SW and the operating system



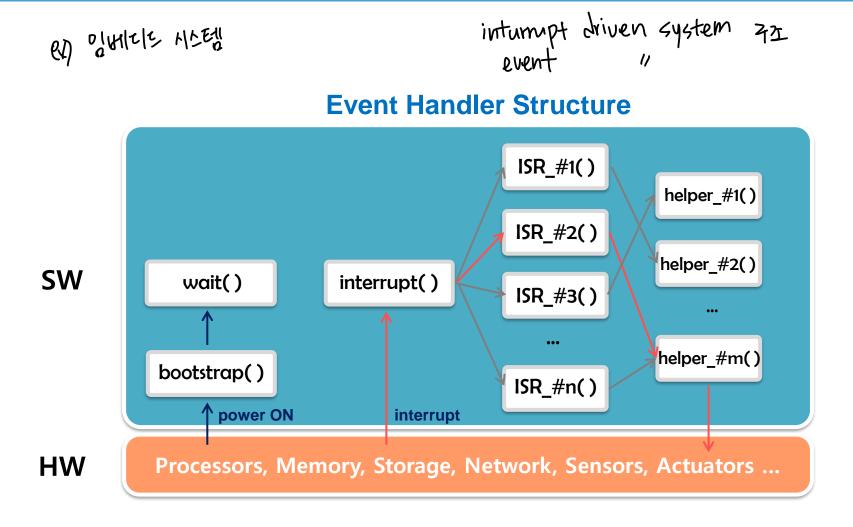
System calls	Library calls
open	fopen
close	fclose
read	fread, getchar, scanf, fscanf, getc, fgetc, gets, fgets
write	fwrite, putchar, printf, fprintf putc, fputc, puts, fputs
lseek	fseek

Execution Flow



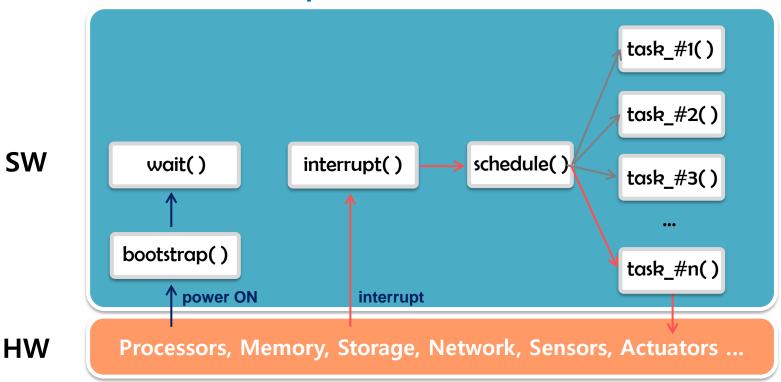
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Execution Flow without OS



Execution Flow without OS

Simple Scheduler Structure

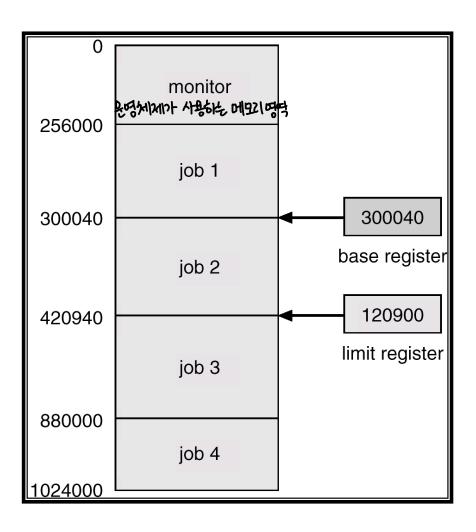


Protection

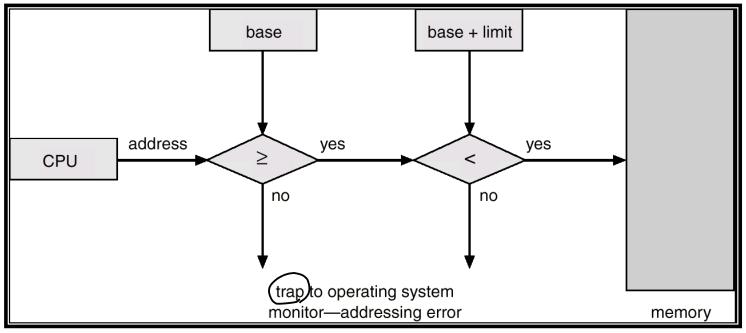
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- OS protects the system from many types of application faults
 ভুকুহ্বস্থত দুধা কল্মোল ছাইট গুলু পামা প্রাণ্ড প্রাণ প্রাণ প্রাণ্ড প্র
- ☐ Example: separation of each program's memory space
 - ছুপু 프Հ그램이 사용하는 메모리의 시작주선
 Base register holds the smallest legal memory address
 - Limit register contains the size of the range হুমুলুল শুরুদ্ধে প্রাথমণ
 - Determine the range of legal addresses
 - Memory outside the defined range is protected

Use of A Base and Limit Register



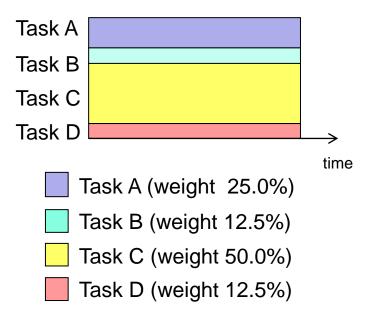
Hardware Address Protection



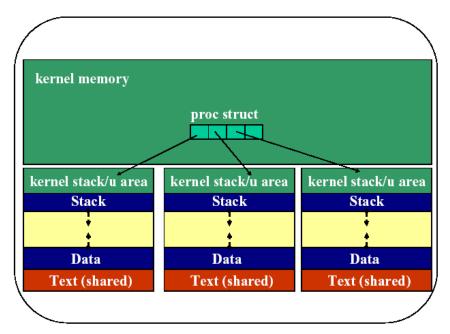
시스템이 가진 물리적 제야원 Illusion अक्षत्रमा ध्याय प्रतिष्ठि 원대체가 속있수를 쓰는것

■ Make hardware limitations go away

- As if there are infinite number of processors (time sharing)
- As if there is extremely large memory (virtual memory)



Time Shared Multitasking



Virtual Memory

Coordination and Optimization

- ☐ Make many things work together efficiently
- □ Concurrency
 - Multitasking (synchronization and scheduling)
 - Multi-user support
 - Multithreading
- ☐ Efficiency
 - Overlapped I/O and Processing
 - I/O devices and the CPU can execute concurrently
 - Effective storage management
 - Caching, paging, and swapping
 - Fast interrupt handling

Multitasking and Synchronization



