DRIVETRAIN

OVERCURRENT

LED ACTIVE (I_LED >10 mA)

VOLTAGE REFERENCES

ANALOG RAILS

DIGITAL RAILS

TEENSY 3.2

Teensy components

DGND
AGND

INPUT/OUTPUT

MAX(VCTL) = 5.0V
--- implies ---
Max(VREF) ≈ 0.75V
Max(ILED) ≈ 1.5 Amps

VI = 1A/1V

Even if multiple devices are
used, only populate J_LDAC1
to allow sychronous update.