Assignment 6.2; Rallno: 2019BTECS00022 Name: Prachi R. Chobhare. Class: SYCSE. Q. Emplore the working of Dyna-85 kit with enecution flow of sample addition program. O Dyna - 85: It is 8085 microprocessor trainer Lit, and it is based on intel 8085 high performance CPV operating at 3MHz by crystal oscillator. has 16 Kbyte system moniter as memory. Codes onel subsolutiones are accesible to/user using variable commands. It has also here keypad & display section for easy operations. It is also capable for serial ID it interface. * Addition of two 8-bit numbers: 1 Using MUT command MVI B, 03 H. ADD B MOV C,A -> Steps: MVI A, ORH: More data 02H to accumulator

MVT B, O3H; More data O3H to B register. ADD B: Add B to A & stored in A. MOV C.A: Copy contents of A to C.i.e. store result in () HLT: Stop. program. A Commands. 9000 H. MOV B, A. LDA 2001 H STA 2002 H => Steps: LDA 2000H: load the content of 2000H memory location to accumulator, i.e. A set 2000H MOV B, A: More contents of accumulator to B LDA 2001H: load content of 2001H memory location to accumulator i.e. A set 2001H ADD B: Add A & B & stored in A.

8TA 2002H: Copy contents of accumulator to 2002H memory location. HLT: 8top program. 3. Using LXI command LXI H, 3000H. mov m, A Steps: LXI H, 8000H: Get the address of first number in HL pair. Now, HL pair points to 3000H.

MOV A, M: Get first operand to the accumulator. INX H : Increment content of Allpair. Now, HL pair points to 3001 H. Mox B, M: Gret second operand to the B register. ADD B: Add B to A & Stored in A.

