



COMPUTER ORGANIZATION AND SOFTWARE SYSTEMS

WEBINAR 3 — CPU SCHEDULING ALGORITHMS

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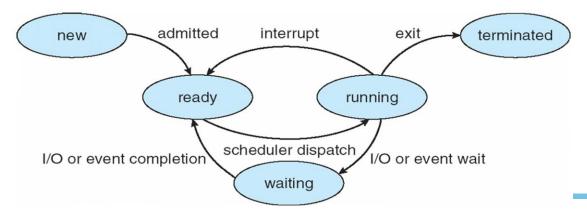
Vivekananda M R

CPU Scheduling Algorithms

- 1. FCFS (First Come First Serve)
- 2. SJF (Shortest Job First)
 - 1. Preemptive / SRTF (Shortest Remaining Time First)
 - 2. Non-Preemptive
- 3. Priority scheduling
 - 1. Preemptive
 - 2. Non-Preemptive
- 4. Round Robin (RR)

Process States

- The state of a process is defined in part by the current activity of that process.
- New: The process is being created.
- Running: Instructions are being executed.
- Waiting: The process is waiting for some event to occur (such as an I/O completion or reception of a signal).
- Ready: The process is waiting to be assigned to a processor.
- Terminated: The process has finished execution.





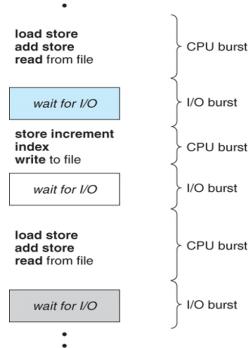


- Non-Preemptive Scheduling:
- Once the resources (CPU cycles) is allocated to a process, the process holds the CPU till it gets terminated.
- Preemptive Scheduling:
- The resources (mainly CPU cycles) are allocated to the process for the limited amount of time and then is taken away, and the process is again placed back in the ready queue if that process still has CPU burst time remaining.

Key terminologies



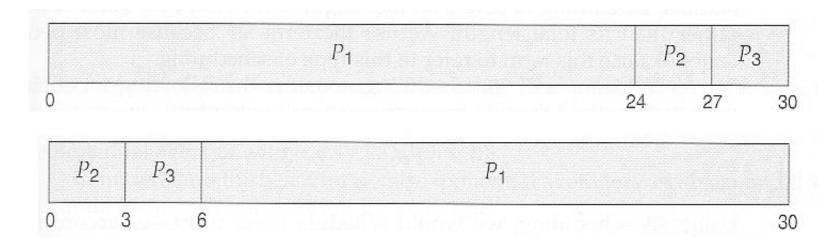
- CPU burst is length of time process needs to use CPU before it next makes a system call (normally request for I/O).
- I/O burst is the length of time process spends waiting for I/O to complete.



Key terminologies



- GANTT chart:
- Generalized Activity Normalization Timetable (GANTT).
- Type of chart that show the amount of work done or production completed in given period of time.



Key terminologies



- Different time with respect to a process.
- Arrival Time (AT):
 - Time at which the process arrives in the ready queue.
- Completion Time (CT):
 - Time at which process completes its execution.
- Burst Time (BT):
 - Time required by a process for CPU execution.
- Turn Around Time (TAT):
 - Time Difference between completion time and arrival time.
 - Turn Around Time = Completion Time Arrival Time
- Waiting Time(WT):
 - Time Difference between turn around time and burst time.
 - Waiting Time = Turn Around Time Burst Time
- Response Time(WT):
 - RT = Start Time Arrival Time



Problem1: FCFS

Consider a System with four processes P1,P2,P3 and P4 whose arrival time and CPU-I/O bursts are as given in the table. Find average Turn Around Time, Waiting Time and Response Time.

Process	AT		ВТ		FT	TAT	WT	RT
		CPU	1/0	CPU				
P1	0	6	3	2				
P2	2	5	1	1				
P3	3	2	1	3				
P4	5	1	1	1				

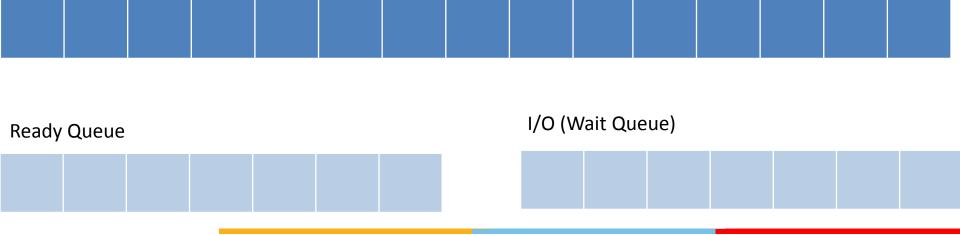
AT – Arrival Time
TAT – Turn Around Time

BT – Bust Time WT – Wait Time FT – Finish Time RT – Response Time

Problem-1: FCFS



Process	AT	ВТ			FT	TAT	WT	RT
		CPU	I/O	CPU		(FT-AT)	(TAT-BT)	
P1	0	6	3	2				
P2	2	5	1	1				
Р3	3	2	1	3				
P4	5	1	1	1				



Problem-2: FCFS



Process	AT		ВТ		FT	TAT	WT	RT
		CPU	1/0	CPU		(FT-AT)	(TAT-BT)	
P1	0	6	10	4				
P2	0	9	15	6				
Р3	0	3	5	2				





Ready Queue

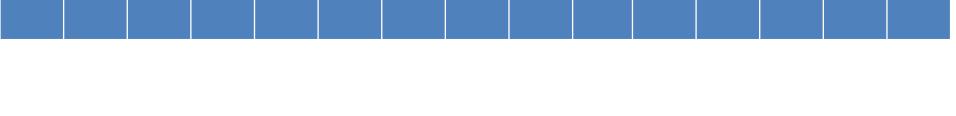
I/O (Wait Queue)

Problem-3: SJF (Non-Preemptive)



Process	AT		ВТ		FT	TAT	WT	RT
		CPU	I/O	CPU		(FT-AT)	(TAT-BT)	
P1	0	6	10	4				
P2	0	9	15	6				
Р3	0	3	5	2				





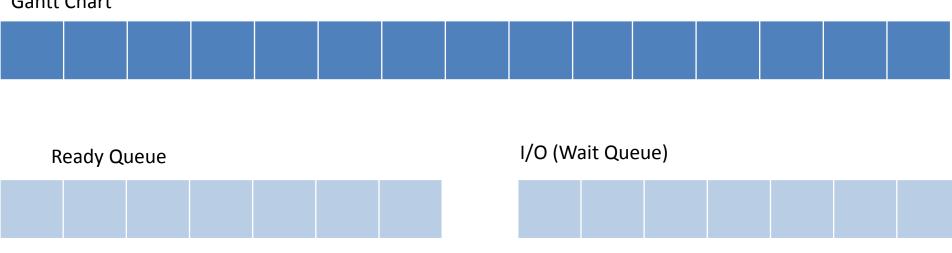
Ready Queue

1/0

Problem-4: SJF (Preemptive) /SRTF



Process	AT		ВТ		FT	TAT	WT	RT
		CPU	I/O	CPU		(FT-AT)	(TAT-BT)	
P1	0	3	2	2				
P2	0	2	4	1				
Р3	2	1	3	2				
P4	5	2	2	1				

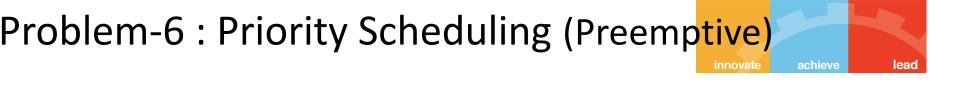


Problem-5: Priority Scheduling(Preemptive)

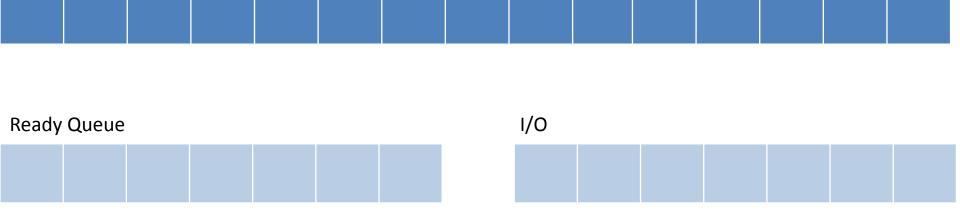
Process	Priority	AT		ВТ		FT	TAT	WT	RT
			CPU	1/0	CPU				
P1	2	0	1	5	3				
P2	3	2	3	3	1				
Р3	1[H]	3	2	3	1				
P4	4[L]	3	2	4	1				







Process	Priority	AT		ВТ		FT TAT			RT
			CPU	I/O	CPU		(FT-AT)	(TAT-BT)	
P1	2	0	1	5	3				
P2	3 [L]	2	3	3	1				
P3	1 [H]	3	2	3	1				



Problem-7: Round Robin Quantum=

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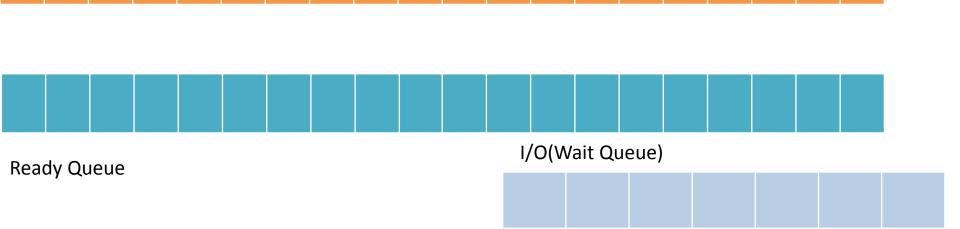
Process	AT		ВТ		FT	TAT	WT	RT
		CPU	I/O	CPU		(FT-AT)	(TAT-BT)	
P1	0	6	10	4				
P2	2	9	15	6				
Р3	4	3	5	2				



Problem-8: Round Robin Quantum=3

2		
vate	achieve	lead

Process	AT		ВТ		FT	TAT	WT	RT
		CPU	I/O	CPU		(FT-AT)	(TAT-BT)	
P1	0	4	2	6				
P2	2	6	2	6				
Р3	4	8	3	5				
P4	8	7	2	4				

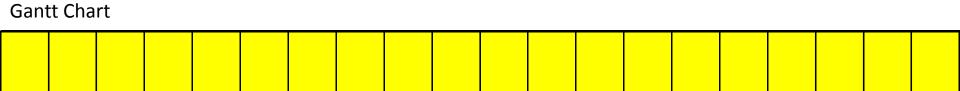


Problem-9: SJF (Non-Preemptive) - HW



lead

Process	AT		ВТ		FT			RT
		CPU	I/O	CPU		(FT-AT)	(TAT-BT)	
P1	0	3	4	3				
P2	2	4	2	4				
P3	4	5	1	4				
P4	6	2	2	4				



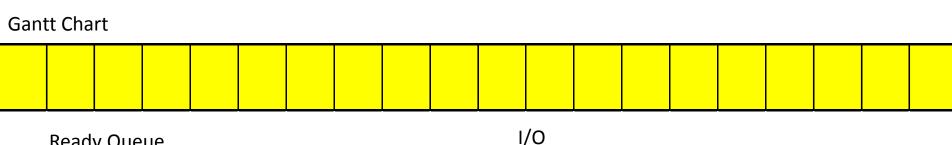
Ready Queue

1/0

Problem-10: FCFS (HW)



Process	AT	ВТ			FT	TAT	WT	RT
		CPU	1/0	CPU		(FT-AT)	(TAT-BT)	
P1	0	3	2	4				
P2	2	5	2	3				
Р3	4	3	3	1				
P4	8	4	2	2				



Questions?





Thank you.

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