University of Batangas
LIPA CITY
COLLEGE OF ENGINEERING & ARCHITECTURE

UNIVERSITY OF BATANGAS – LIPA CAMPUS

COLLEGE OF ENGINEERING AND ARCHITECTURE

COMPUTER ENGINEERING

Laboratory #5

HDL

NAME: Dimple G. Valencia

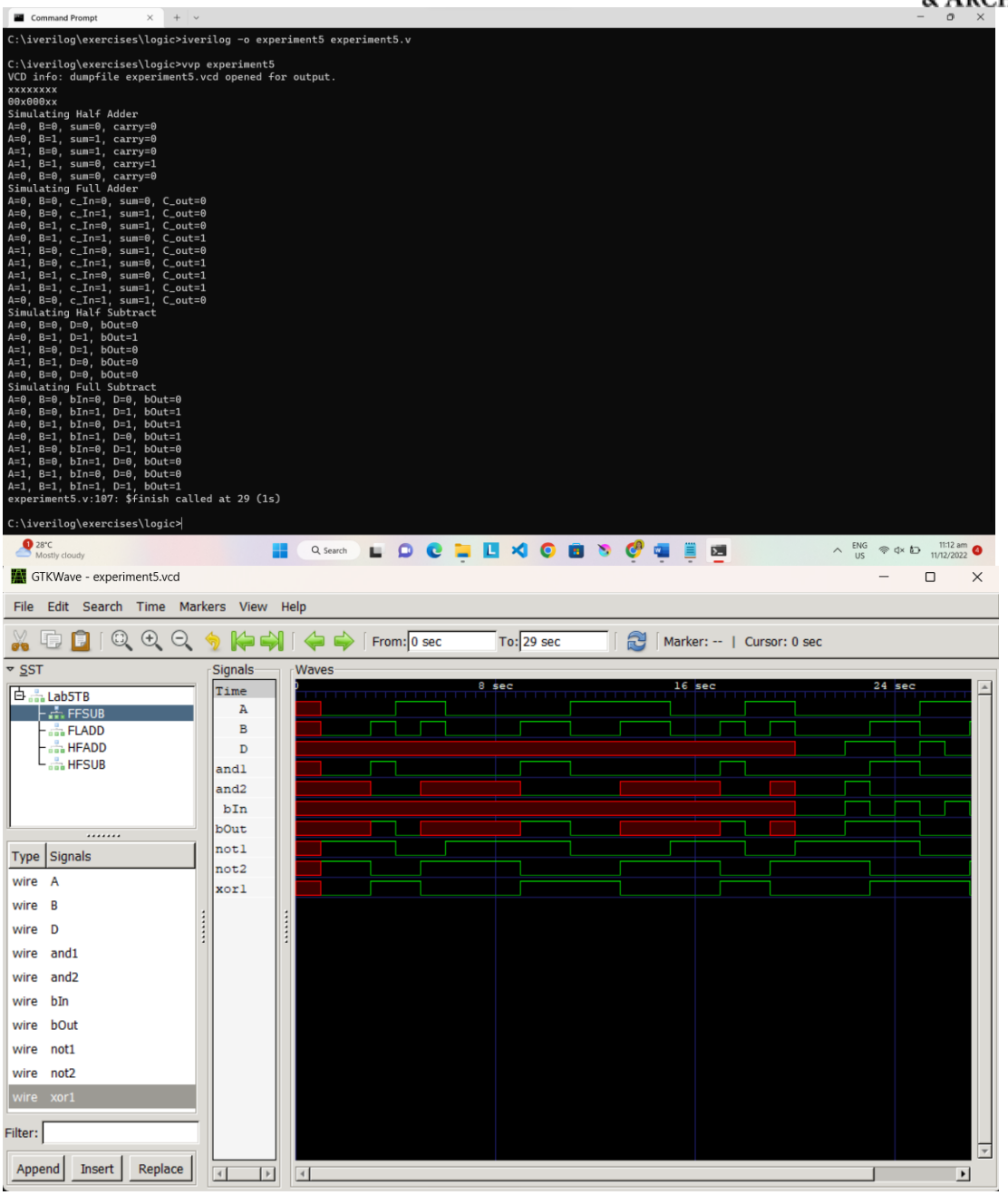
STUDENT NUMBER: 2021083

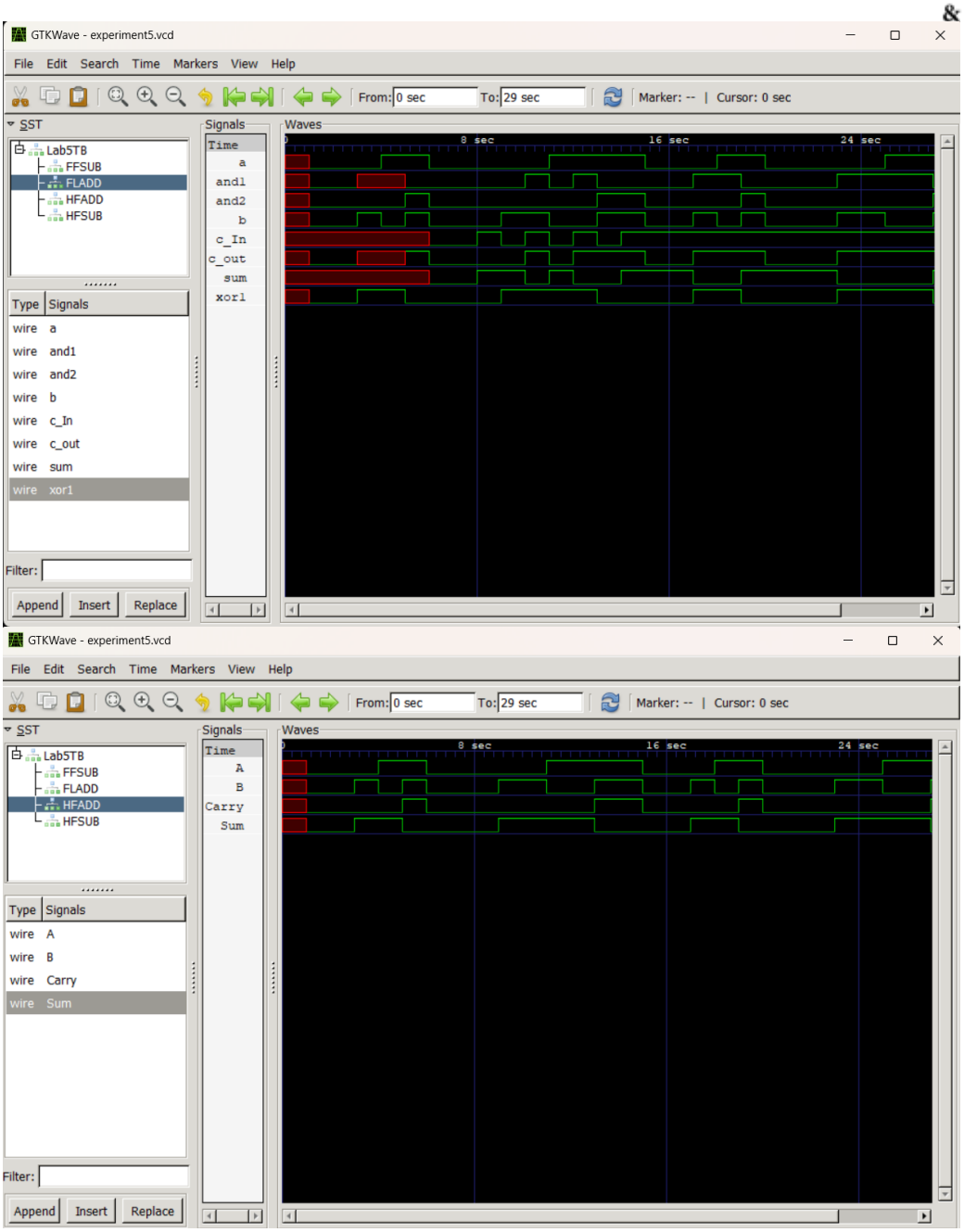
DATE OF SUBMISSION: 12/20/2022

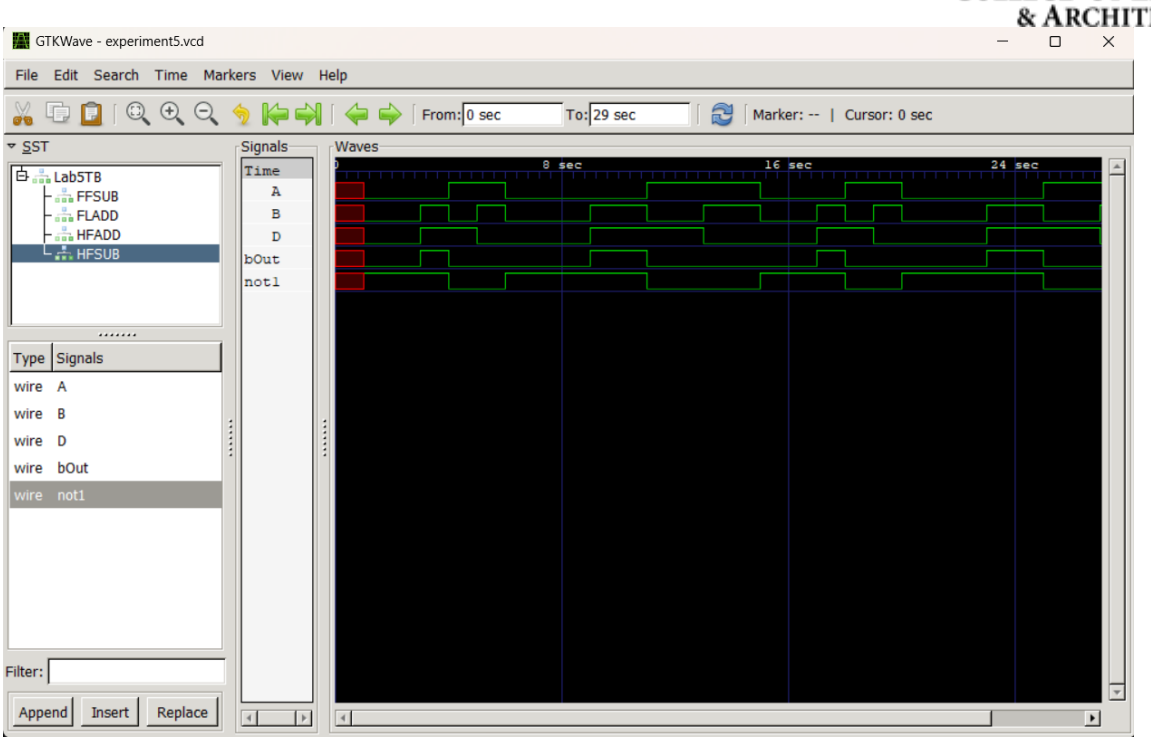
ENGR. CHARLES RAY JUANILLAS

PROFESSOR

Result and Discussion:




GTKWave - experiment5.vcd
File Edit Search Time Markers View Help
From: 0 sec
To: 29 sec
Marker: -- | Cursor: 0 sec
SST
Lab5TB
FFSUB
FLADD
HFADD
HFSUB
Signals
Time
a
and1
and2
b
c_In
c_out
sum
xor1
Waves
8 sec
16 sec
24 sec
Type Signals
wire a
wire and1
wire and2
wire b
wire c_In
wire c_out
wire sum
wire xor1
Filter:
Append
Insert
Replace
GTKWave - experiment5.vcd
File Edit Search Time Markers View Help
From: 0 sec
To: 29 sec
Marker: -- | Cursor: 0 sec
SST
Lab5TB
FFSUB
FLADD
HFADD
HFSUB
Signals
Time
A
B
Carry
Sum
Waves
8 sec
16 sec
24 sec
Type Signals
wire A
wire B
wire Carry
wire Sum
Filter:
Append
Insert
Replace


GTKWave - experiment5.vcd
File Edit Search Time Markers View Help
From: 0 sec
To: 29 sec
Marker: -- | Cursor: 0 sec
SST
Lab5TB
FFSUB
FLADD
HFADD
HFSUB
Type Signals
wire A
wire B
wire D
wire bOut
wire not1
Filter:
Append Insert Replace
Signals
Time
A
B
D
bOut
not1
Waves
0 sec
16 sec
24 sec