MASEEDU NITIN

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EDUCATION INSTITUTION PLACE

B. V. Raju Institute of Technology JNTUH Narsapur, TS
B.Tech in Electronics & Communication Engineering Expected

Graduation: June 2024CGPA (Until 3-1): 8.12

Sri Chaitanya Junior Kalasala TSBIE Hyderabad, TS

Intermediate - TSBIE Graduation: May 2020

Marks: 912/1000

M.B Grammar High School SSC Hyderabad, TS

High School - SSC Graduation: May 2018

Marks: 9.0/10

ABOUT

• Organised and dependable candidate successful at managing multiple priorities with a positive attitude. Willingness to take on added responsibilities to meet team goals. Hardworking and passionate job seeker with strong determination. I am not that smart but I do work hard. I'm a fast pace learner and a good listener too. I can tackle obstacles with positive attitude. I'm an enthusiast and a believer.

PROJECTS

• I've done a project on "Implementation of single chamber pacemaker with minimum delay using verilog." in B.Tech 2nd Year-2nd Semester.

Nowadays, the technological advancements surge of low power and high-speed devices and it is very Essential in devices such as a cardiac pacemaker to increase the lifespan. According to a survey one Person dies every 34 seconds from cardiovascular disease. Heart disease cost about \$220 billion each Year. The annual number of deaths from cardiovascular disease in India is projected to rise from 2.26Million (1990) to 4.47 million (2020). All these problems are due to Arrhythmia. It can be cured by placing Pacemaker connected to the heart. Pacemaker records the live status of the heart and if there occurs any Changes in the rate of pulse it acts more accurately than the sinu atrial pulse. In this project we worked on the minimization of the delay between the sensing and pacing.

• In this project my role was designing the state diagram and building a code based on it.

TECHNOLOGIES

- Languages (Intermediate) Python, HTML, Verilog, MySQL
- Tools Solidworks, DSCH Tool, Microwind, MatLab, PyCharm, Xilinx ISE, Arduino IDE
- Tools(Beginner) Solidworks, Blender

CERTIFICATIONS

•	Maven Silicon VLSI System On Chip Design.	11 th January,2023
•	Basics of Python – Infosys Springboard	24th August,2022
•	Time management – Infosys Springboard	09 th August,2022
•	Attended Workshop on "Industrial exposure on emerging trends in vlsi design.	19 th – 24 th Sep,2022
•	AWS Cloud Foundations - AWS Academy	13 th April,2022
•	IoT Workshop.	30 th March,2022

Hero Campus Challenge Season 8

31st January, 2023

Excellence Certification in IoT 03rd April – 04th April,2023

Maruti Suzuki XCELerate
 O6th April, 2023

EXTRA CURRICULARS

- Joint Secretary of Cultural Club, BVRIT.
- Organised a non-technical event in Promethean 2K22 BVRIT.
- Member of NSS Group.
- School People Leader for 4 Years.

HOBBIES

- Playing Guitar
- Dancing
- Singing
- Art
- Video Gaming
- Cooking

DECLARATION

• All the information mentioned above in the resume is correct to the best of my knowledge and belief.

Hyderabad Maseedu Nitin