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| Name: Muhammad Boota | EE-272L Digital Systems Design |
| Reg. No.: 2022-EE-134 | Marks Obtained: \_\_\_\_\_\_\_\_\_\_\_\_ |

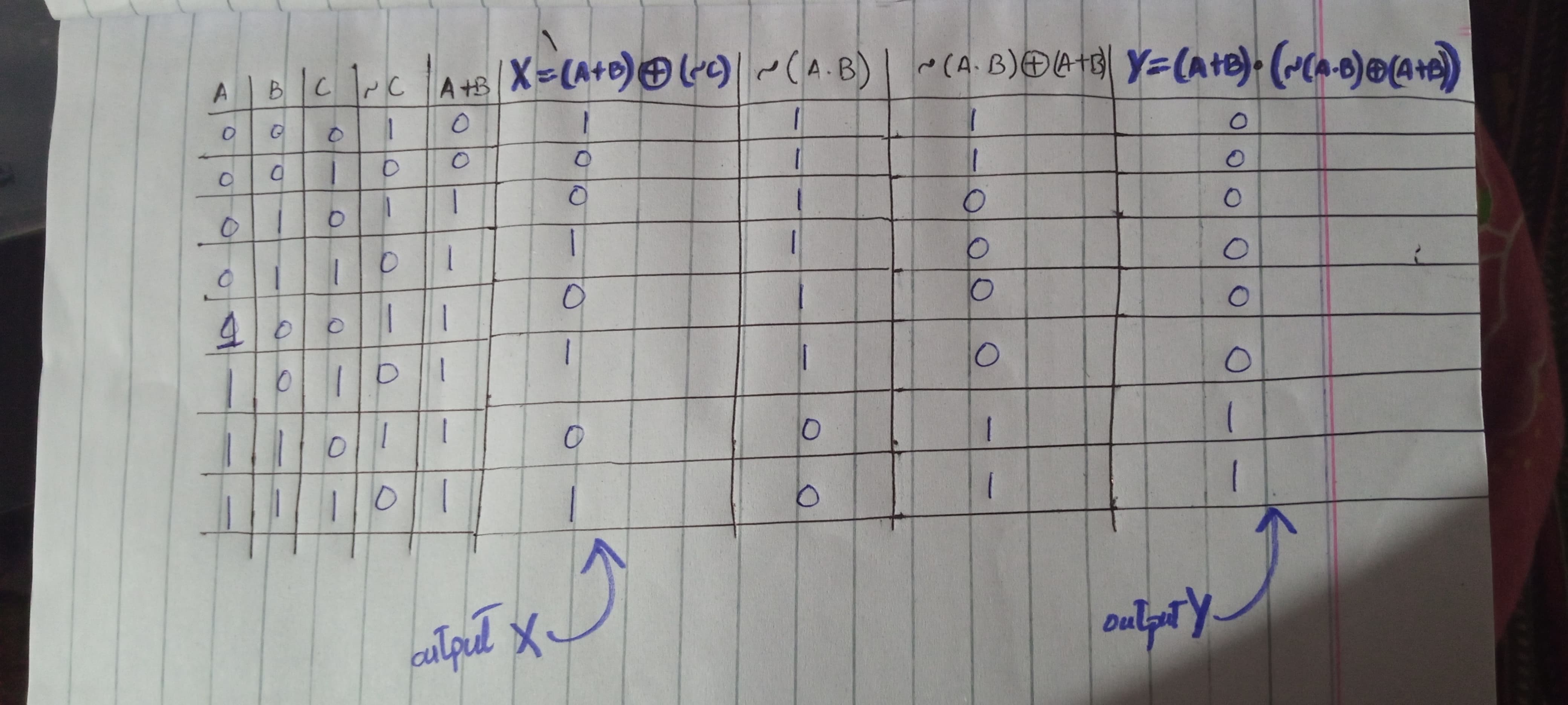
**Lab Manual**

**Lab 3**

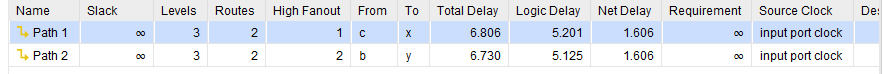
**Combinational Circuits: Structural Modeling Simulation**

**Question No.1**

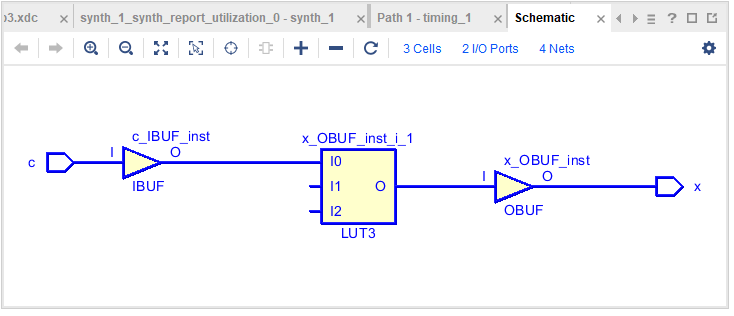
**(a) Truthtable of the circuit**



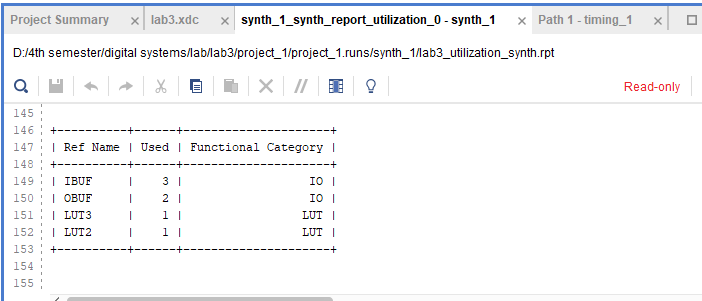
**(b)** **Maximum combinational delay:**

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**From Figure path (from c to x) has the maximum combinational delay**

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**(c) Resource Utilization:**

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1. **No. of LUTs=2**
2. **No. of IOs=5**