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| --- | --- |
| Name: Muhammad Boota | EE-272L Digital Systems Design |
| Reg. No.: 2022-EE-134 | Marks Obtained: \_\_\_\_\_\_\_\_\_\_\_\_ |

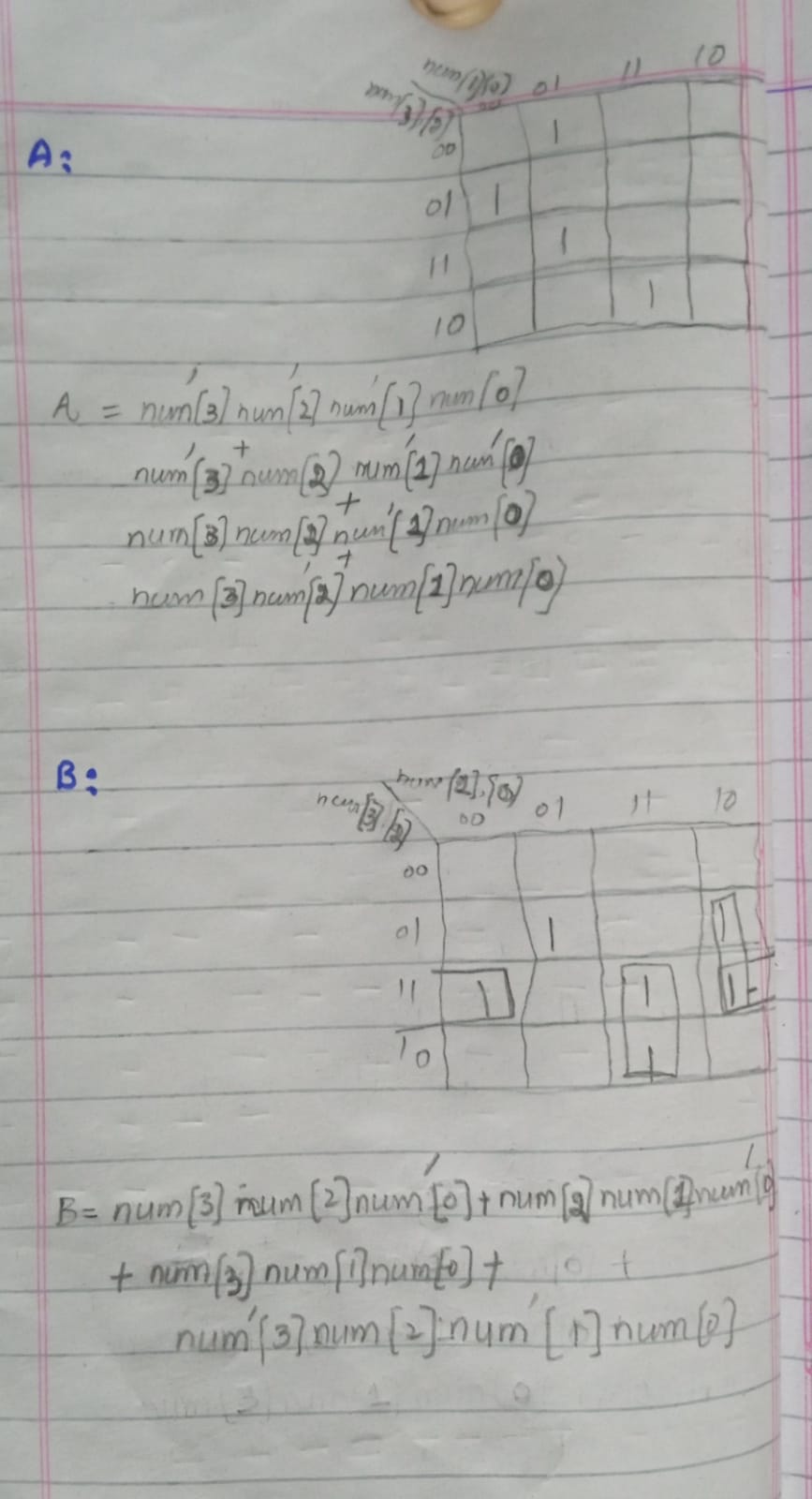
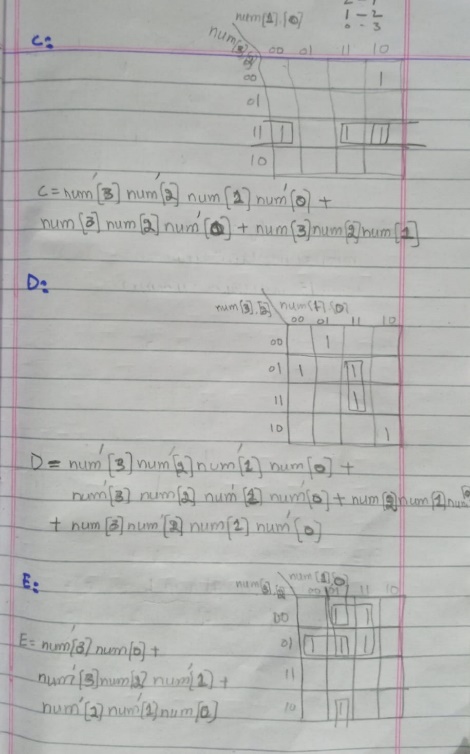
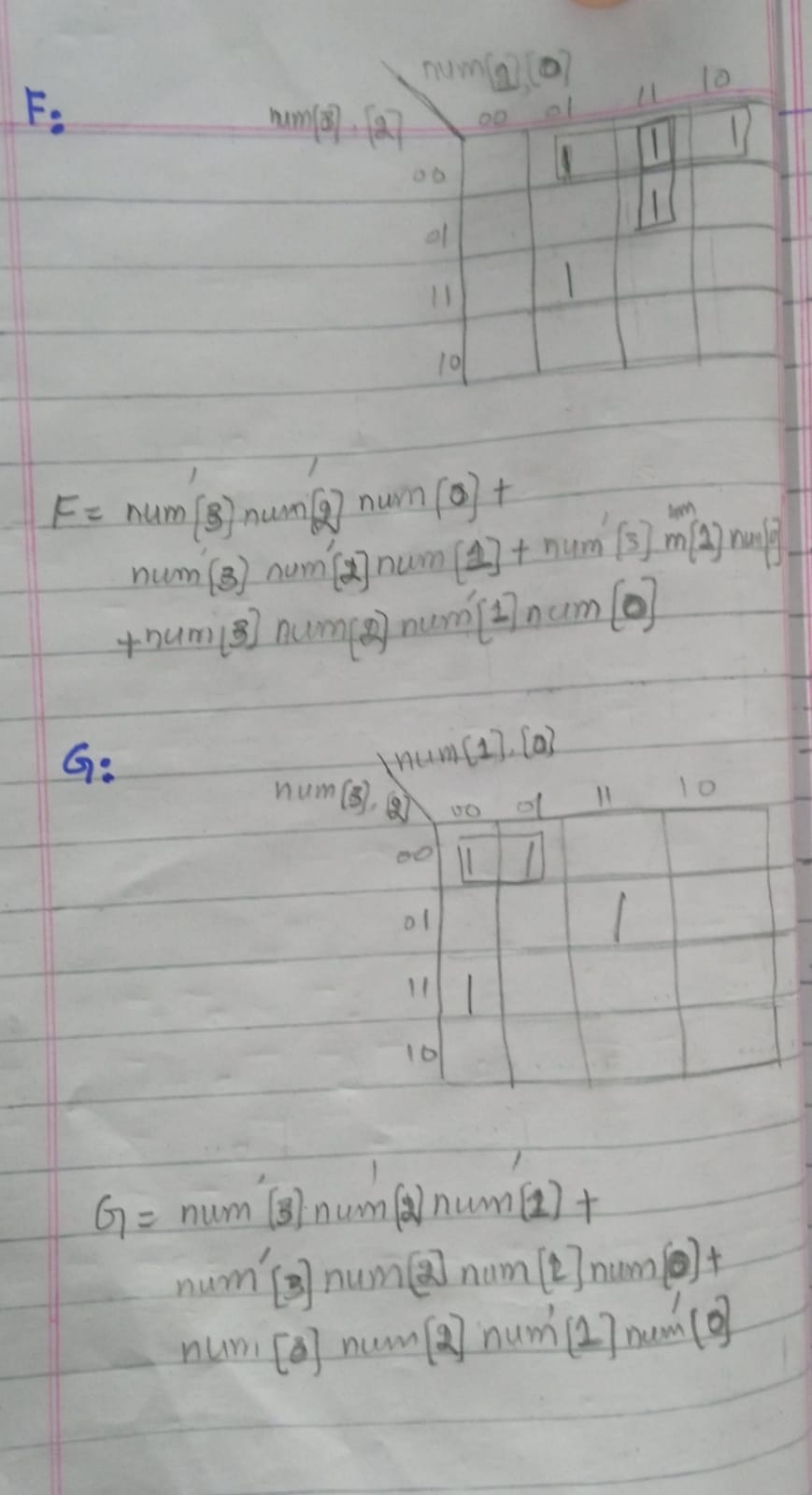
**Lab Manual**

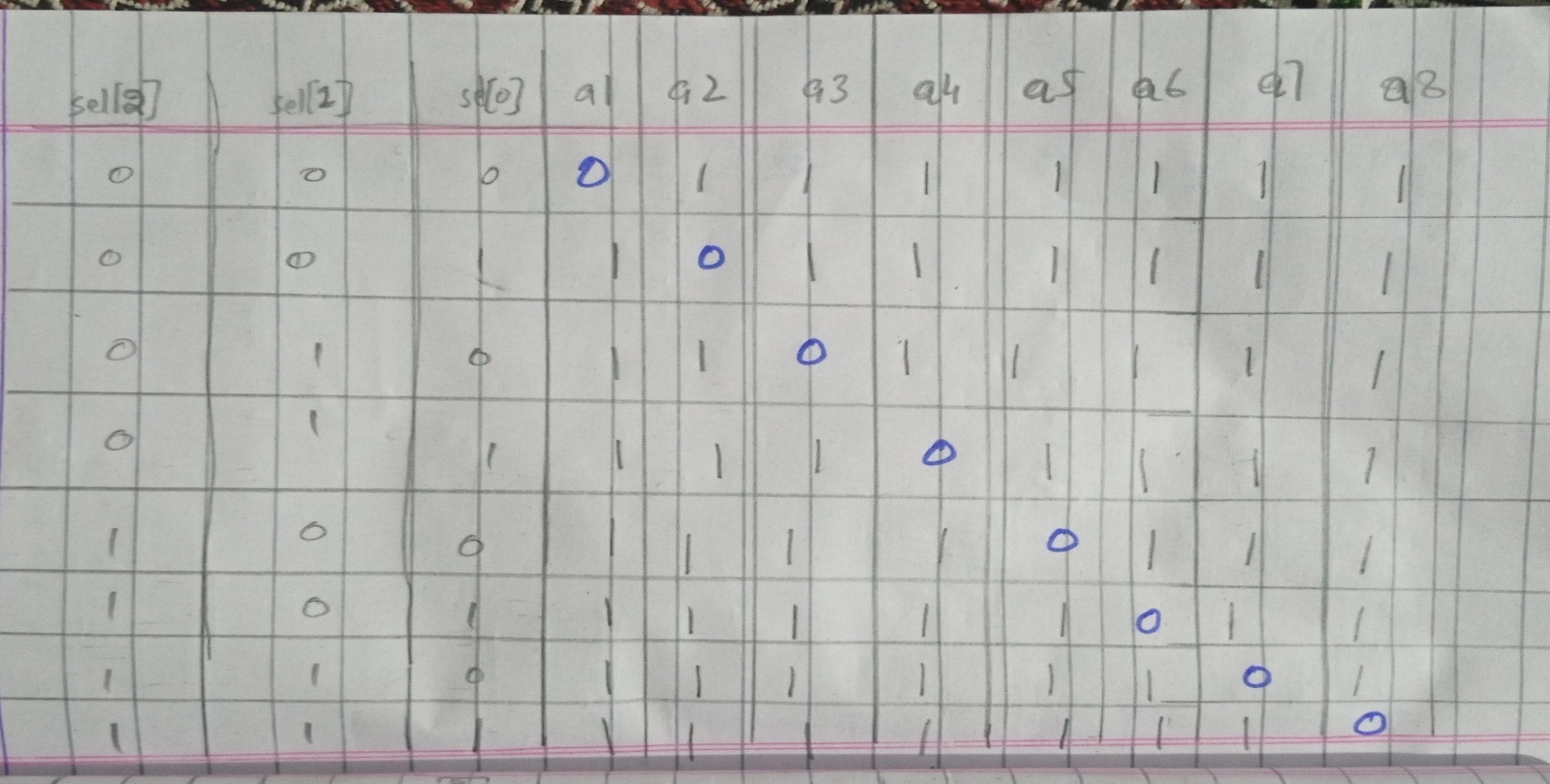
**Lab 5**

1. **Truthtable of the circuit**

A sheet of paper with writing on it

Description automatically generated**(i)**

**b)K-maps used to minimize the logic**

**(ii)**

**b) K-maps used to minimize the logic**

A notebook with writing on it

Description automatically generated