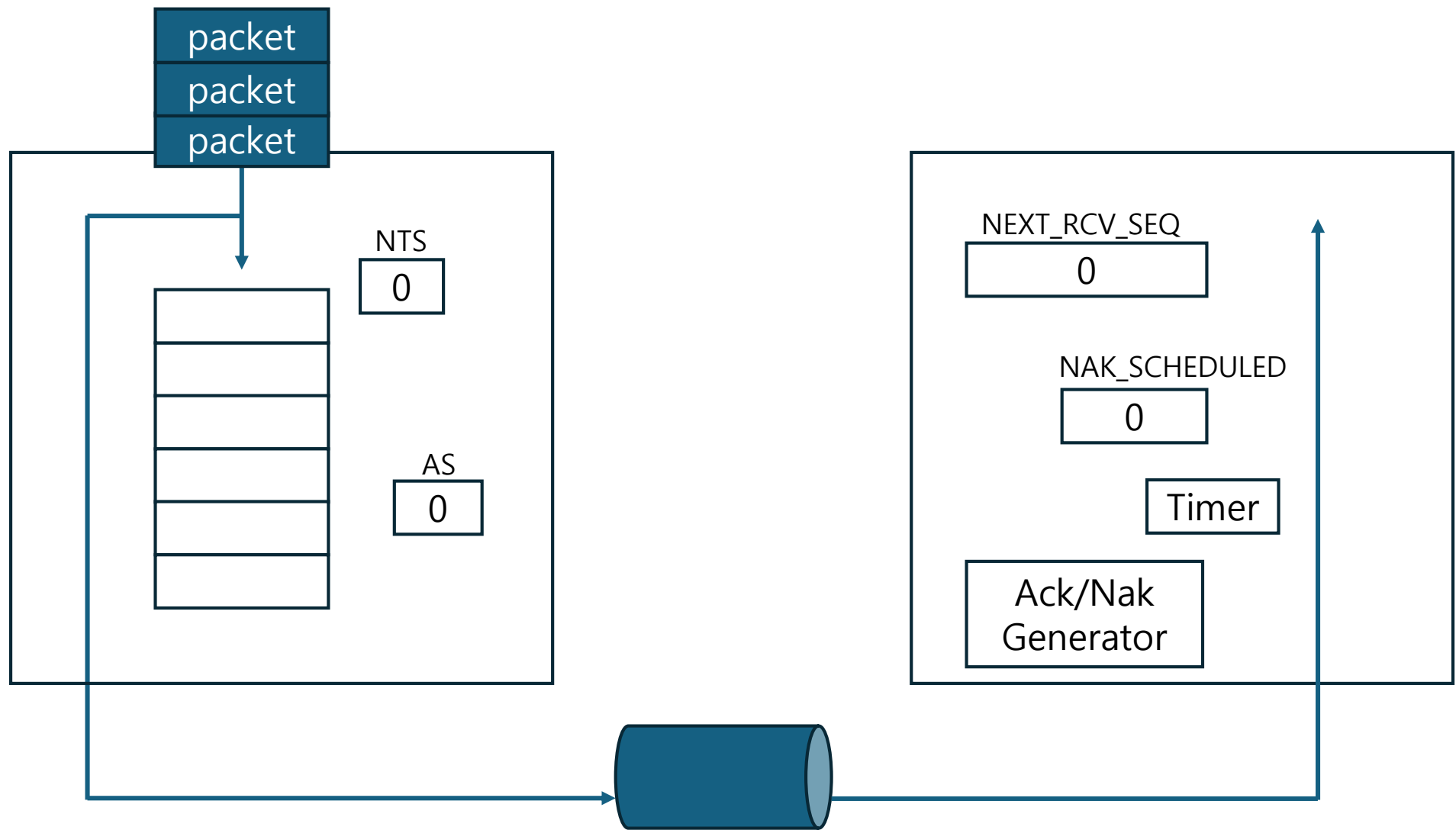
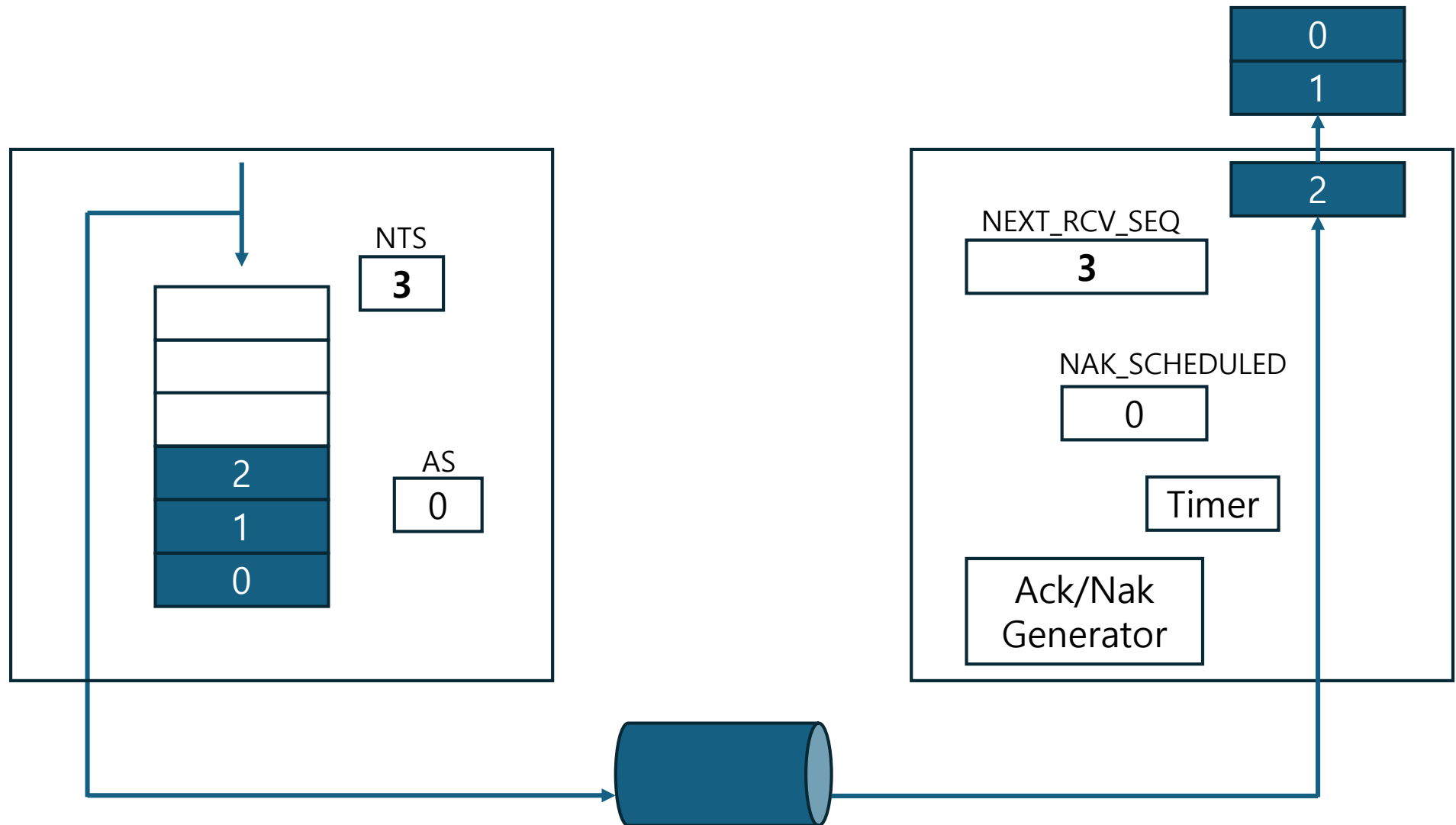
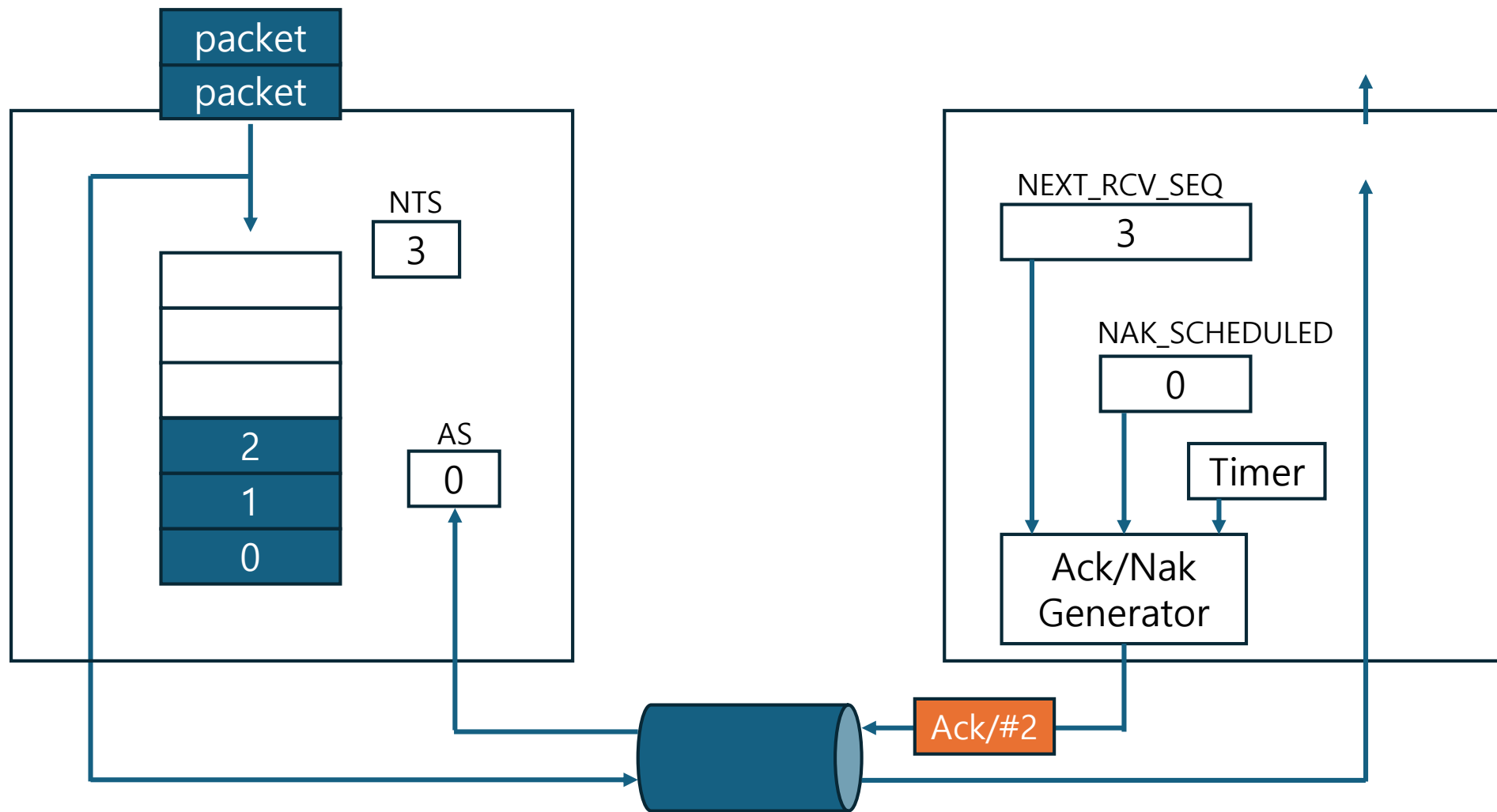
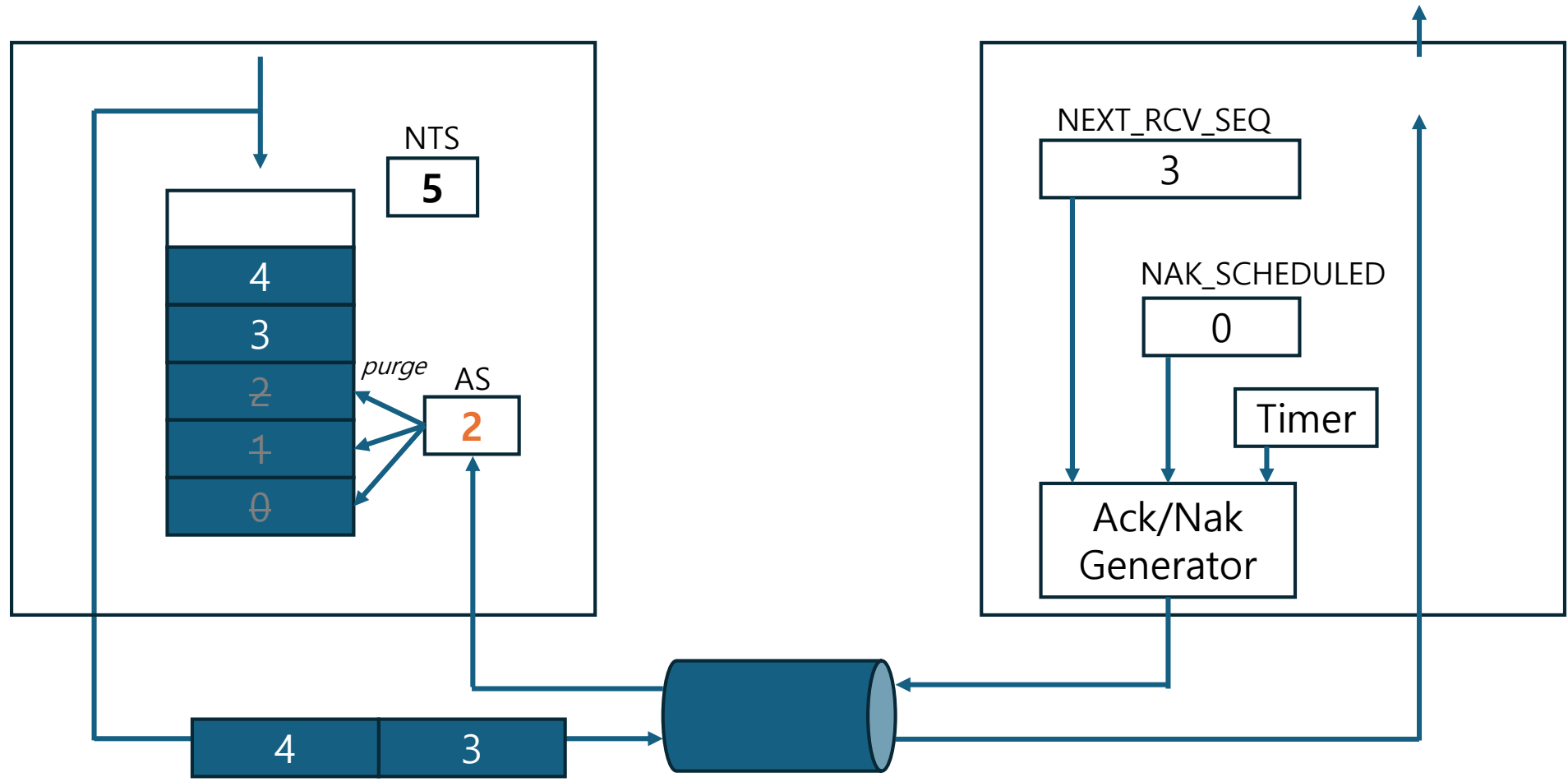


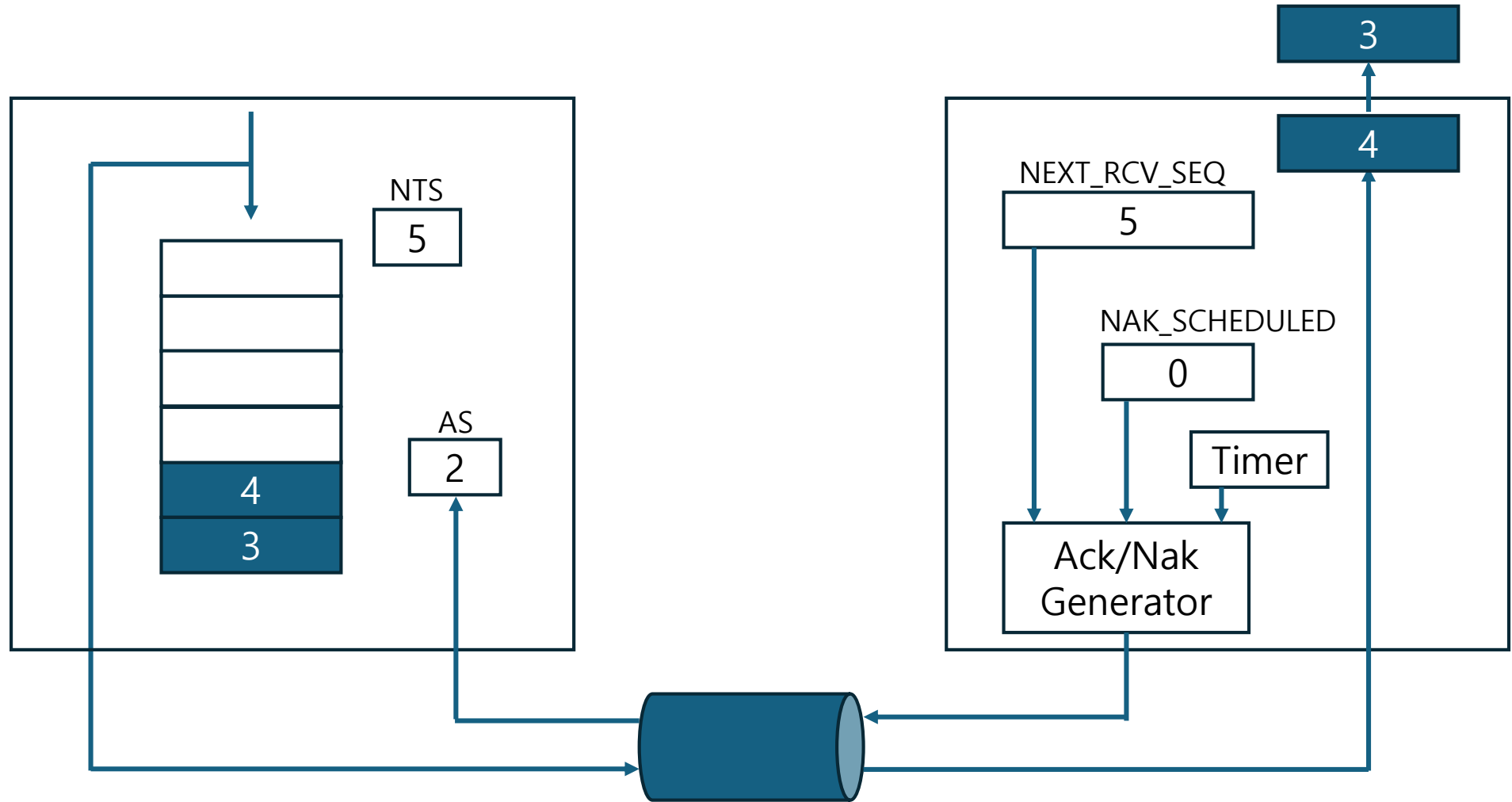
- 아래 그림은 이해를 위한 Figure
- 실제 HW 동작상의 Timing Flow는 다를 수 있음을 유의



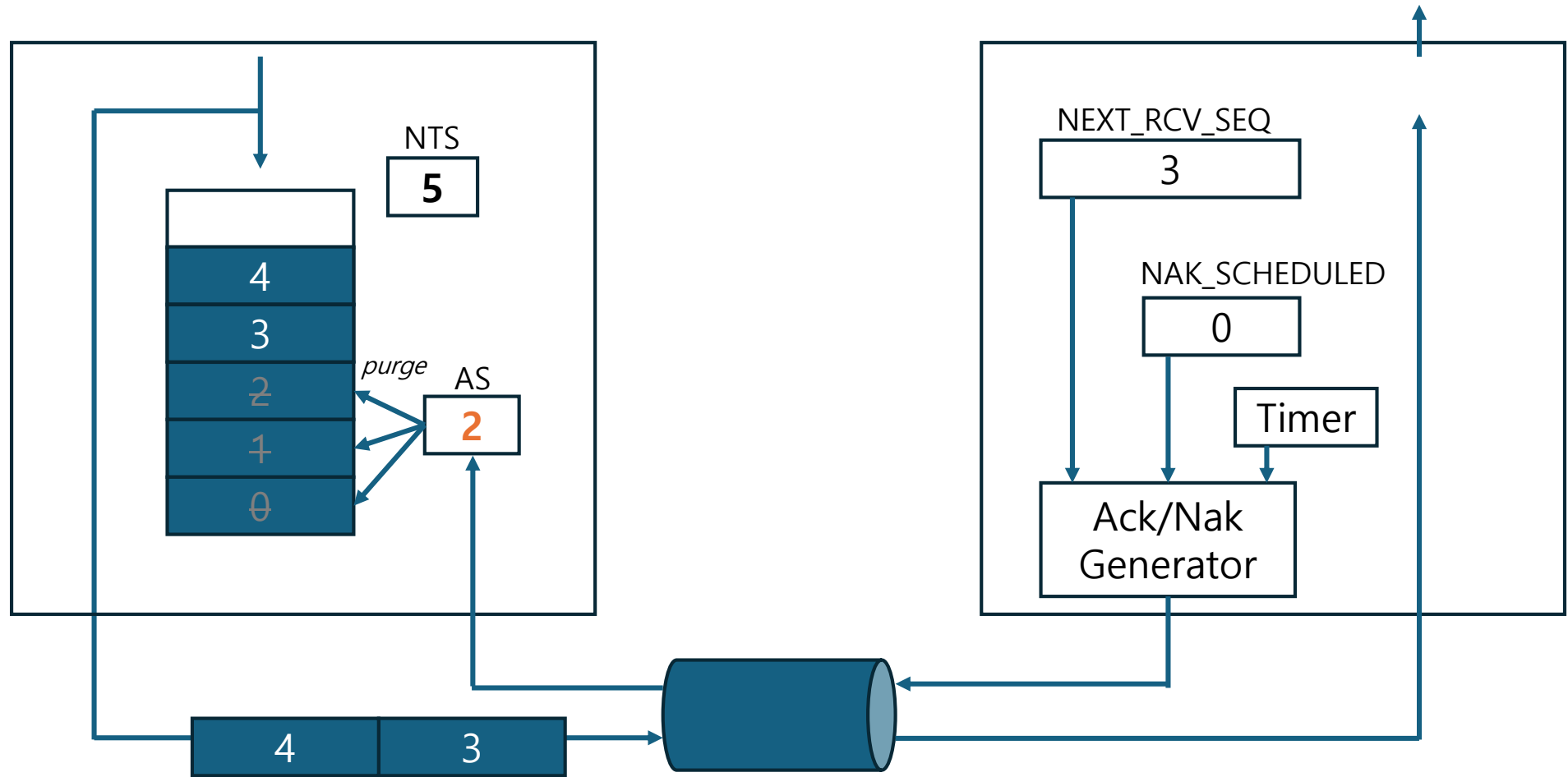


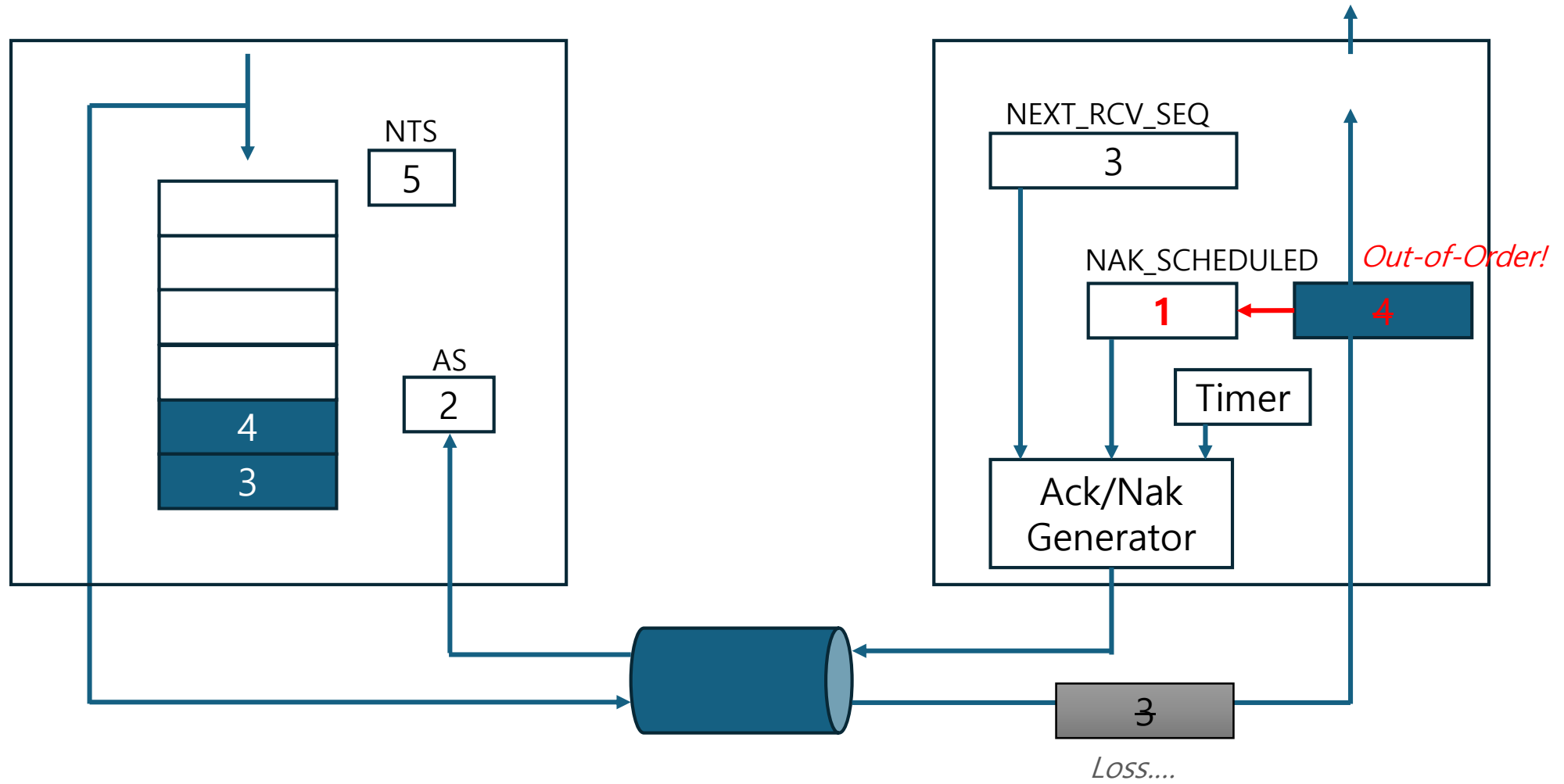




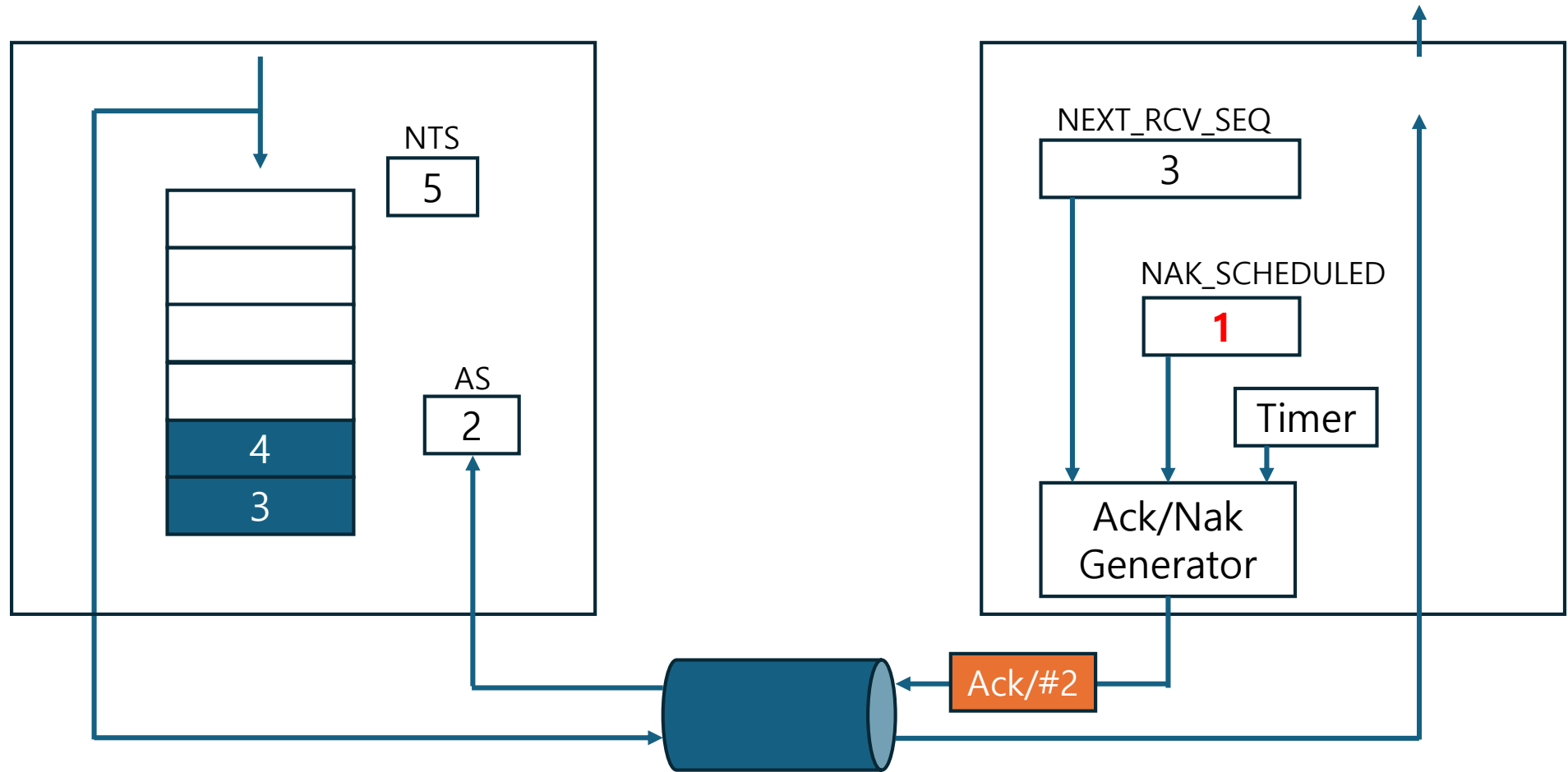


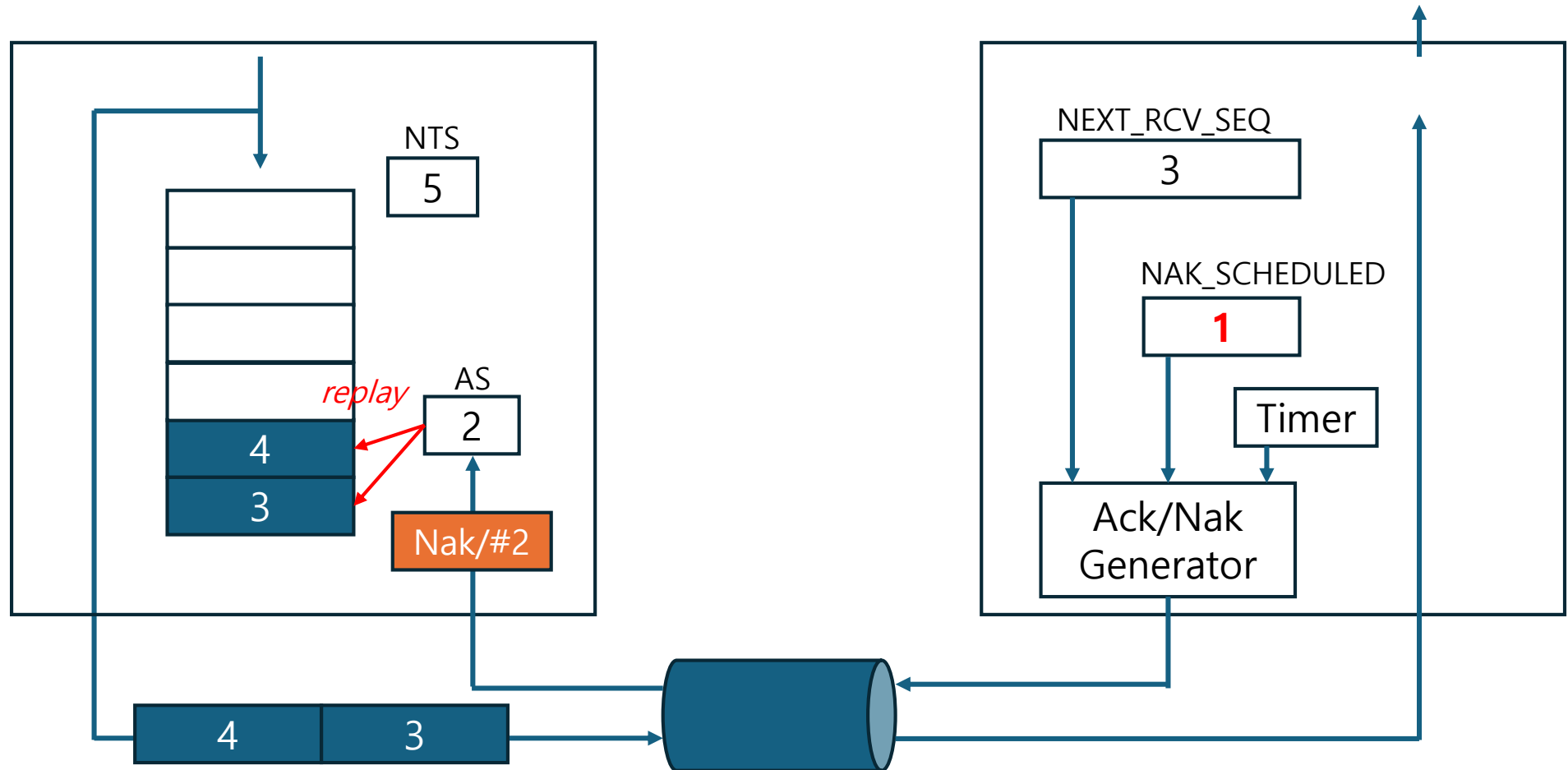
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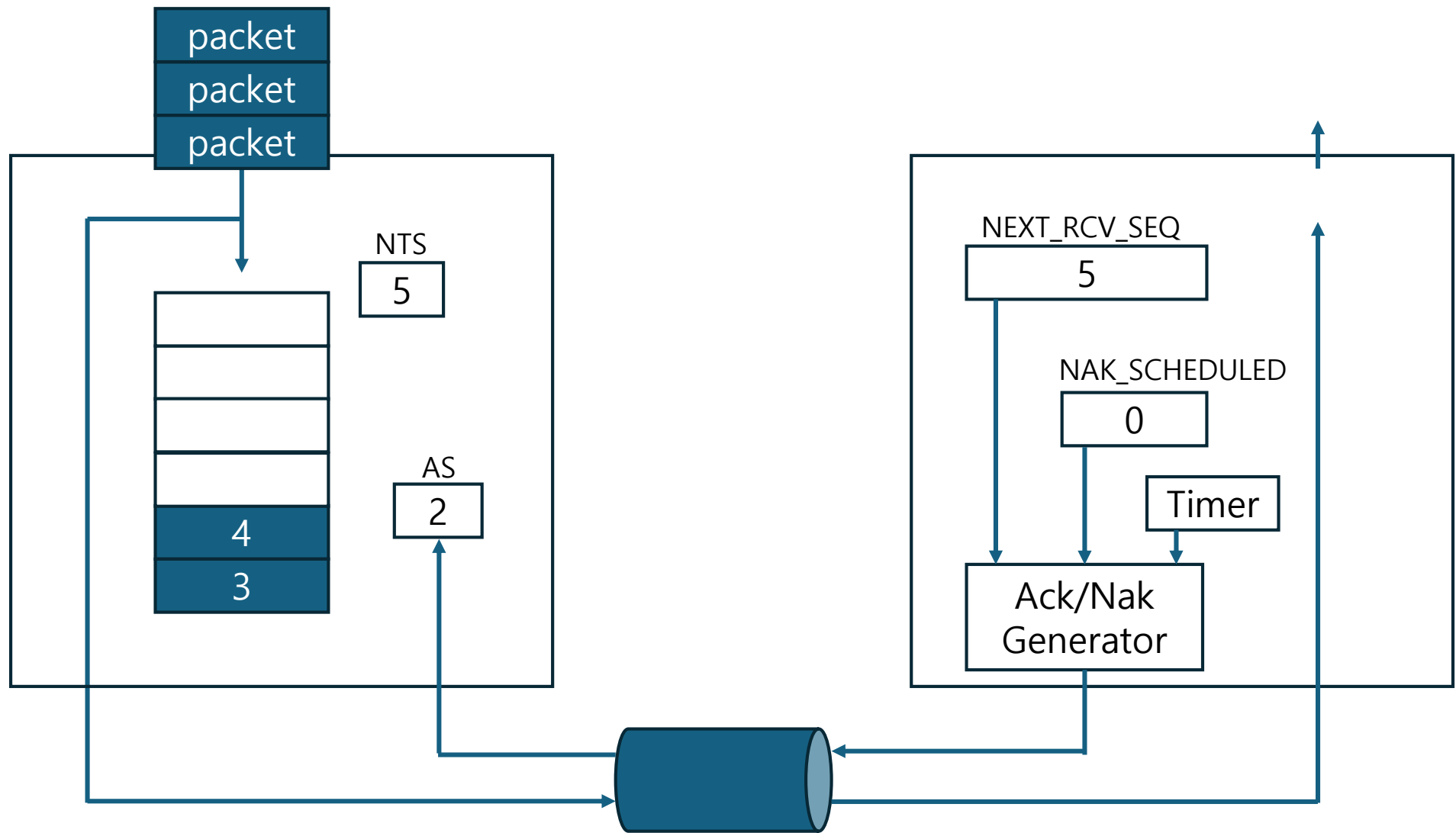


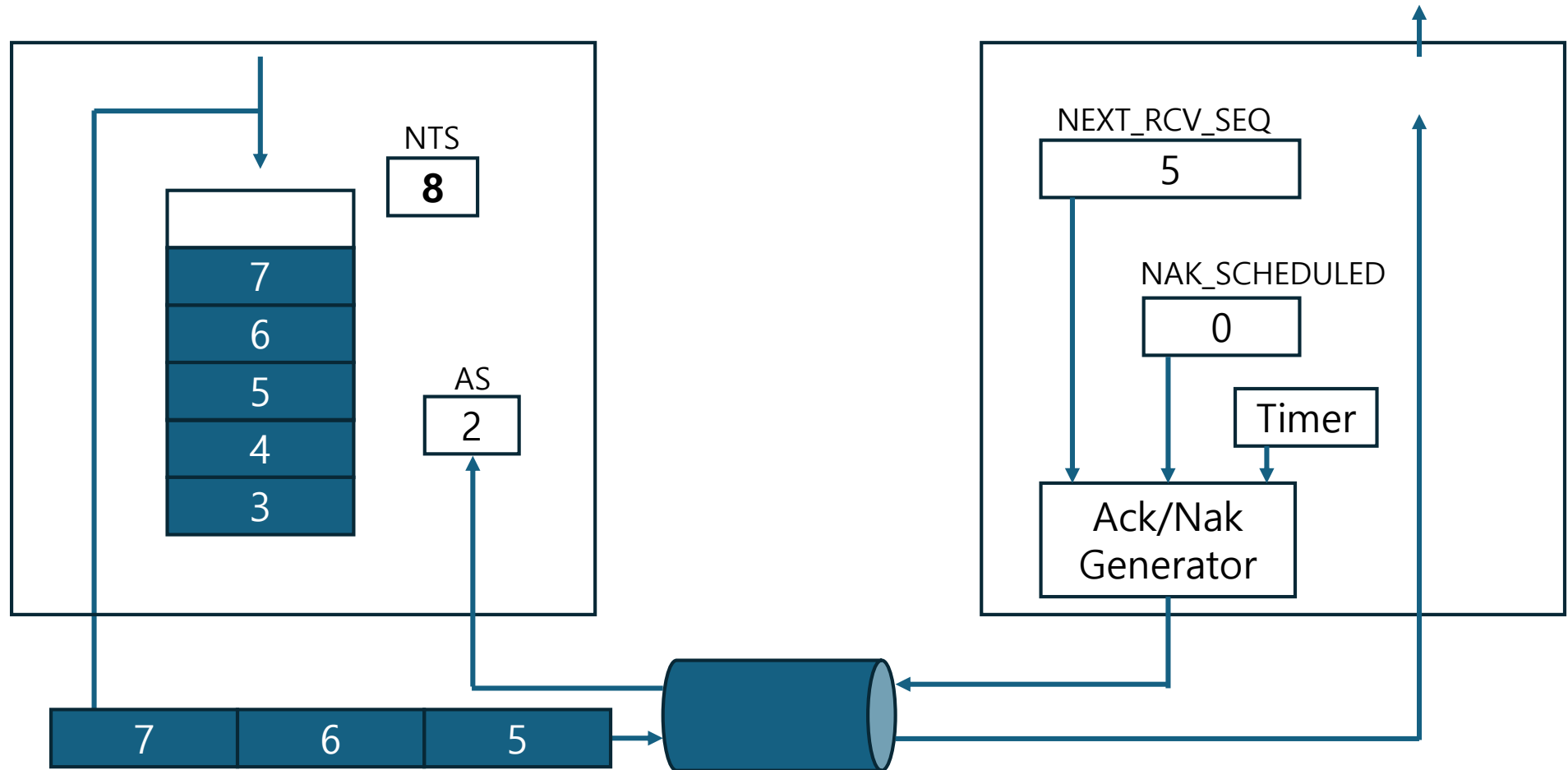


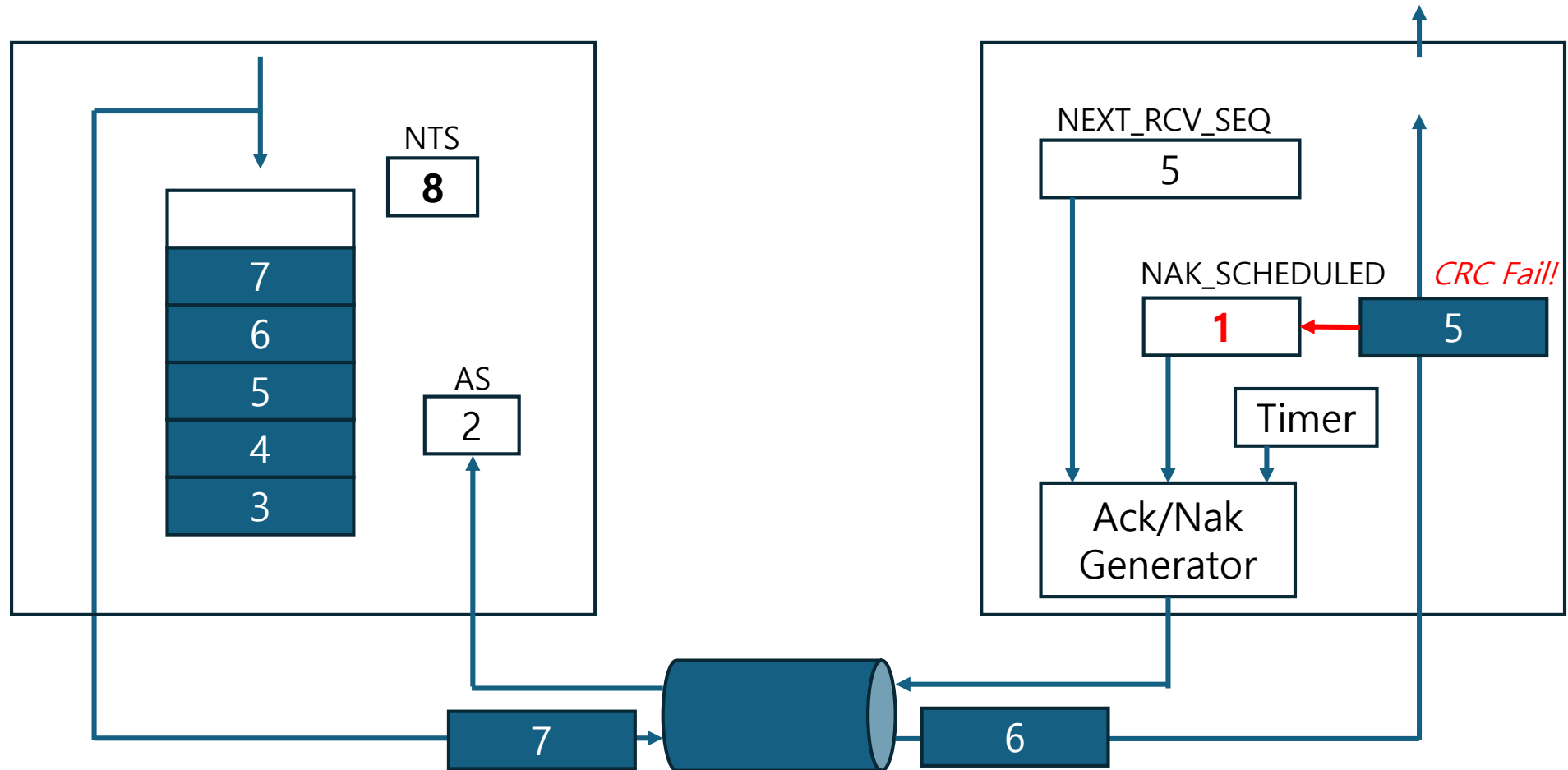


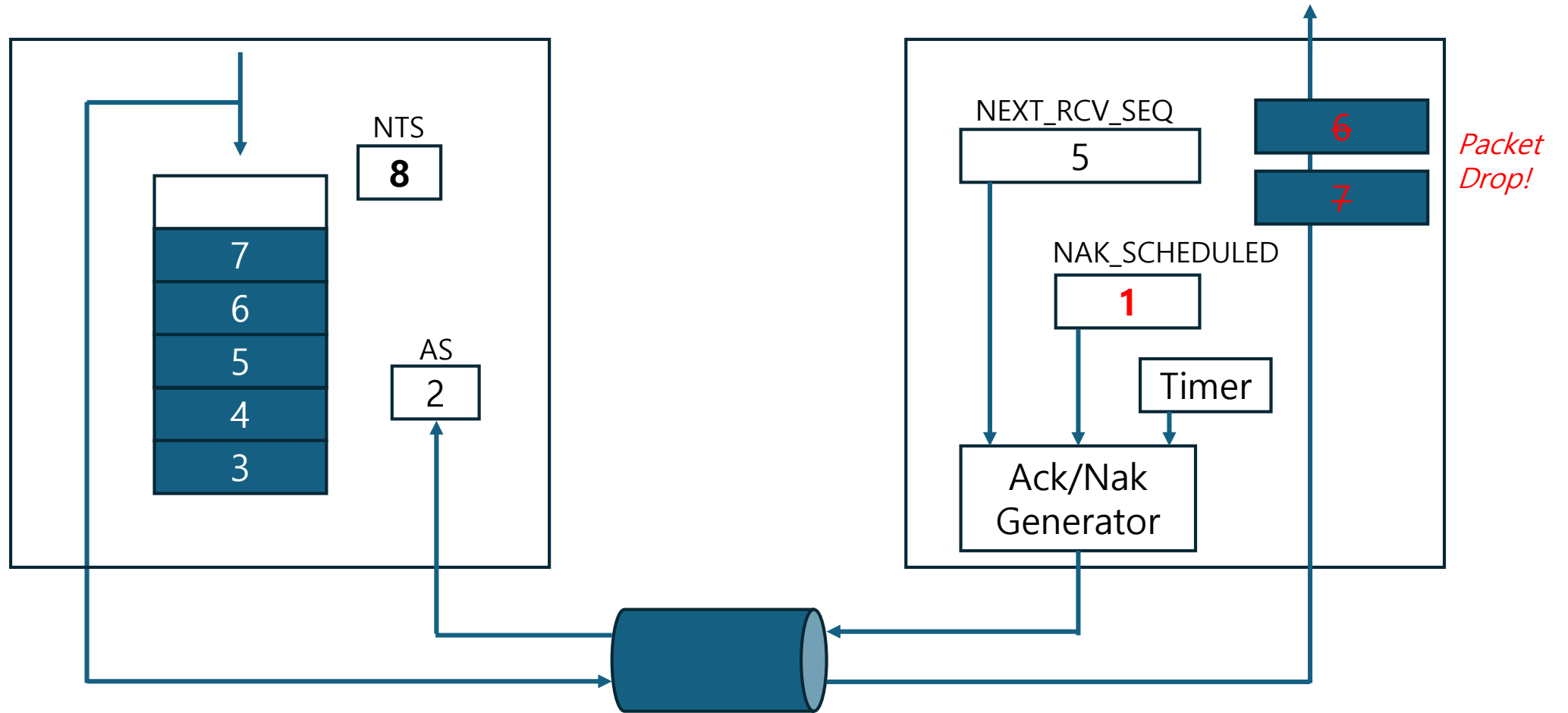


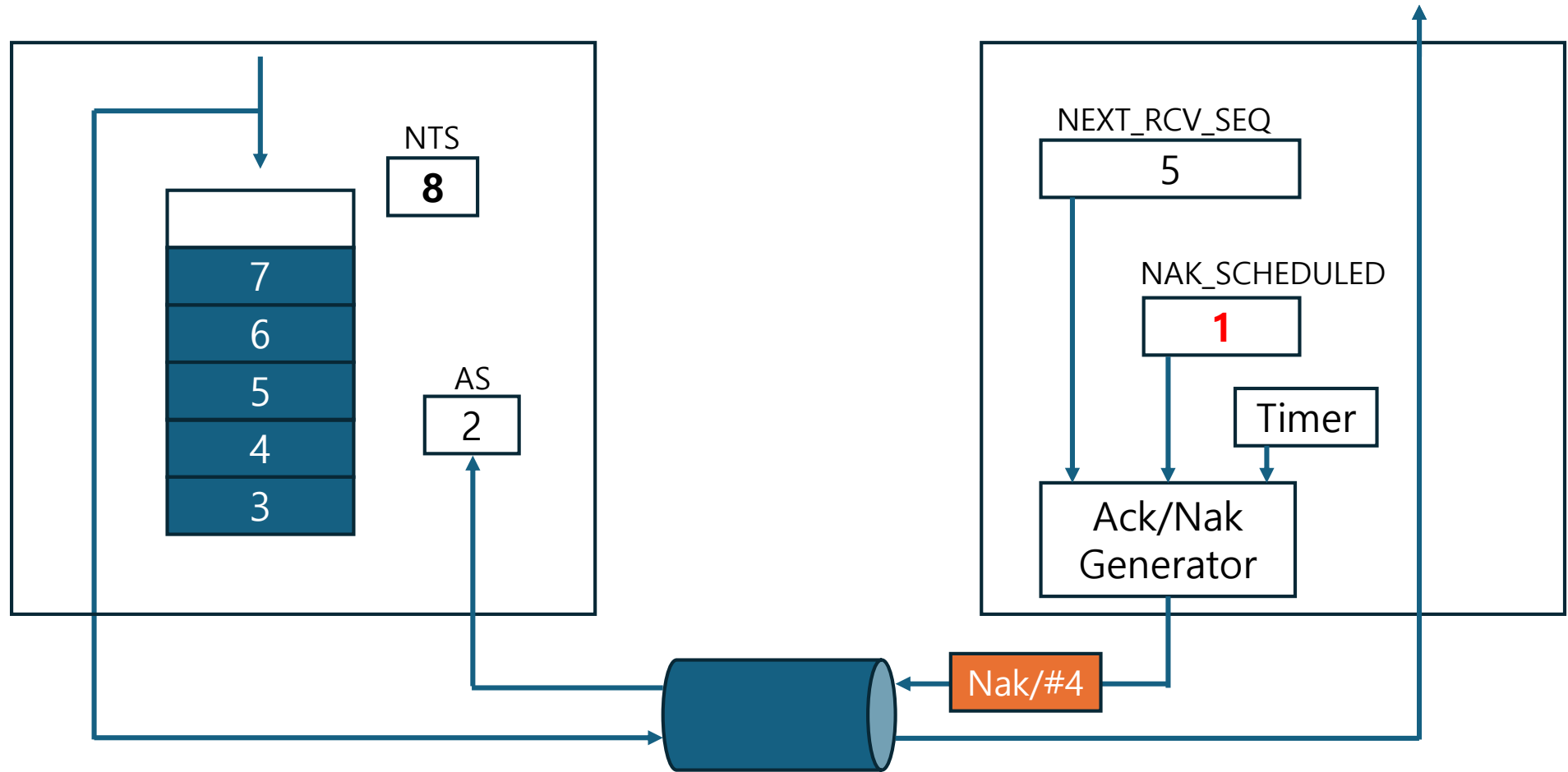


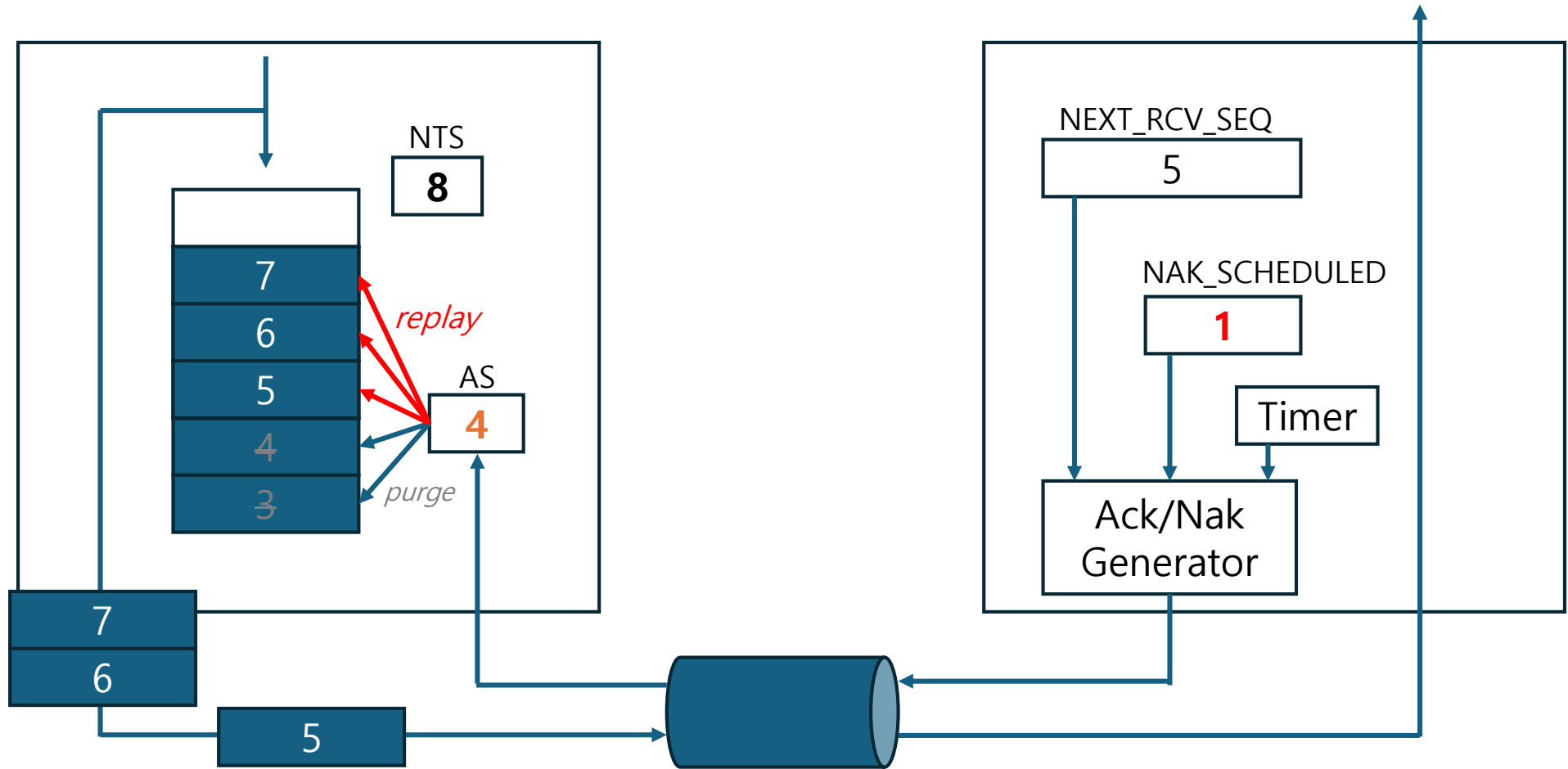














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